

RF POWER MOSFET N-CHANNEL ENHANCEMENT MODE

250V 750W 25MHz

The ARF1519 is an RF power transistor designed for very high power scientific, commercial, medical and industrial RF power generator and amplifier applications up to 25 MHz.

- **Specified 250 Volt, 13.56 MHz Characteristics:**
 - Output Power = 750 Watts.**
 - Gain = 17dB (Class C)**
 - Efficiency > 75%**
- **High Performance Power RF Package.**
- **Very High Breakdown for Improved Ruggedness.**
- **Low Thermal Resistance.**
- **Nitride Passivated Die for Improved Reliability.**

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	ARF1519	UNIT
V_{DSS}	Drain-Source Voltage	1000	Volts
I_D	Continuous Drain Current @ $T_C = 25^\circ\text{C}$	20	Amps
V_{GS}	Gate-Source Voltage	± 30	Volts
P_D	Total Device Dissipation @ $T_C = 25^\circ\text{C}$	1350	Watts
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 175	°C
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 300\mu\text{A}$)	1000			Volts
$V_{DS(ON)}$	On State Drain Voltage (Clock) ($I_{D(ON)} = 10A, V_{GS} = 10V$)		5	7	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = 1000V, V_{GS} = 0V$)			300	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 800V, V_{GS} = 0V, T_C = 125^\circ\text{C}$)			3000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			± 600	nA
g_{fs}	Forward Transconductance ($V_{DS} = 15V, I_D = 10A$)	3	14		mhos
$V_{isolation}$	RMS Voltage (60Hz Sinewave from terminals to mounting surface for 1 minute)	TBD			Volts
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 6mA$)	2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic (per package unless otherwise noted)	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.13	°C/W
$R_{\theta CS}$	Case to Sink (Use High Efficiency Thermal Joint Compound and Planar Heat Sink Surface.)		0.09		



CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 150V$ $f = 1\text{ MHz}$		4600	5600	pF
C_{oss}	Output Capacitance			310	350	
C_{rss}	Reverse Transfer Capacitance			90	120	

FUNCTIONAL CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
G_{PS}	Common Source Amplifier Power Gain	$f = 13.56\text{MHz}$	17	20		dB
η	Drain Efficiency	$V_{GS} = 0V$ $V_{DD} = 200V$	70	75		%
Ψ	Electrical Ruggedness VSWR 10:1	$P_{out} = 750W$	No Degradation in Output Power			

① Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

Per transistor section unless otherwise specified.

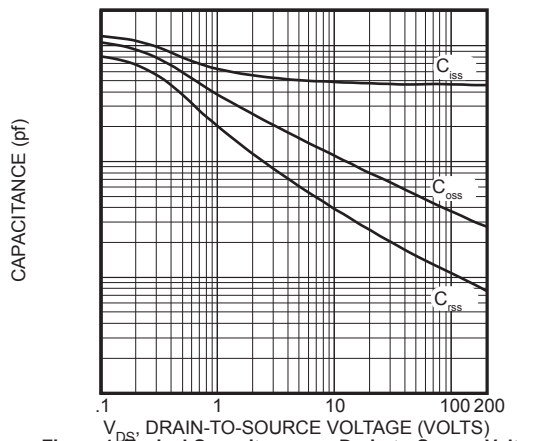


Figure 1, Typical Capacitance vs. Drain-to-Source Voltage

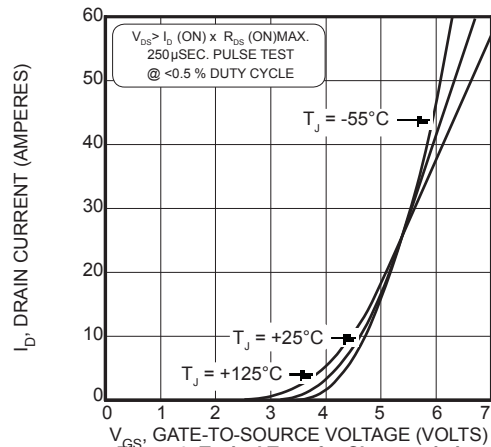


Figure 2, Typical Transfer Characteristics

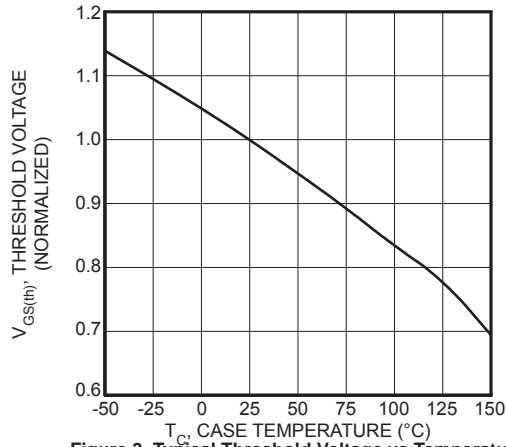


Figure 3, Typical Threshold Voltage vs Temperature

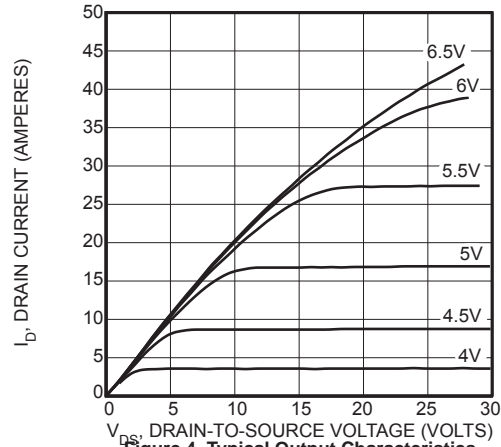


Figure 4, Typical Output Characteristics

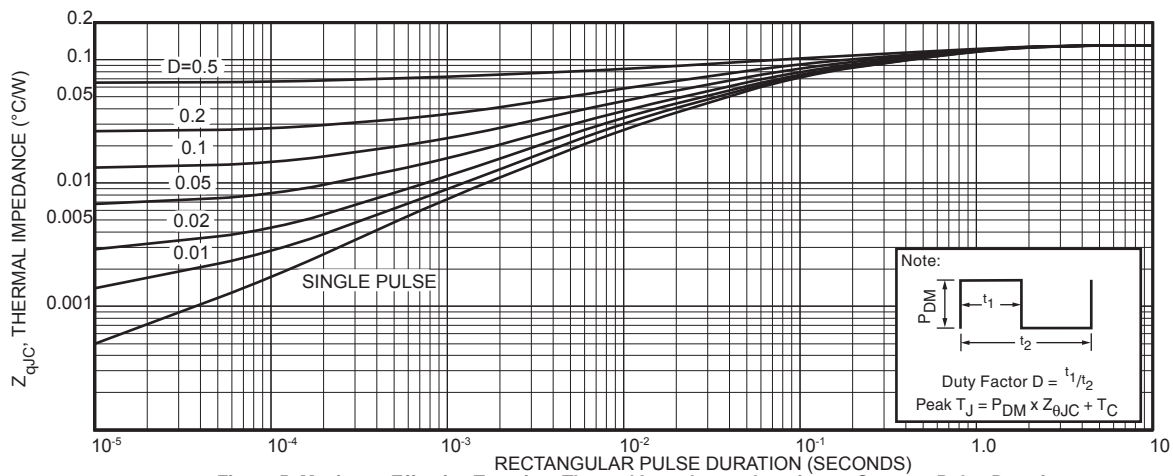


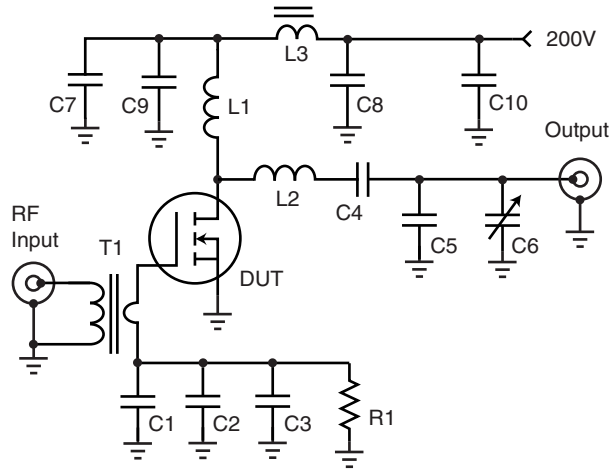
Figure 5, Maximum Effective Transient Thermal Impedance, Junction-to-Case vs. Pulse Duration

Table 1 - Typical Class AB Large Signal Impedance -- ARF1519

F (MHz)	Z _{in} (Ω)	Z _{OL} (Ω)
2.0	10.6 - j 12.2	31 - j 4.7
13.5	0.5 - j 2.7	15.6 - j 16

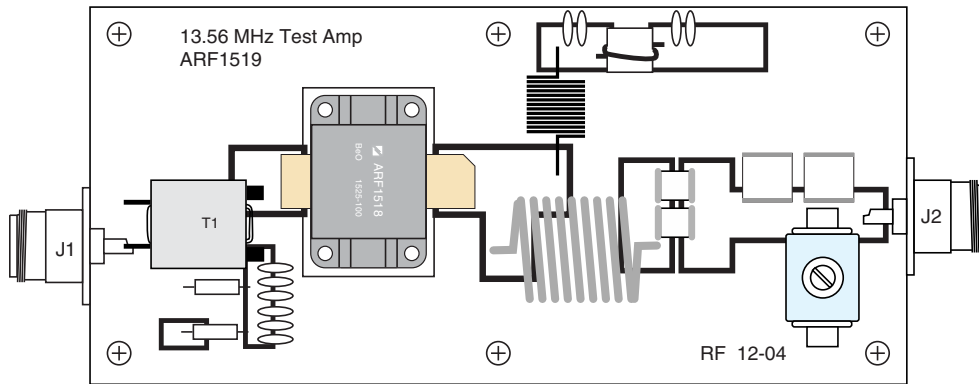
Z_{in} - Gate shunted with 25Ω I_{DQ} = 100mA
 Z_{OL} - Conjugate of optimum load for 750 Watts output at V_{dd} = 200V

ARF1519 -- 13.56 MHz Test Circuit



- C1-C3 1nF X7R 100V smt
- C4 2x 8.2 nF 1kV COG
- C5 270pF x2 ATC 100C
- C7-C10 8.2 nF 1kv COG
- C11 390 + 27 pF ATC 100C
- L1 2uH - 22t #24 enam. .312" dia.
- L2 368 nH - 5t #12 .625" dia .5" l
- L3 500nH 2t on 850u .5" bead
- R1 2.2k 0.5W
- T1 10:1t transformer

Parts placement - Not to Scale.

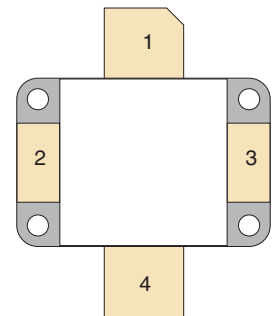
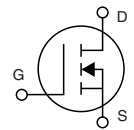
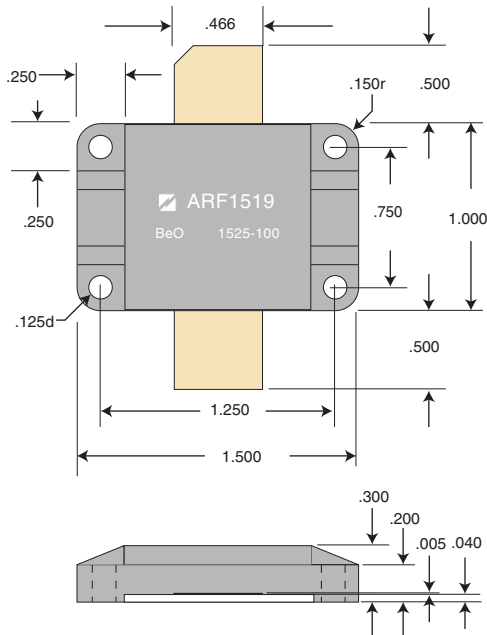


Thermal Considerations and Package Mounting:

The rated 1350W power dissipation is only available when the package mounting surface is at 25°C and the junction temperature is 200°C. The thermal resistance between junctions and case mounting surface is 0.12°C/W. When installed, an additional thermal impedance of 0.1°C/W between the package base and the mounting surface is typical. Insure that the mounting surface is smooth and flat. Thermal joint compound must be used to reduce the effects of small surface irregularities. The heatsink should incorporate a copper heat spreader to obtain best results. Use 4-40 or M3 screws torqued to 1.2 Nm.

HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and mounting surface is beryllium oxide-BeO. Beryllium oxide dust is toxic when inhaled. Care must be taken during handling and mounting to avoid damage to this area. These devices must never be thrown away with general industrial or domestic waste.



- 1 Drain
- 2 Source
- 3 Source
- 4 Gate