Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS V)

TPCA8106

Lithium Ion Battery Applications
Notebook PC Applications
Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON-resistance: $RDS(ON) = 2.9 \text{ m}\Omega \text{ (typ.)}$

 $(V_{GS} = -10V)$

- High forward transfer admittance: $|Y_{fs}| = 79S$ (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

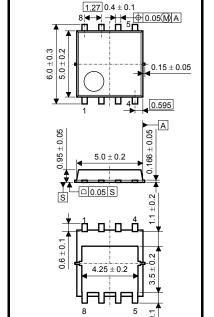
Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	-30	V
Gate-source voltage		V_{GSS}	±20	٧
Drain current	DC (Note 1)	I _D	-40	Α
Drain current	Pulsed (Note 1)	I_{DP}	-120	ζ
Drain power dissipati	on (Tc=25°C)	P_{D}	45	W
Drain power dissipati	on $(t = 10 s)$	PD	2.8	W
	(Note 2a)	ים י	2.0	,,,
Drain power dissipati	on (t = 10 s)	Pn	1.6	W
	(Note 2b)	. 0		
Single pulse avalanch	ne energy (Note 3)	E _{AS}	208	mJ
Avalanche current		I _{AR}	-40	Α
Repetitive avalanche	energy	E _{AR}	4.5	mJ
(To	c = 25°C) (Note 4)		7.5	1113
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 150	°C

Note: For Note 1 to 4, please refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba

absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



4: GATE[∞]

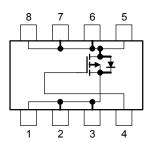
2-5Q1A

Weight: 0.069 g (typ.)

1,2,3: SOURCE 5,6,7,8: DRAIN

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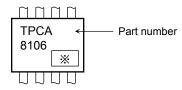
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

Marking (Note 5)

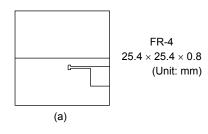


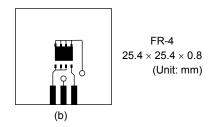
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

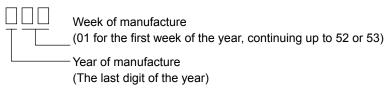




Note 3: $V_{DD} = -24~V,~T_{Ch} = 25^{\circ}C$ (initial), L = 100 $\mu H,~R_G = 25~\Omega,~I_{AR} = -40~A$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)



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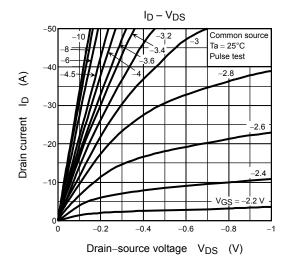
Electrical Characteristics (Ta = 25°C)

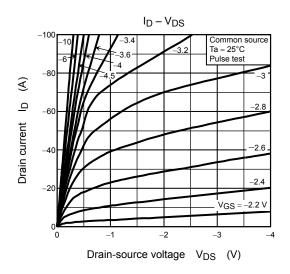
Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cut-OFF cu	ırrent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source bre	akdown voltago	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
Diain-source bre	ardown voltage	V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-13	_	_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain source ON	rocietanco	Pro (ou)	$V_{GS} = -4 \text{ V}, I_D = -20 \text{ A}$	_	5.5	7.8	- mΩ
Diain-source ON	-iesistance	NDS (ON)	$V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	_	2.9	3.7	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -20 \text{ A}$	39.5	79	_	S
Input capacitance	e	C _{iss}		_	4600	_	
Gate threshold voltage Drain-source ON-resistance Forward transfer admittance Input capacitance Reverse transfer capacitance Output capacitance Rise time Turn-ON time Fall time		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	970	_	pF
Output capacitan	ice	Coss		_	1500	_	
	Rise time	t _r	o v 7	_	10	-10	
Cuitabing time	Turn-ON time	t _{on}	V _{GS} V _{OUT} V _{OUT}	_	20	_	
Switching time	Fall time	$R_{DS (ON)} = R_{DS (ON)$	_	– ns			
	Turn-OFF time			_	750	_	
Total gate charge (gate-source plus		Qg	$V_{DD} \approx -24 \text{ V}, V_{GS} = -10 \text{ V},$	_	130	_	
Gate-source cha	rge 1	Q _{gs1}	$I_D = -40 \text{ A}$		12		nC
Gate-drain ("mille	er") charge	Q _{gd}		_	40	_	

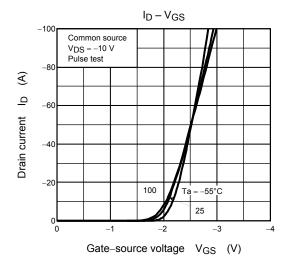
Source-Drain Ratings and Characteristics (Ta = 25°C)

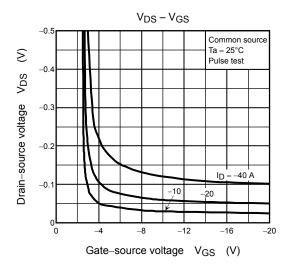
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	-120	Α
Forward voltage (diode)			V _{DSF}	I _{DR} = -40 A, V _{GS} = 0 V	_	_	1.2	V

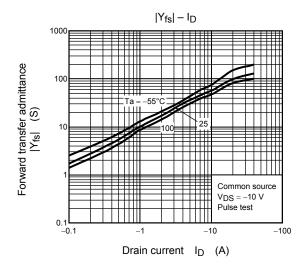
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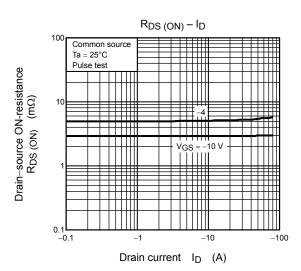


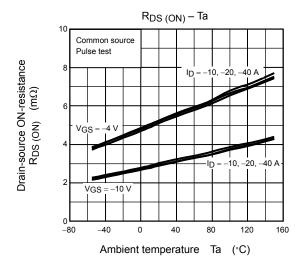


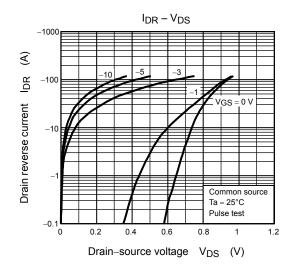


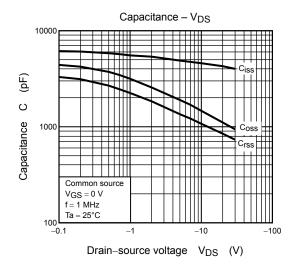


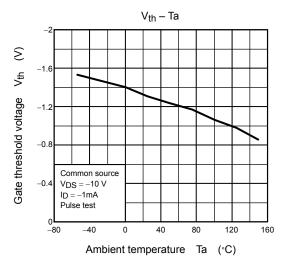


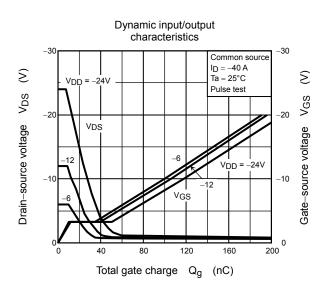




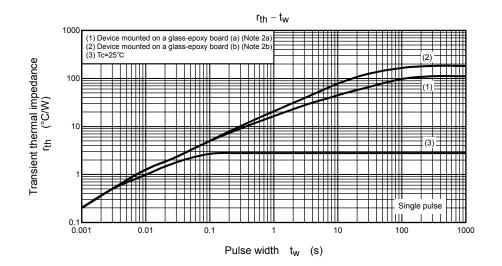


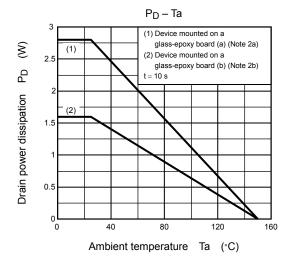


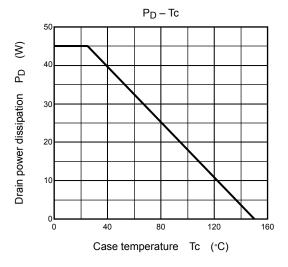


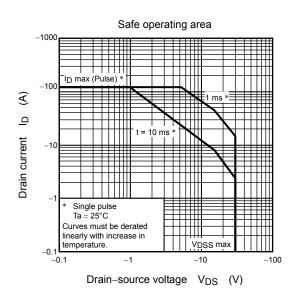


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