

# RKZ6.8Z4KT

## Silicon Planar Zener Diode for Surge Absorption

REJ03G1820-0100  
Rev.1.00  
Sep 15, 2009

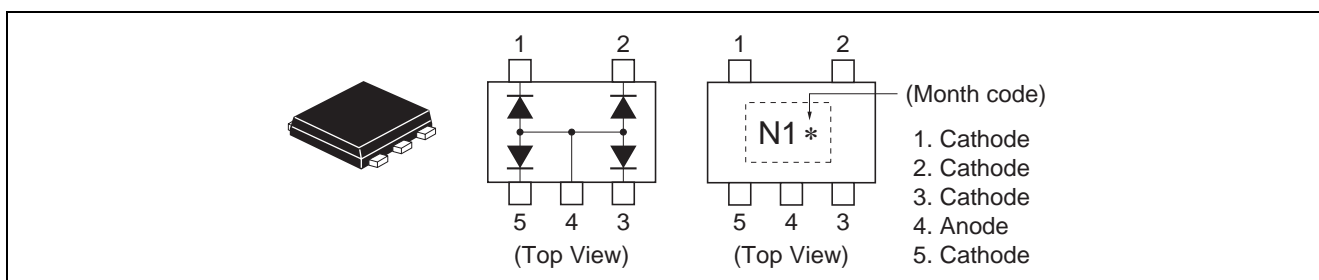
### Features

- RKZ6.8Z4KT can absorbing surge with four devices in one chip configuration.
- Low capacitance (C = 4.0 pF Typ) enable protecting signal line from ESD.
- VSON-5 Package is suitable for high density surface mounting.

### Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKZ6.8Z4KT H RKZ6.8Z4KT P	N1	VSON-5	PUSN0005KB-A	H (3,000 pcs / reel) P (3,000 pcs / reel)

### Pin Arrangement



### Month Code

Month of Manufacture	Assemble
	MALAYSIA
January	1
February	2
March	3
April	4
May	5
June	6

Month of Manufacture	Assemble
	MALAYSIA
July	7
August	8
September	9
October	W
November	X
December	Y

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four devices total, See Fig.2.

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	Vz	6.47	—	7.00	V	Iz = 5 mA, 40 ms pulse
Reverse current	IR1	—	—	2	nA	VR = 3.5 V
Capacitance	C	—	4.0	4.5	pF	VR = 0 V, f = 1 MHz
Dynamic resistance	rd	—	—	30	Ω	Iz = 5 mA
ESD-Capability *2, *3	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; IR > 2 μA at VR = 3.5 V.

3. Between cathode and anode.

Main Characteristics

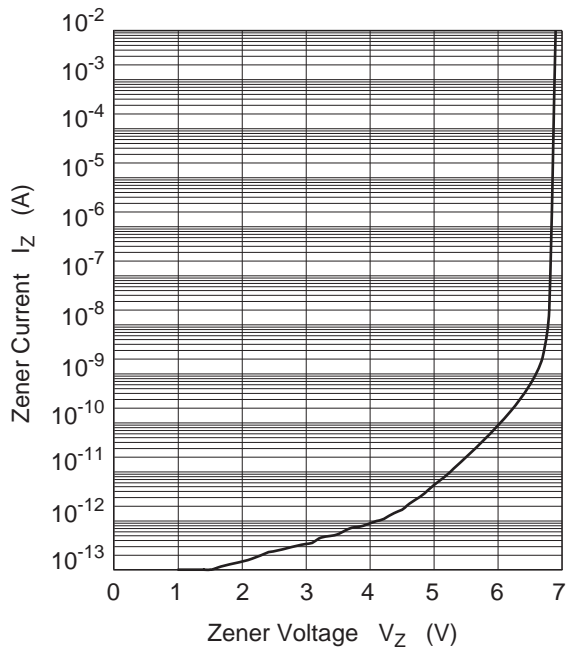


Fig.1 Zener current vs. Zener voltage

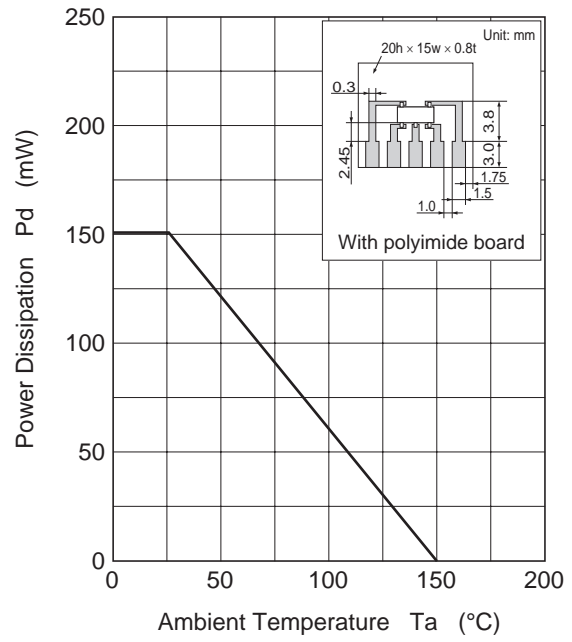


Fig.2 Power Dissipation vs. Ambient Temperature

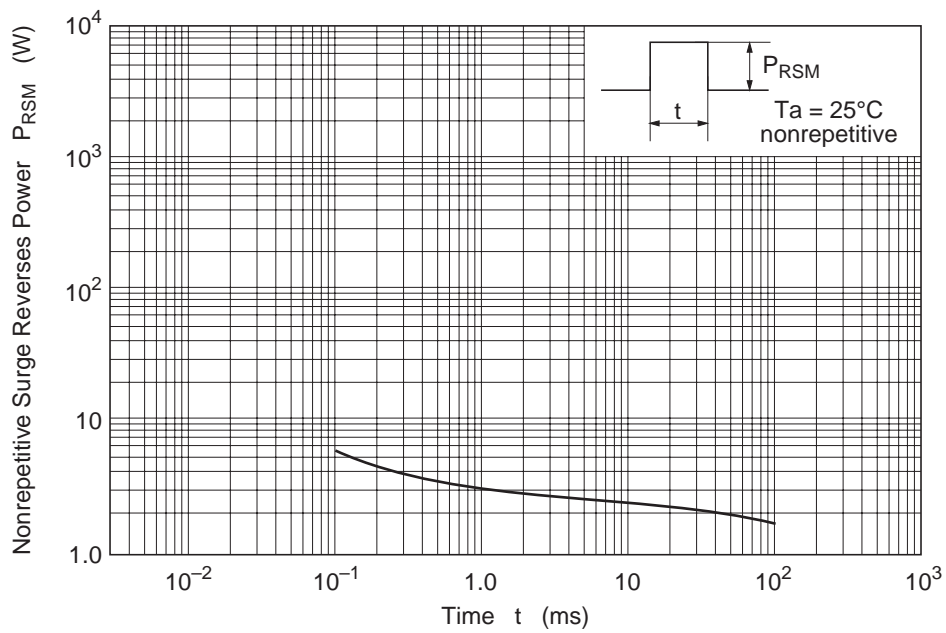
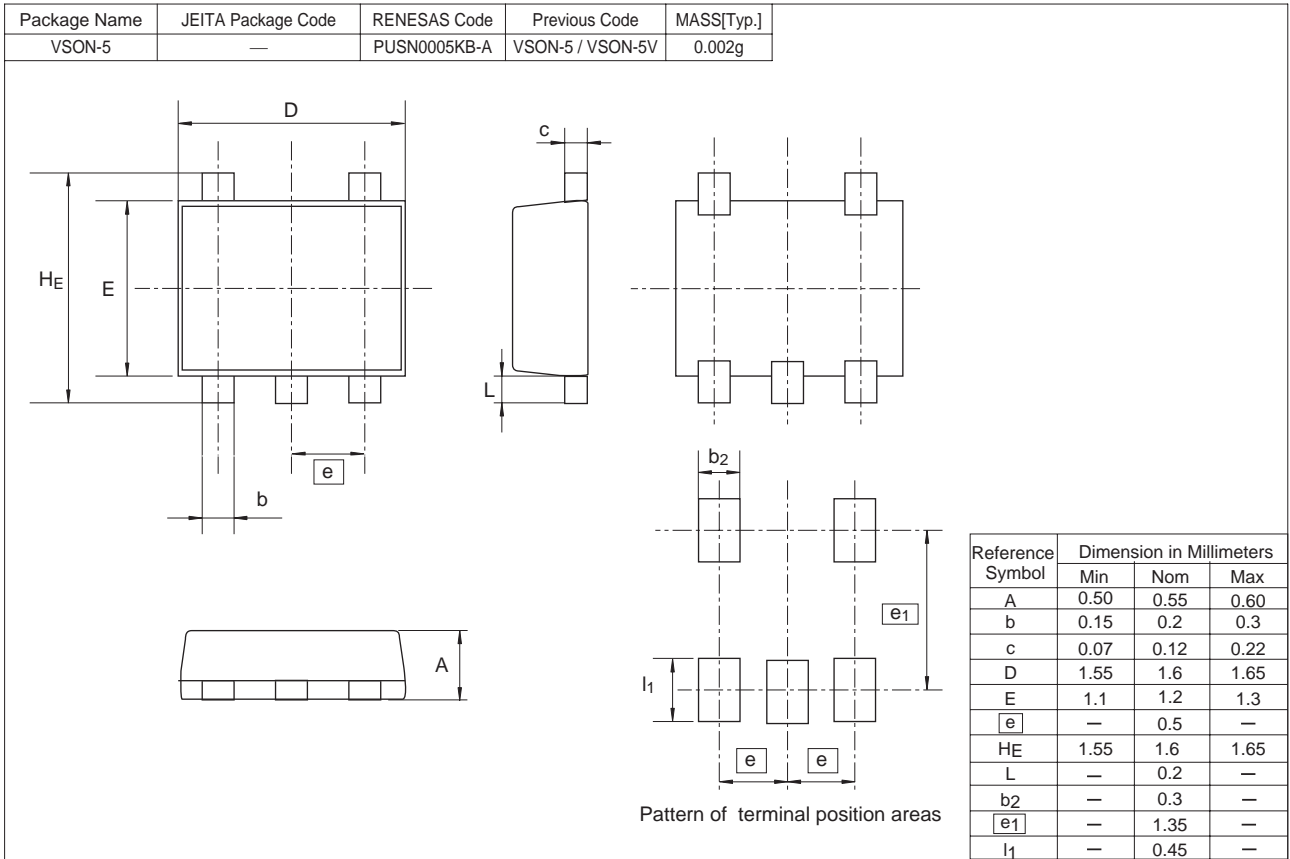


Fig.3 Surge Reverse Power Ratings

### Package Dimensions



Notes:

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