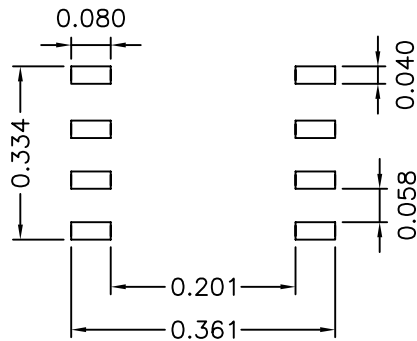
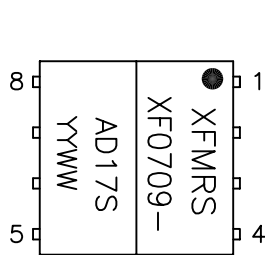
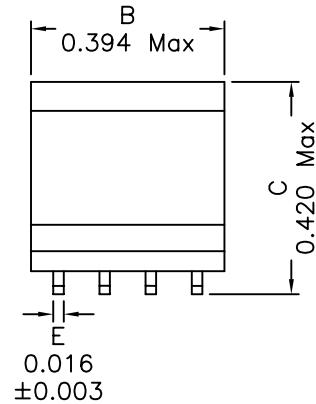
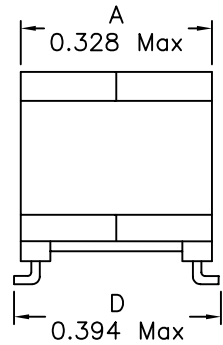
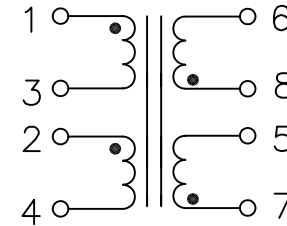


1. Mechanical Dimensions:



SUGGESTED PCB LAYOUT

2. Schematic:



3. Electrical Specifications: @25°C

OCL: Pins 1-4 1.444mH±10% @10KHz 0.1V, Tie Pins 2&3
 LL: Pins 1-4 5.0uH Max @10KHz 0.1V, Tie Pins 2&3, Short SEC
 CW/W: Pins 1-8 200pF Max @10KHz 0.1V, Short 2-3 & 6-7
 THD: 80db Min @30KHz, Vrms=4.05V/135 Ohms, Line:Chip
 THD: 75db Min @300KHz, Vrms=4.05V/135 Ohms, Line:Chip
 Insertion Loss: Pins 1-4 0.5db Max @100KHz Line to Chip, Line Open
 Long Bal: 40dB Min 25KHz to 2.2MHz, Ground Pin 8
 DC Res.: Pins 1-3 4.0 Ohms Max
 DC Res.: Pins 2-4 4.0 Ohms Max
 DC Res.: Pins 8-6 1.20 Ohms Max
 DC Res.: Pins 7-5 1.20 Ohms Max
 Turns Ratio: (1-3):(2-4):(8-6):(7-5) = 1.33:1.33:1:1±2%
 Hipot: 1500Vac, PRI to SEC
 Hipot: 500Vac, Pri to Pri, Sec to Sec, All to Core

Notes:

- Solderability: Leads shall meet MIL-STD-202, Method 208D for solderability.
- Flammability: UL94V-0
- ASTM oxygen index: > 28%
- Insulation System: Class F 155°C. UL file E151556
- Operating Temperature Range: -40°C to +85°C
- Complies with IEC/EN60950 with Basic Insulation for working voltage of 250V.

DOC REV: A/1

XFMRS Inc	Title: ADSL TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED TOLERANCES:		P/N: XF0709-AD17S
.xx ±0.010	DWN.	Kang Chen	REV. A
Dimensions in Inch	CHK.	YK Liao	Mar-01-04
SHEET 1 OF 1	APP.	Joe Huff	Mar-01-04