## Features

- Low Insertion Loss : 0.4 dB @ 2.5 GHz

$$
0.5 \mathrm{~dB} @ 5.8 \mathrm{GHz}
$$

- Isolation: 24 dB @ 2.5 GHz

$$
27 \text { dB @ } 5.8 \text { GHz }
$$

- Low DC Power Consumption
- Miniature QFN12L (3x3 mm) Plastic Package
- PHEMT process


## Description

The HWS382 is a GaAs PHEMT MMIC SPDT switch operating at DC-6 GHz in a low cost miniature QFN12L ( $3 \times 3 \mathrm{~mm}$ ) plastic package. The HWS382 features low insertion loss and high isolation with very low DC power consumption. This switch can be used in IEEE $802.11 \mathrm{a} / \mathrm{b} / \mathrm{g}$ WLAN PC card and access point applications as transmit/receive switch, antenna diversity switch, or band-selection switch.

## QFN12L (3 x 3 mm)



Electrical Specifications at $25^{\circ} \mathrm{C}$ with $0,+3 \mathrm{~V}$ Control Voltages

| Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $0.10-6.00 \mathrm{GHz}$ |  | 0.5 |  | dB |
|  | $2.40-2.50 \mathrm{GHz}$ |  | 0.4 | 0.6 | dB |
|  | $5.15-5.85 \mathrm{GHz}$ |  | 0.5 | 0.8 | dB |
|  | $0.10-6.00 \mathrm{GHz}$ |  | 24 |  | dB |
| Return Loss | $2.40-2.50 \mathrm{GHz}$ | 22 | 24 |  | dB |
|  | $5.15-5.85 \mathrm{GHz}$ | 24 | 27 |  | dB |
| Input Power for One dB Compression | $0.10-6.00 \mathrm{GHz}$ |  | 15 |  | dB |
|  | $2.40-2.50 \mathrm{GHz}$ | 12 | 20 |  | dB |
|  | $5.15-5.85 \mathrm{GHz}$ | 10 | 18 |  | dB |
| Control Current | $2.00-6.00 \mathrm{GHz}$ |  | 30 |  | dBm |

Note: All measurements made in a 50 ohm system with $0 /+3.0 \mathrm{~V}$ control voltages, unless otherwise specified.

## Typical Performance Data with 8pF Capacitors @ $+25^{\circ} \mathrm{C}$

Insertion Loss vs Frequency


Isolation vs Frequency


Return Loss vs Frequency


## Absolute Maximum Ratings

| Parameter | Absolute Maximum |
| :--- | :---: |
| RF Input Power | $+32 \mathrm{dBm} @+3 \mathrm{~V}$ |
| Control Voltage | +6 V |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

## Pin Out (Top View)



Note:

1. DC blocking capacitors $\mathrm{C}_{\mathrm{B}}=8 \mathrm{pF}$ are required on all RF ports.
2. Exposed pad in the bottom must be connected to ground by via holes.

## Logic Table for Switch On-Path

| VC1 | VC2 | RFC-RF1 | RFC-RF2 |
| :---: | :---: | :---: | :---: |
| 0 | 1 | Insertion Loss | Isolation |
| 1 | 0 | Isolation | Insertion Loss |

$$
\begin{aligned}
& \prime 1 '=+3 \mathrm{~V} \text { to }+5 \mathrm{~V} \\
& ' 0 \prime=0 \mathrm{~V} \text { to }+0.2 \mathrm{~V}
\end{aligned}
$$

