TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA78DS05F, TA78DS06F, TA78DS08F, TA78DS09F, TA78DS10F, TA78DS12F, TA78DS15F, TA78DS05AF

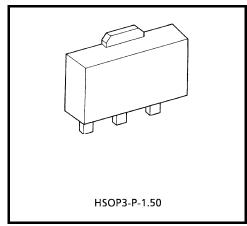
5 V, 6 V, 8 V, 9 V, 10 V, 12 V, 15 V

Low Dropout Voltage Regulator

The TA78DS××F series consists of fixed-positive-output voltage regulator ICs capable of sourcing current up to 30 mA. Due to the features of low dropout voltage and low standby current, these devices are useful for battery-powered equipment. This series includes built-in current limiting, thermal shutdown, over voltage protection, input fault protection and excessive transient protection circuits.

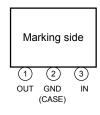
Features

- Low standby current of 600 µA typical.
- Maximum output current of 30 mA.
- Low dropout voltage of less than 0.3 V.
- Multi-protection: Reverse connection of power supply, 60V load dump, thermal shut down and current limiting.
- Packaged in PW-Mini (SOT-89).

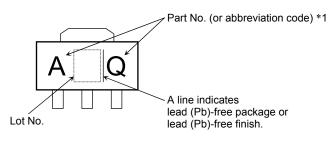


Weight: 0.05 g (Typ.)

Pin Assignment



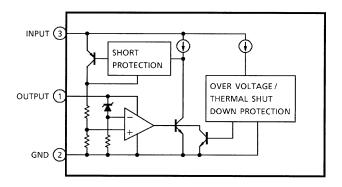
Marking



	Part No. (or abbreviation code)	Part No.				
	AQ	TA78DS05AF				
	AP	TA78DS05F				
	BP	TA78DS06F				
*1	СР	TA78DS08F				
	DP	TA78DS09F				
	EP	TA78DS10F				
	FP	TA78DS12F				
	GP	TA78DS15F				



Block Diagram



Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit
Operating input voltage		V _{IN}	29	V
Input voltage of surge		V _{IN}	60	V
Power dissipation	ver dissipation (Ta = 25°C)		500	mW
Operating temperature		T _{opr}	-40~85	°C
Storage temperature		T _{stg}	-55~150	°C
Junction temperature		Tj	150	°C
Thermal resistance		R _{th (j-a)}	250	°C/W
Soldering temperature t	ime	T _{sol}	260 (10 s)	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

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Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



TA78DS05F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			5.35 V ≤ V _{IN} ≤ 26 V	4.75	5.0	5.25	
Output voltage	Vout	_	5.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	4.5	5.0	5.5	V
Line ne maletien	Pogulino		9.0 V ≤ V _{IN} ≤ 16 V	_	1	10	mV
Line regulation	Reg·line	_	6.0 V ≤ V _{IN} ≤ 26 V	_	4	30	1110
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	1	50	mV
Quiescent current	ΙΒ	_	I _{OUT} = 0 mA	_	0.6	1	mA
Quiescent current			6 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	_	0.7	1	IIIA
Dropout voltage	Vo		I _{OUT} = 5 mA	1	0.1	0.2	V
Dropout voltage	V_D		I _{OUT} = 10 mA	_	0.2	0.3	
Max operating voltage	V _{IN}	_	_	29	33	_	V

TA78DS05AF Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			5.35 V ≤ V _{IN} ≤ 26 V	4.8	5.0	5.2	
Output voltage	Vouт	_	5.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	4.75	5.0	5.25	V
Line regulation	Poglino		9.0 V ≤ V _{IN} ≤ 16 V	_	1	10	mV
Line regulation	Reg·line		6.0 V ≤ V _{IN} ≤ 26 V	_	4	30	IIIV
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	1	50	mV
Quiescent current		_	I _{OUT} = 0 mA	١	0.6	1	mA
Quiescent current	I _B		6 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	١	0.7	1	ш
Dropout voltage	Vo		I _{OUT} = 5 mA	١	0.1	0.2	V
Dropout voltage	V_{D}		I _{OUT} = 10 mA	_	0.2	0.3	
Max operating voltage	V _{IN}	_	_	29	33	_	٧



TA78DS06F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_i = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			6.35 V ≤ V _{IN} ≤ 26 V	5.7	6.0	6.3	
Output voltage	Vout	_	6.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	5.4	6.0	6.6	V
Line regulation	Pogulino		10 V ≤ V _{IN} ≤ 17 V	_	1	20	mV
	Reg·line		7.0 V ≤ V _{IN} ≤ 26 V	_	4	40	1110
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	1	60	mV
Quiescent current	I _B	_	I _{OUT} = 0 mA	_	0.6	1.1	mA
Quescent current			7 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	_	0.7	1.1	IIIA
Dropout voltage	Vo	-	I _{OUT} = 5 mA	_	0.1	0.2	V
Dropout voltage	V_{D}		I _{OUT} = 10 mA	_	0.2	0.3	
Max operating voltage	V _{IN}	_	_	29	33	_	V

TA78DS08F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			8.35 V ≤ V _{IN} ≤ 26 V	7.6	8.0	8.4	
Output voltage	Vouт	_	8.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	7.2	8.0	8.8	V
Line regulation	Poglino		12 V ≤ V _{IN} ≤ 19 V	_	2	30	mV
Line regulation	Reg·line		9.0 V ≤ V _{IN} ≤ 26 V	_	5	60	IIIV
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	-	4	80	mV
Quiescent current	1		I _{OUT} = 0 mA	١	0.7	1.2	mA
Quiescent current	I _B		9 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	١	0.8	1.2	ш
Dropout voltage	Vo		I _{OUT} = 5 mA	١	0.1	0.2	- V
Dropout voltage	V_D		I _{OUT} = 10 mA	_	0.2	0.3	
Max operating voltage	V _{IN}	_	_	29	33	-	>



TA78DS09F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			9.35 V ≤ V _{IN} ≤ 26 V	8.55	9.0	9.45	
Output voltage	V _{OUT}	_	9.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	8.1	9.0	9.9	V
Line regulation	Pogulino		13 V ≤ V _{IN} ≤ 20 V	_	2	35	mV
Line regulation	Reg·line		10 V ≤ V _{IN} ≤ 26 V	_	5	70	1117
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	4	90	mV
	IB	_	I _{OUT} = 0 mA	_	0.7	1.3	
Quiescent current			$10 \text{ V} \leq \text{V}_{\text{IN}} \leq 26 \text{ V},$ $\text{I}_{\text{OUT}} = 5 \text{ mA}$	-	0.8	1.3	mA
Dropout voltage	V _D		I _{OUT} = 5 mA	_	0.1	0.2	V
Dropout voltage	VD		I _{OUT} = 10 mA	_	0.2	0.3	- v
Max operating voltage	V _{IN}	_	_	29	33	_	V

TA78DS10F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			10.35 V ≤ V _{IN} ≤ 26 V	9.5	10.0	10.5	
Output voltage	V _{OUT}	_	10.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	9.0	10.0	11.0	V
Line regulation	Pogulino		14 V ≤ V _{IN} ≤ 21 V	_	3	40	mV
Line regulation	Reg·line	_	11 V ≤ V _{IN} ≤ 26 V	_	7	80	IIIV
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	6	100	mV
	ΙΒ	_	I _{OUT} = 0 mA	_	0.7	1.4	
Quiescent current			11 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	_	0.8	1.4	mA
Dropout voltage	\/-		I _{OUT} = 5 mA	_	0.1	0.2	V
Dropout voltage	V_{D}		I _{OUT} = 10 mA	-	0.2	0.3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Max operating voltage	V _{IN}	_	_	29	33	_	V

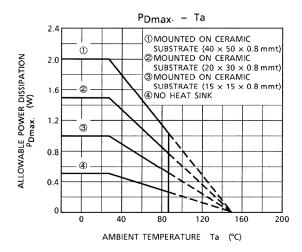


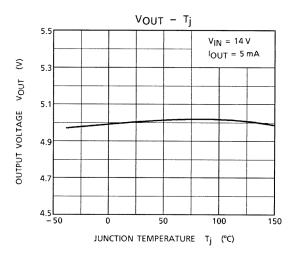
TA78DS12F Electrical Characteristics (Unless otherwise specified, V_{IN} = 18 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

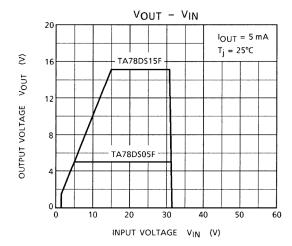
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			12.35 V ≤ V _{IN} ≤ 26 V	11.4	12.0	12.6	
Output voltage	V _{OUT}	_	12.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	10.8	12.0	13.2	٧
Line regulation	Pogulino		16 V ≤ V _{IN} ≤ 23 V	_	4	50	mV
Line regulation	Reg·line		13 V ≤ V _{IN} ≤ 26 V	_	8	100	1110
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	2	120	mV
	ΙΒ	_	I _{OUT} = 0 mA	_	0.8	1.5	
Quiescent current			$13 \text{ V} \le \text{V}_{\text{IN}} \le 26 \text{ V},$ $\text{I}_{\text{OUT}} = 5 \text{ mA}$	-	1.0	1.5	mA
Dropout voltage	Vo		I _{OUT} = 5 mA	_	0.1	0.2	V
Dropout voltage	V_D		I _{OUT} = 10 mA	_	0.2	0.3	
Max operating voltage	V _{IN}	_	_	29	33	_	V

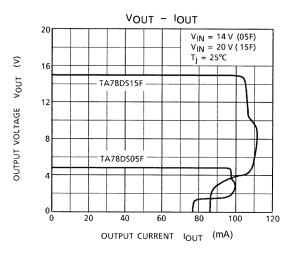
TA78DS15F Electrical Characteristics (Unless otherwise specified, V_{IN} = 20 V, I_{OUT} = 5 mA, C_{IN} = 0.1 μ F, C_{OUT} = 3.3 μ F, T_j = 25°C)

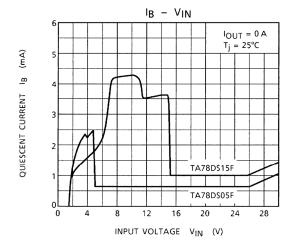
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
			15.35 V ≤ V _{IN} ≤ 26 V	14.25	15.0	15.75	
Output voltage	V _{OUT}	_	15.35 V ≤ V _{IN} ≤ 26 V, -40°C ≤ Ta ≤ 85°C	13.5	15.0	16.5	V
Line regulation	Poglino		19 V ≤ V _{IN} ≤ 26 V	_	5	60	mV
Line regulation	Reg·line	_	16 V ≤ V _{IN} ≤ 26 V	_	8	130	IIIV
Load regulation	Reg·load	_	5.0 mA ≤ I _{OUT} ≤ 30 mA	_	1	150	mV
	IB	_	I _{OUT} = 0 mA	_	1.0	1.6	
Quiescent current			16 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 5 mA	_	1.2	1.6	mA
Dropout voltage	Vo		I _{OUT} = 5 mA	_	0.1	0.2	V
Dropout voltage	V_D	_	I _{OUT} = 10 mA	_	0.2	0.3	v
Max operating voltage	V _{IN}	_	_	29	33	_	V

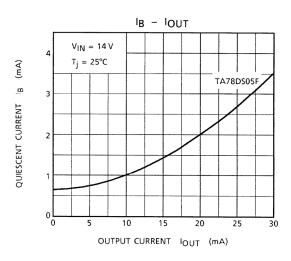


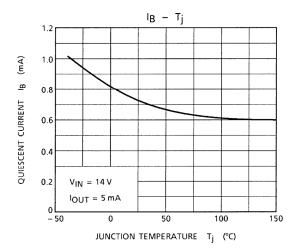


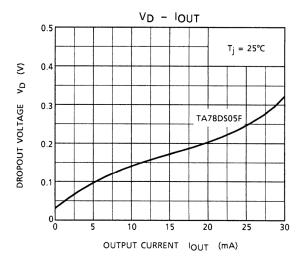


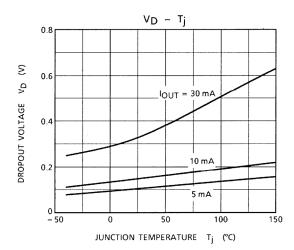


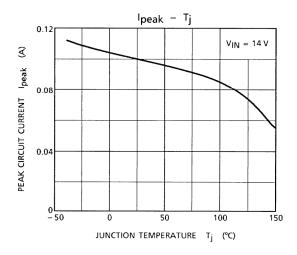


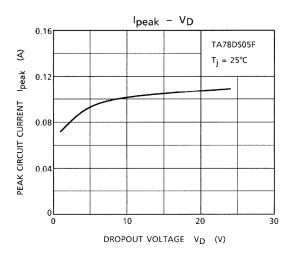






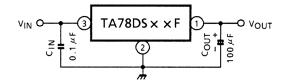








Application Circuit



The capacitors C_{IN}/C_{OUT} must be guaranteed to operate within the temperature range in which the regulator operates correctly.

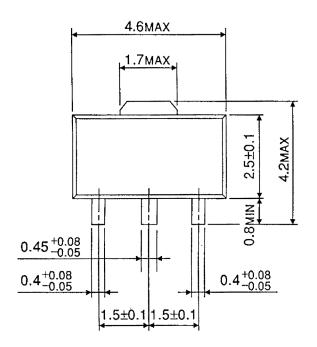
The equivalent series resistance (ESR) of C_{OUT} must be less than 1 Ω inside the operating temperature range.

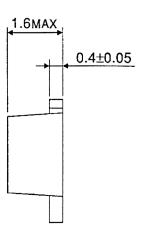
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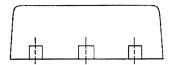
Unit: mm

Package Dimensions

HSOP3-P-1.50







Weight: 0.05 g (Typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN

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