TOSHIBA SCHOTTKY BARRIER RECTIFIER STACK SCHOTTKY BARRIER TYPE

20FWJ2CZ47M

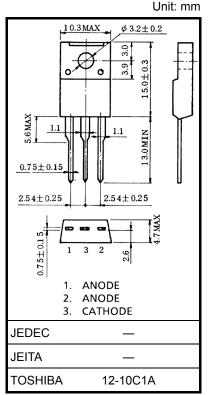
SWITCHING MODE POWER SUPPLY APPLICATION CONVERTER & CHOPPER APPLICATION

• Peak Forward Voltage: VFM ≤ 0.47 V

- Repetitive Peak Reverse Voltage: VRRM = 30V
- Average Output Rectified Current: IO = 20A
- Low Switching Losses and Output Noise.

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Reverse Voltage	V _{RRM}	30	V	
Average Output Rectified Current	Ι _Ο	20	А	
Peak One Cycle Surge Forward	IFSM	200 (50H _Z)	A	
Current (Sine Wave)		220 (60H _Z)		
Junction Temperature	Тј	-40~125	°C	
Storage Temperature Range	T _{stg}	-40~150	°C	
Screw Torque	—	0.6	N∙m	

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

Weight: 2.0 g (typ.)

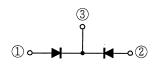
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	TYP.	MAX	UNIT
Peak Forward Voltage (Note 1)	V _{FM}	I _{FM} = 10A	_	0.47	V
Repetitive Peak Reverse Current (Note 1)		V _{RRM} = Rated	_	10	mA	
Junction Capacitance ((Note 1)	Cj	V _R = 10V, f = 1.0MHz	680	-	pF
Thermal Resistance		R _{th (j−c)}	Total DC, Junction to Case	_	2.7	°C / W

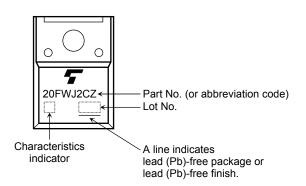
Note 1: A value applied to one cell.

POLARITY



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MARKING



Abbreviation Code	Part No.		
20FWJ2CZ	20FWJ2CZ47M		

Handling Precaution

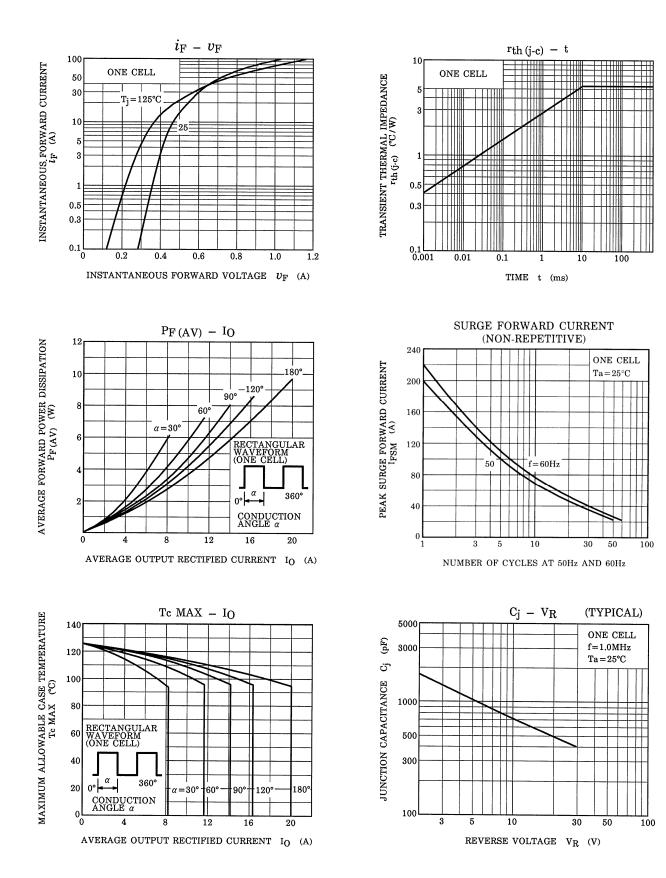
Schottky barrier diodes have reverse current characteristics compared to other diodes. There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. Please take forward and reverse loss into consideration during design.

The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

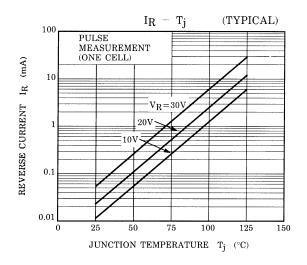
- VRRM: Use this rating with reference to the above. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.
- $I_{O}: \qquad \mbox{We recommend that the worst case current be no greater than 80\% of the absolute maximum rating of I_O and T_j be below 100°C. When using this device, take the margin into consideration by using an allowable Tamax-I_O curve.}$
- IFSM: This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.
- $T_{j} : \qquad \text{Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at a T_{j} of below 100^{\circ}\text{C}.$

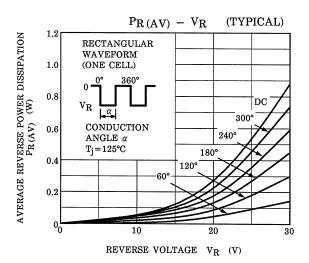
Please refer to the Rectifiers databook for further information.

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RESTRICTIONS ON PRODUCT USE

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- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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