

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TA7522SG, TA7522FG

## Dual Voltage Comparator

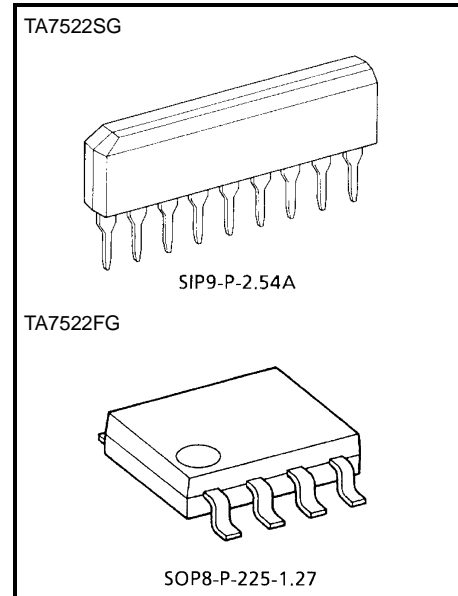
The TA7522SG is an easy-to-use IC incorporating two voltage comparator circuits. Since one channel has an inverted-output buffer, a CR oscillator can be easily built up.

The TA7522FG is in 8-pin dual inline package. The IC incorporating two-circuit package. It does not contain an inverted buffer output.

In addition, the TA7522SG/FG have a wide operating temperature range that allows it to be used in a wide variety of applications.

### Features

- Two-circuit package
- High gain: 95dB (typ.)
- Single 3 V power supply for operation
- Inverted-output also available: TA7522SG only
- 0 V input causes action in the IC with a single power supply.
- Wide common-mode input range
- No latch-up
- Operating temperature range: -40 to 85°C
- Open-collector output
- SIP-9 pin (TA7522SG)
- SOP-8 pin (TA7522FG)
- Lead(Pb)-Free product



#### Weight

SIP9-P-2.54A: 0.92 g (typ.)

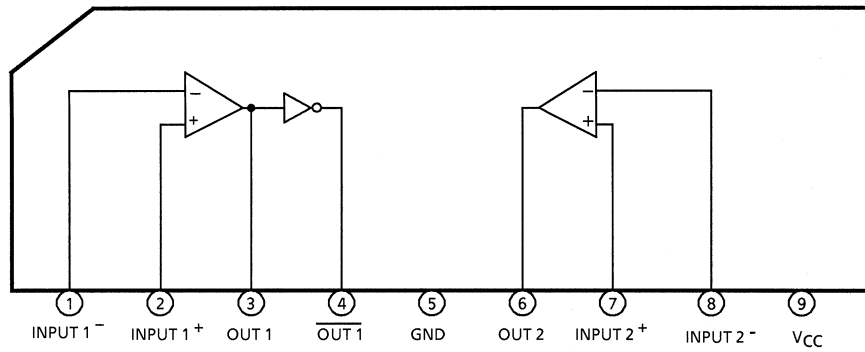
SOP8-P-225-1.27: 0.08 g (typ.)

About solderability, following conditions were confirmed

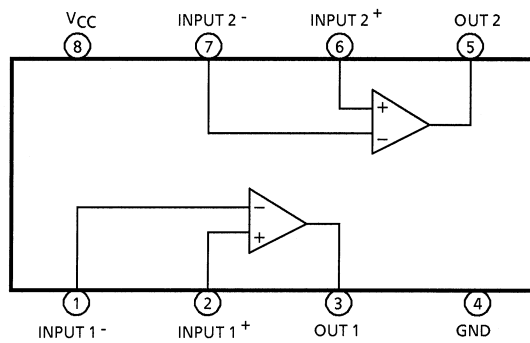
- Solderability
  - (1) Use of Sn-37Pb solder Bath
    - solder bath temperature = 230°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux
  - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
    - solder bath temperature = 245°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux

**Block Diagram and Pin Layout**

**TA7522SG**



**TA7522FG**



Note 1: The TA7522SG and TA7522FG are the same chip housed in different packages.

Note 2: Some functional blocks, circuits, or constants are omitted or simplified in the block diagram to clarify the descriptions of the relevant features.

**Pin Description**

Pin No.		Symbol	Description
TA7522SG	TA7522FG		
1	1	INPUT1 <sup>-</sup>	Inverted-input pin
2	2	INPUT1 <sup>+</sup>	Non-inverted-input pin
3	3	OUT1	Output pin corresponding to INPUT1
4	—	$\overline{\text{OUT1}}$	Output pin for inversion of OUT1
5	4	GND	Grounded
6	5	OUT2	Output pin corresponding to INPUT2
7	6	INPUT2 <sup>+</sup>	Non-inverted-input pin
8	7	INPUT2 <sup>-</sup>	Inverted-input pin
9	8	V <sub>CC</sub>	Power supply pin

**Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.3 to 18	V
Supply voltage surge	V <sub>CC SURGE</sub>	+30 (within 1 sec)	V
Power dissipation	P <sub>D</sub>	500/440	mW
Differential input voltage	DV <sub>IN</sub>	±18	V
Input voltage	V <sub>IN</sub>	-0.3 to 18	V
Output current	I <sub>SINK</sub>	30	mA
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Note 1: The absolute maximum ratings of a semiconductor device are a set of specified parameter values which must not be exceeded during operation, even for an instant.

If any of these levels is exceeded during operation, the device's electrical characteristics may be irreparably altered and the reliability and lifetime of the device can no longer be guaranteed, possibly causing damage to any other equipment with which it is used. Applications using the device should be designed such that the absolute maximum ratings will never be exceeded in any operating conditions.

Ensuring that the parameter values remain within these specified ranges during device operation will help to ensure that the integrity of the device is not compromised.

Note 2: P<sub>D</sub>: TA7522SG/TA7522FG

## Electrical Characteristics (unless otherwise specified, Ta = -40 to 85°C)

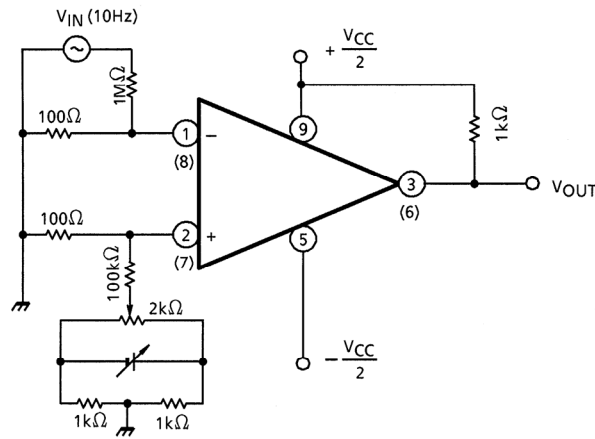
Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Voltage gain	G <sub>V</sub>	1	V <sub>CC</sub> = 6 V, R <sub>L</sub> = 1 kΩ f = 10 Hz	60	95	—	dB
Input offset voltage	V <sub>IO</sub>	2	V <sub>CC</sub> = 6 V, R <sub>L</sub> = 1 kΩ CMV <sub>IN</sub> = 3 V	—	2	10	mV
Input bias current	I <sub>I</sub>	3	V <sub>CC</sub> = 6 V, CMV <sub>IN</sub> = 3 V	—	-0.2	-2	μA
Input offset current	I <sub>IO</sub>	3	V <sub>CC</sub> = 6 V, CMV <sub>IN</sub> = 3 V	—	0.02	0.3	μA
Common-mode input voltage	CMV <sub>IL</sub>	4	V <sub>CC</sub> = 6.5 V, R <sub>L</sub> = 1 kΩ V <sub>IO</sub> = 20 mV	—	-0.5	0	V
	CMV <sub>IH</sub>		V <sub>CC</sub> = 6.5 V, R <sub>L</sub> = 1 kΩ V <sub>IO</sub> = 20 mV	5.0	5.3	—	V
Zero output voltage	V <sub>OL</sub>	5	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.1 V I <sub>OL</sub> = 10 mA	—	0.18	0.4	V
			V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.1 V, I <sub>OL</sub> = 15 mA, V <sub>OL</sub> (OUT1) ≥ 2 V	—	0.25	0.4	V
Output leakage current	I <sub>LEAK</sub>	6	V <sub>CC</sub> = 6 V, V <sub>OUT</sub> = 30 V	—	—	10	μA
		6	V <sub>CC</sub> = 6 V, V <sub>OUT</sub> = 0.4 V	—	-1.5	-10	μA
Current consumption	I <sub>CC</sub>	7	V <sub>CC</sub> = 6.5 V, R <sub>L</sub> = ∞	—	3	7	mA

Note: An ambient temperature of 25°C is assumed for the typical values.

**Test Circuit**

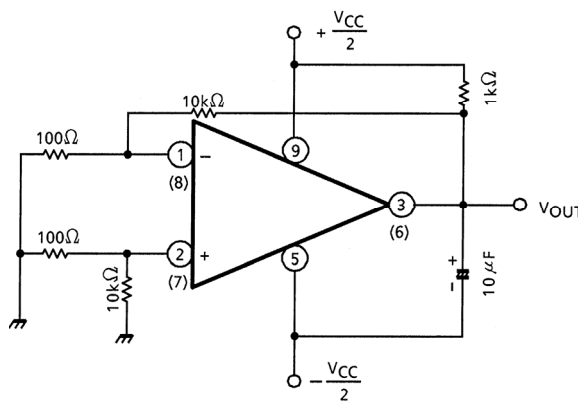
(Below are examples for the TA7522SG. Note that, for the TA7522FG, pin numbers are different.)

**1.  $G_V$**



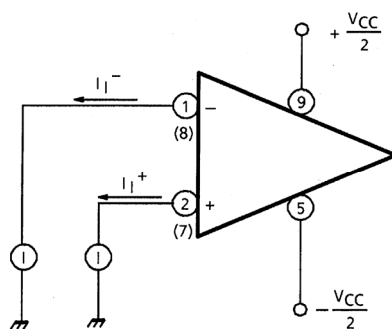
$$G_V = 20 \log \frac{V_{OUT}}{V_{IN}} + 80 \text{ (dB)}$$

**2.  $V_{IO}$**



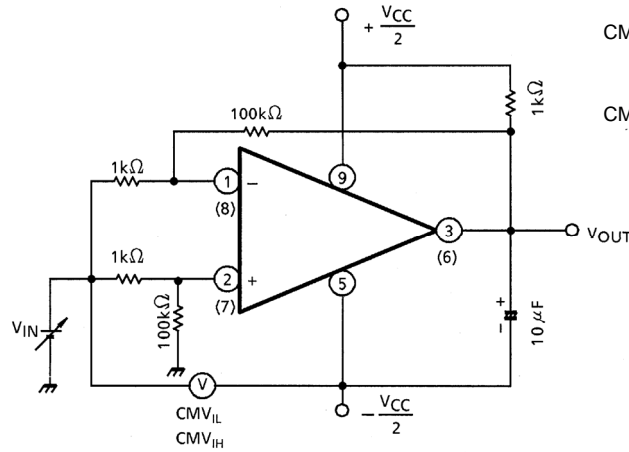
$$V_{IO} = \frac{V_{OUT}}{100}$$

**3.  $I_I, I_{IO}$**



$$I_{IO} = |I_I^- - I_I^+|$$

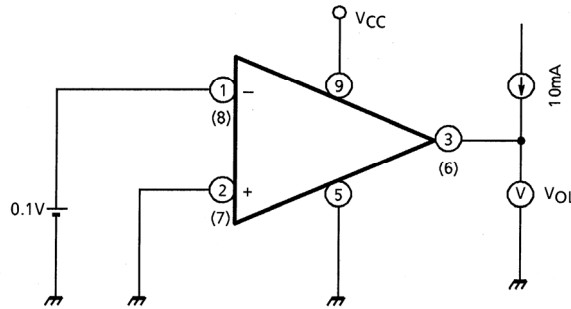
**4. CMV<sub>IL</sub>, CMV<sub>IH</sub>**



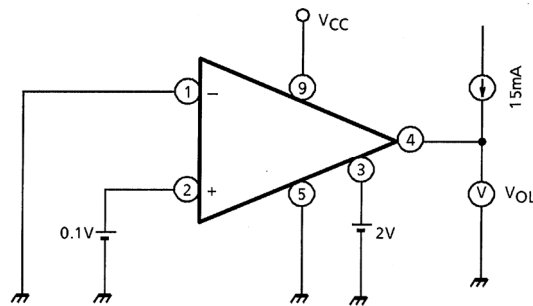
CMV<sub>IL</sub>: Input voltage relative to pin 5 as it is obtained when V<sub>IN</sub> is decreased until output V<sub>OUT</sub> becomes ±2 V.  
 CMV<sub>IH</sub>: Input voltage relative to pin 5 as it is obtained when V<sub>IN</sub> is increased until output V<sub>OUT</sub> becomes ±2 V.

**5. V<sub>OL</sub>**

**5.1 OUT1, OUT2**

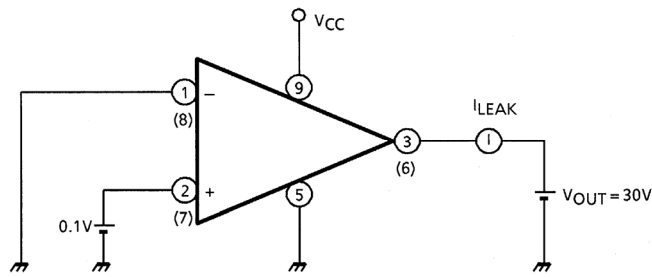


**5.2  $\overline{\text{OUT1}}$**

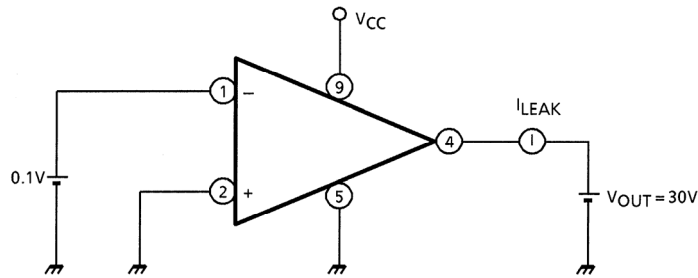


**6.  $I_{LEAK}$**

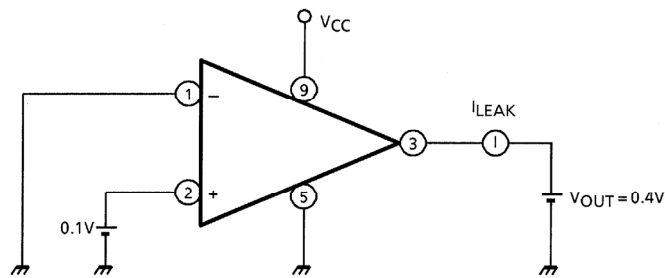
**6.1 OUT1, OUT2**



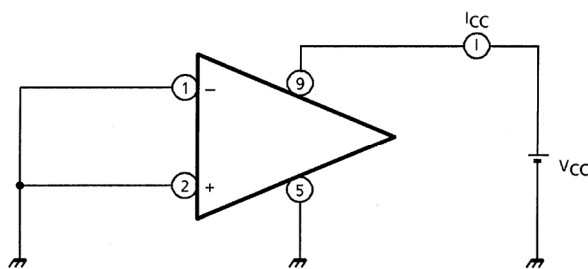
**6.2  $\overline{OUT1}$**



**6.3 OUT1**



**7.  $I_{CC}$**

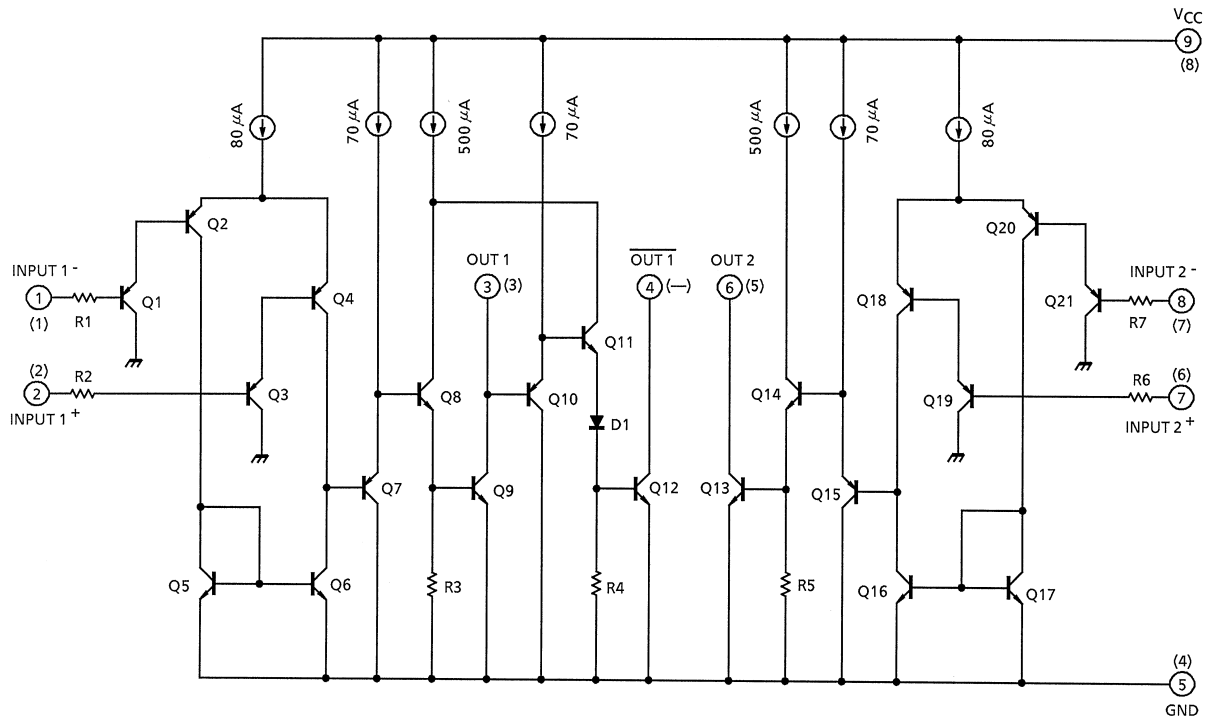


All inputs are grounded.

Note: The components illustrated in the test circuit diagrams are intended only to confirm device characteristics. Toshiba does not guarantee that these components will prevent malfunction or failure in your particular application device.

## Equivalent Circuit

(The Circles O indicate the pin numbers on the TA7522SG.  
The parentheses ( ) indicate the pin numbers on the TA7522FG.)



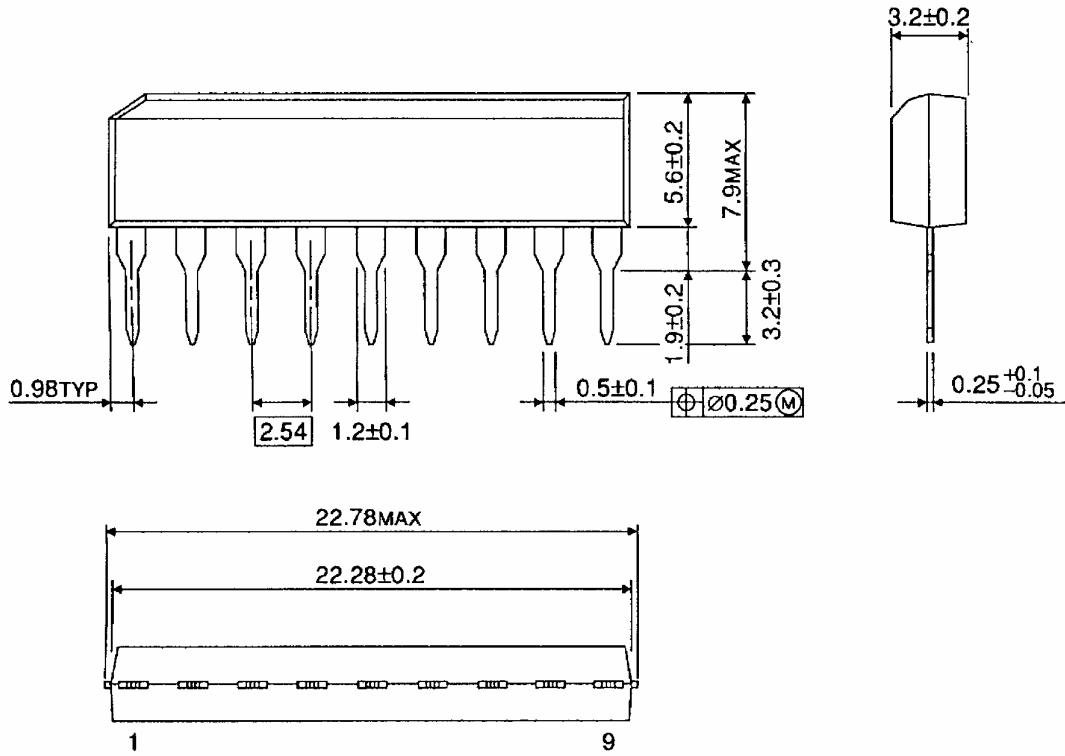
Note: The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.



## Package Dimensions

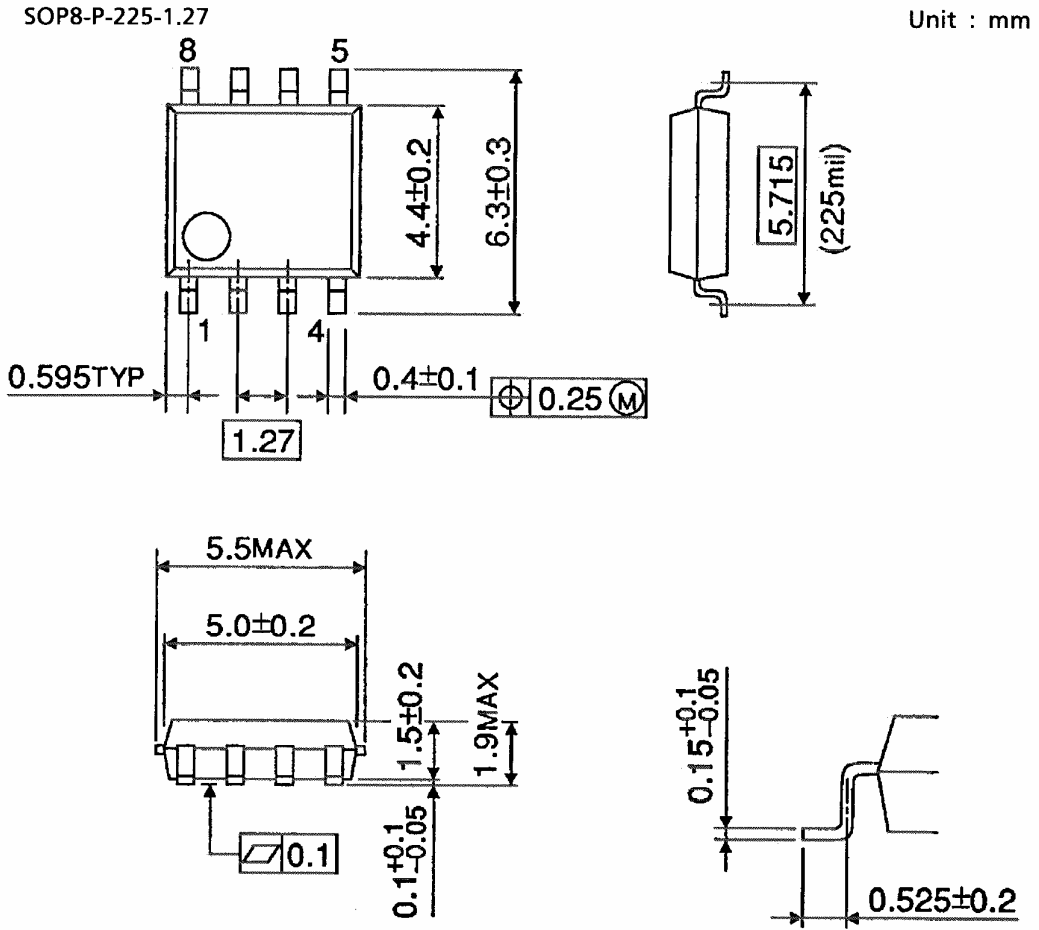
SIP9-P-2.54A

Unit : mm



Weight: 0.92 g (typ.)

## Package Dimensions



Weight: 0.08 g (typ.)

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