## Step-up Converter for Cell Phone Camera Flash LEDs

## Features

■ Supply voltage range: 2.6 V to 5.5 V
■ 17V Maximum Output Voltage
■ Two levels of current up to 300 mA set with external resistors

- Dedicated pin to select the required level of current

■ Operating frequency: 1.5 MHz : PWM Controller

- Torch mode supported
- Shutdown pin with true load disconnection
- Efficiency: $90 \%$ at $100 \mathrm{~mA} ; 80 \%$ at 300 mA
- DFN10L (3mm x 3mm) Package


## Description

The STCF01 is a dedicated IC designed to drive two, three, or four white, cell phone camera flash LEDs with constant current. The step-up (boost) converter input is connected directly to the battery, and its converter output voltage is automatically determined using current feedback-based duty cycle control.

The STCF01 has a dedicated pin for two levels of LED current selection. An external resistor is used

Preliminary Data

to set the required current for each level. Compared to the linear current control technique, this method allows designers to achieve the best and most efficient performance possible with the selected current, thereby avoiding linear element losses.

The Shutdown (SHDN) pin saves power when the camera flash is not used by consuming less than $0.1 \mu \mathrm{~A}$ of current. When the SHDN pin is high (logic ' 1 '), the device is turned OFF and there is no DC current path from the supply to the white LEDs (Load Disconnect). If it is held to GND, the output current continuously flows through the LEDs (Torch mode). The SHDN pin, when it is set to low (logic ' 0 '), is also used to set the flash function time duration.

## Order Codes

| Type | Package | Comments |
| :---: | :---: | :---: |
| STCF01PMR | DFN10L $(3 \mathrm{~mm} \times 3 \mathrm{~mm})$ | 4500 parts per reel |

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## 1 Diagram

Figure 1. Block Diagram


## 2 Pin Configuration

Figure 2. Pin Connnections (Top View )


## Table 1. Pin Description

$\left.$| PIN N | SYMBOL | NOTE |
| :---: | :---: | :--- |
| 1 | PWRGND | This is the application power signal reference. Connect the input and output capacitors to <br> this pin. |
| 2 | OVP | This pin senses step-up output voltage, which provides overvoltage protection in the event <br> that the output voltage exceeds the OVP threshold |
| 3 | $V_{\text {I }}$ | This pin supplies the input voltage for the step-up stage as well as the supply voltage for the <br> overall device. |
| 4 | HC | This pin sets the high level current for the white LEDs with a resistor that is connected <br> between this pin and $V_{1}$. |
| 5 | LC | This pin sets the low level current for the white LEDs with a resistor that is connected <br> between this pin and $V_{1}$. |
| 6 | SGLGND | This pin is the logic signal reference for the IC. <br> 7 FB |
| 8 | SEL pin senses the current flowing through the white LEDs and uses this feedback to |  |
| provide current regulation. |  |  | | This pin is used to select the signal level of the white LEDs; a low level signal sets the low |
| :--- |
| level LED current, while the high level signal sets the high level LED current. | \right\rvert\, | This pin enables or disables the Shutdown mode. A high level signal enables device |
| :--- |
| Shutdown mode, where most of the device internal logic is turned OFF. If this pin is held to |
| GND, the output current flows through the LEDs continuously (Torch mode). |
| 9 | SHDN | This is the switch node pin, which is connected to the internal N-channel MOSFET drain. |
| :---: |
| 10 |

## 3 Application Information

Figure 3. Application Circuit


## 4 Maximum Ratings

Table 2. Absolute Maximum Ratings

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{I}}$ | DC Input Voltage to SGLGND | -0.3 to 6 | V |
| $\mathrm{~V}_{\text {SW }}$ | Switch Voltage | -0.3 to 20 | V |
| FB, SEL, | Voltage Range | -0.3 to $\mathrm{V}_{1}+0.3$ | V |
| SHDN, LC, HC | Over Voltage Protection | -0.3 to 20 | V |
| OVP | Human Body Model | 1.5 | kV |
| ESD | Continuous Power Dissipation (at $\left.\mathrm{T}_{\mathrm{A}}=70^{\circ} \mathrm{C}\right)$ | mW |  |
| $\mathrm{P}_{\text {TOT }}$ | Operating Junction Temperature Range | -40 to 85 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{OP}}$ | -40 to 125 | ${ }^{\circ} \mathrm{C}$ |  |
| $\mathrm{T}_{\mathrm{J}}$ | Junction Temperature | -65 to 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {STG }}$ | Storage Temperature Range | ${ }^{2}$ |  |

Note: Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional Operation under these conditions is not implied.

Table 3. Thermal Data

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{R}_{\text {thJA }}$ | Thermal Resistance Junction-Ambient | 30.9 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\text {thJC }}$ | Thermal Resistance Junction-Case | 2.96 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## 5 Electrical Characteristics

Table 4. DC Electrical Characteristics
$\left(\mathrm{T}_{J}=-40^{\circ} \mathrm{C}\right.$ to $85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{I}}=3.6 \mathrm{~V}, \mathrm{C}_{\mathrm{I}}=2.2 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{O}}=4.7 \mu \mathrm{~F}, \mathrm{~L}=10 \mu \mathrm{H}, \mathrm{R}_{\mathrm{LC}}=12 \mathrm{k} \Omega, \mathrm{R}_{\mathrm{HC}}=4 \mathrm{k} \Omega$, $\mathrm{V}_{\mathrm{OVP}}=8 \mathrm{~V}$, Typ. values @ $25^{\circ} \mathrm{C}$, unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| General Section |  |  |  |  |  |  |
| $V_{1}$ | Max Operation Supply Voltage |  |  |  | 5.5 | V |
| $\mathrm{V}_{\text {UVLO }}$ | Under Voltage lockout threshold | $\mathrm{V}_{\mathrm{SEL}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{SHDN}}=0 \mathrm{~V},$ <br> Min. duty cycle | 2.3 | 2.45 | 2.6 | V |
| $\mathrm{l}_{\mathrm{Q}}$ | Quiescent Current | $\mathrm{V}_{\text {SEL }}=0 \mathrm{~V}$ |  | 2.3 | 3 | mA |
|  |  | Shutdown Mode |  | 0.1 | 0.5 | $\mu \mathrm{A}$ |
| ${ }_{\text {f }}$ W | Switching Frequency | $\mathrm{V}_{\text {SEL }}=0 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}$ | 1.2 | 1.5 | 1.8 | MHz |
| $V_{\text {OVP }}$ | Over Voltage Threshold | No Load | 17 | 18 | 19 | V |
| $\mathrm{I}_{\text {PKMax }}$ | Maximum Inductor Current | $\mathrm{V}_{\text {SHDN }}=0 \mathrm{~V}$ |  | 1.2 |  | A |
| Current Control |  |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{HC}}$ | HC Pin Voltage | $\mathrm{V}_{\text {SEL }}=3 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}$ | $\mathrm{V}_{1}-0.65$ | V 1 -0.6 | $\mathrm{V}_{1}-0.55$ | V |
| $\mathrm{V}_{\mathrm{LC}}$ | LC Pin Voltage | $\mathrm{V}_{\text {SEL }}=0 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}$ | $\mathrm{V}_{1}-0.65$ | $\mathrm{V}_{1}-0.6$ | $\mathrm{V}_{1}-0.55$ | V |
| $\mathrm{ILED}^{\text {/ }} \mathrm{HC}$ | High Level Current Multiplier | $\mathrm{V}_{\text {SEL }}=3 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}\left(25^{\circ} \mathrm{C}\right)$ | 1900 | 2100 | 2300 | A/A |
| liED/LC | Low Level Current Multiplier | $\mathrm{V}_{\text {SEL }}=0 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}\left(25^{\circ} \mathrm{C}\right)$ | 2200 | 2400 | 2600 | A/A |
| $\mathrm{R}_{\text {ON }} \mathrm{FB}$ | Resistance ON Feedback | $\mathrm{I}_{\mathrm{FB}}=100 \mathrm{~mA}$ |  | 1.2 |  | $\Omega$ |
| $\mathrm{I}_{\text {FB(LEAK) }}$ | Current Feedback Leakage | $\mathrm{V}_{\text {SHDN }}=3 \mathrm{~V}, \mathrm{~V}_{\mathrm{FB}}=\mathrm{V}_{1}$ |  | 0.1 | 1 | $\mu \mathrm{A}$ |
| Switch Section |  |  |  |  |  |  |
| $\mathrm{R}_{\mathrm{ON}}$ | Internal Switch ON-Resistance | ISW = 1A Note: 1 |  | 0.3 |  | $\Omega$ |
| $\mathrm{I}_{\text {SW(LEAK) }}$ | Internal Switch Leakage Current | $\mathrm{V}_{\text {SHDN }}=3 \mathrm{~V}, \mathrm{~V}_{\text {SW }}=16.5 \mathrm{~V}$ |  | 0.1 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{D}_{\text {MAX }}$ | Maximum Duty Cycle | $\mathrm{I}_{\mathrm{FB}}=0 \mathrm{~mA}$ |  | 90 |  | \% |
| $\mathrm{D}_{\text {MIN }}$ | Minimum Duty Cycle | $\mathrm{I}_{\mathrm{FB}}=200 \mathrm{~mA} ; \mathrm{V}_{\mathrm{SEL}}=0 \mathrm{~V}$ <br> Pulse Skipping |  | 0 |  | \% |
| Control Input Section |  |  |  |  |  |  |
| $\begin{gathered} \mathrm{V}_{\mathrm{H}(\mathrm{SEL})} \\ \mathrm{V}_{\mathrm{H}(\mathrm{SHDN})} \end{gathered}$ | SEL and SHDN Input High Threshold | $\mathrm{V}_{1}=2.6 \mathrm{~V}$ | 1.2 |  |  | V |
|  |  | $\mathrm{V}_{1}=3.6 \mathrm{~V}$ | 1.4 |  |  |  |
|  |  | $\mathrm{V}_{1}=5.5 \mathrm{~V}$ | 1.6 |  |  |  |
| $\mathrm{V}_{\mathrm{L}(\mathrm{SEL})}$ <br> $\mathrm{V}_{\mathrm{L}(\mathrm{SHDN})}$ | SEL and SHDN Input Low Threshold | $\mathrm{V}_{1}=2.6 \mathrm{~V}$ |  |  | 0.4 | V |
|  |  | $\mathrm{V}_{1}=3.6 \mathrm{~V}$ |  |  | 0.5 |  |
|  |  | $\mathrm{V}_{\mathrm{I}}=5.5 \mathrm{~V}$ |  |  | 0.6 |  |
| Thermal Shutdown |  |  |  |  |  |  |
| $\mathrm{T}_{\text {SD }}$ | Thermal Shutdown |  |  | 145 |  | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{HS}}$ | Thermal Shutdown Hysteresis |  |  | 15 |  | ${ }^{\circ} \mathrm{C}$ |

Note: 1 Typical value, not production tested

## 6 Detailed Description

The STCF01 white Led boost converter drives two up to four white LEDs with a constant current. It needs few external components: two ceramic capacitors ( $\mathrm{C}_{\mathrm{l}}=2.2 \mu \mathrm{~F} \mathrm{C}_{\mathrm{O}}=4.7 \mu \mathrm{~F}$ ), one inductor $\mathrm{L}=10 \mu \mathrm{H}$ and one schottky diode. The device works with a minimum $\mathrm{V}_{\mathrm{I}}=2.6 \mathrm{~V}$, and it has an Over Voltage Protection on the output guaranteed at minimum value equal to 17 V . This value ensures proper operation with a maximum of four White LEDs in series. In the worst case of $\mathrm{V}_{\mathrm{I}}=2.6 \mathrm{~V}$ at $\mathrm{V}_{\mathrm{OVP}}=8 \mathrm{~V}$ (typical value of two LEDs) it is possible to obtain $\mathrm{I}_{\mathrm{O}}=270 \mathrm{~mA}$, while at $\mathrm{V}_{\mathrm{OVP}}=11 \mathrm{~V}$ (typical value of three LEDs) it is possible obtain $\mathrm{I}_{\mathrm{O}}=180 \mathrm{~mA}$ (Figure 4: Maximum Output Current Vs Input Voltage). The maximum IO current is limited by inductor peak current internally set at Typ 1.2A. This feature allows saving the battery life from intensive use of the flash. The SEL pin allows to select high and low current value flowing on the White LEDs.
The two current values are set through external resistors $R_{H C}$ and $R_{L C}$ according the following formula:
$\mathrm{I}_{\text {LED (FLASH })}=2100{ }^{*}\left(\mathrm{~V}_{\mathrm{I}}-\mathrm{V}_{\mathrm{HC}}\right) / \mathrm{R}_{\mathrm{HC}}$
$\operatorname{liED}(\mathrm{TORCH})=2400 *\left(\mathrm{~V}_{\mathrm{I}}-\mathrm{V}_{\mathrm{LC}}\right) / \mathrm{R}_{\mathrm{LC}}$

An High logic level on SHDN pin puts the device in shutdown mode; if it is hold to Low the flash or torch mode is activated. When the SHDN pin is LOW the device provides the requested current in less than $200 \mu \mathrm{~s}$ (see TURN ON TIME plot). This fast turn-on time makes the device suitable for single shoot and multiple shoot operation modes.

## $7 \quad$ Typical Operating Characteristics

( $T_{J}=-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}, V_{I}=3.6 \mathrm{~V}, C_{I}=2.2 \mu \mathrm{~F}, C_{O}=4.7 \mu \mathrm{~F}, \mathrm{~L}=10 \mu \mathrm{H}, R_{L C}=12 \mathrm{k} \Omega, R_{H C}=4 \mathrm{k} \Omega, V_{O V P}$ $=8 \mathrm{~V}$, Typ. values @ $25^{\circ} \mathrm{C}$, unless otherwise specified).

Figure 4. Maximum Output Current Vs Input Voltage


Figure 6. Efficiency Vs Temperature


Figure 8. Efficiency vs Temperature


Figure 5. Maximum Output Current Vs Input Voltage


Figure 7. Efficiency Vs Temperature


Figure 9. Quiescent Current at Full Operation Vs Temperature


Figure 10. Feedback Voltage Vs Temperature


Figure 12. $\mathrm{V}_{\mathrm{I}}-\mathrm{V}_{\mathrm{LC}} \mathrm{Vs}_{\mathrm{s}}$ Temperature


Figure 14. Peak Inductor Current Vs Temperature


Figure 11. Current Feedback Leakage Vs Temperature


Figure 13. $\mathrm{V}_{\mathrm{I}}-\mathrm{V}_{\mathrm{HC}} \mathrm{Vs}^{\text {Temperature }}$


Figure 15. RonfB Vs Temperature


Figure 16. Maximum Duty Cycle Vs Temperature


Figure 18. Turn On Time

$\mathrm{V}_{\text {SHDN }}=$ pulse, $\mathrm{FREQ} .=1 \mathrm{~Hz}, \mathrm{~T}_{\mathrm{r}}=\mathrm{T}_{\mathrm{f}}=10 \mu \mathrm{~s}$
Figure 20. Multi Shoot Mode

$\mathrm{V}_{\mathrm{SEL}}=\mathrm{GND}, \mathrm{V}_{\mathrm{SHDN}}=$ pulse

Figure 17. Turn On and Off Response


Figure 19. Start Up Transient


Figure 21. Torch Mode Behavior


## 8 Package Mechanical Data

In order to meet environmental requirements, ST offers these devices in ECOPACK ${ }^{\circledR}$ packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

## DFN10 (3x3) MECHANICAL DATA

| DIM. | mm. |  |  | mils |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 0.80 | 0.90 | 1.00 | 31.5 | 35.4 | 39.4 |
| A1 |  | 0.02 | 0.05 |  | 0.8 | 2.0 |
| A2 |  | 0.70 |  |  | 25.6 |  |
| A3 |  | 0.20 |  | 7.9 |  |  |
| b | 0.18 | 0.23 | 0.30 | 7.1 | 9.1 | 11.8 |
| D |  | 3.00 |  |  | 118.1 |  |
| D2 | 2.21 | 2.26 | 2.31 | 87.0 | 89.0 | 91.0 |
| E |  | 3.00 |  |  | 118.1 |  |
| E2 | 1.29 | 1.34 | 1.39 | 50.8 | 52.8 | 54.8 |
| e |  | 0.50 |  |  | 19.7 |  |
| L | 0.45 | 0.55 | 0.65 | 17.7 | 21.7 | 25.6 |



Tape \& Reel QFNxx/DFNxx (3x3) MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A |  |  | 330 |  |  | 12.992 |
| C | 12.8 |  | 13.2 | 0.504 |  | 0.519 |
| D | 20.2 |  |  | 0.795 |  |  |
| N | 60 |  |  | 2.362 |  |  |
| T |  | 3.3 |  |  | 0.130 |  |
| Ao |  | 3.3 |  |  | 0.130 |  |
| Bo |  | 1.1 |  |  | 0.043 |  |
| Ko |  | 4 |  |  | 0.157 |  |
| Po |  | 8 |  |  | 0.315 |  |
| P |  |  |  |  |  |  |



## 9 Revision History

| Date | Revision | Description of Change |  |
| :---: | :---: | :--- | :--- |
| 10 -Oct-2005 | 1 | First Release. |  |

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