

P-Channel 12-V (D-S) MOSFET

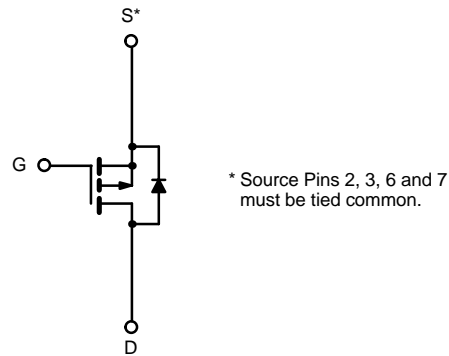
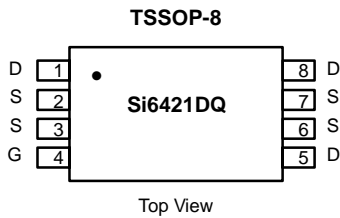
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-12	0.0105 @ $V_{GS} = -4.5$ V	-9.5
	0.0135 @ $V_{GS} = -2.5$ V	-8.5
	0.0175 @ $V_{GS} = -1.8$ V	-7.3

FEATURES

- TrenchFET® Power MOSFET

APPLICATIONS

- Load Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V_{DS}	-12		V
Gate-Source Voltage		V_{GS}	± 8		
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	$T_A = 25^\circ\text{C}$	I_D	-9.5	-7.5	A
	$T_A = 70^\circ\text{C}$		-7.5	-6	
Pulsed Drain Current (10 μs Pulse Width)		I_{DM}	-30		
Continuous Source Current (Diode Conduction) ^a		I_S	-1.5	-0.95	W
Maximum Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D	1.75	1.08	
	$T_A = 70^\circ\text{C}$		1.14	0.69	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	55	70	$^\circ\text{C/W}$
	Steady State		95	115	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	38	50	

Notes

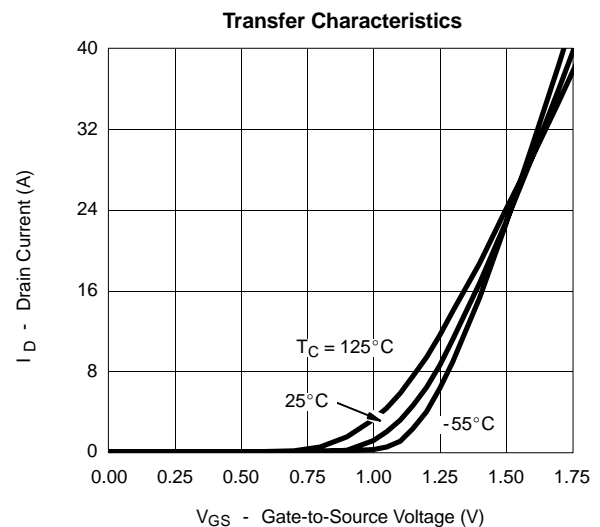
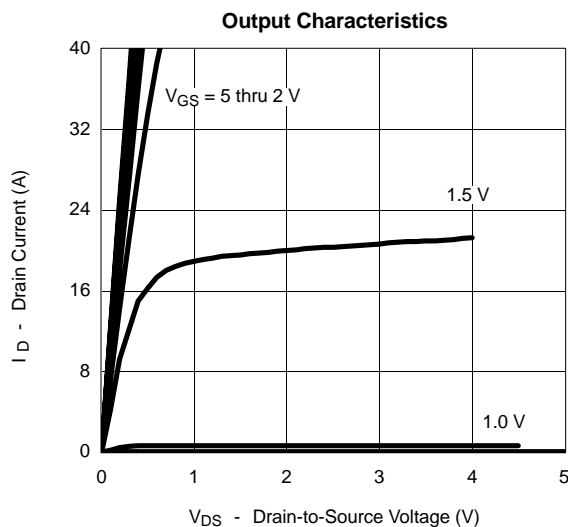
a. Surface Mounted on 1" x 1" FR4 Board.


SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -550 μA	-0.40		-0.8	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -9.6 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -9.6 V, V _{GS} = 0 V, T _J = 70 °C			-10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -4.5 V	20			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -9.5 A		0.008	0.0105	Ω
		V _{GS} = -2.5 V, I _D = -8.5 A		0.0105	0.0135	
		V _{GS} = -1.8 V, I _D = -7.5 A		0.0135	0.0175	
Forward Transconductance ^a	g _{fs}	V _{DS} = -15 V, I _D = -9.5 A		50		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.5 A, V _{GS} = 0 V		-0.64	-1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -5 V, I _D = -9.5 A		60	90	nC
Gate-Source Charge	Q _{gs}			8		
Gate-Drain Charge	Q _{gd}			16		
Gate Resistance	R _g			4.3		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 15 Ω I _D ≅ -1 A, V _{GEN} = -4.5 V, R _G = 6 Ω		46	70	ns
Rise Time	t _r			92	140	
Turn-Off Delay Time	t _{d(off)}			235	350	
Fall Time	t _f			165	250	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = -1.5 A, di/dt = 100 A/μs		140	

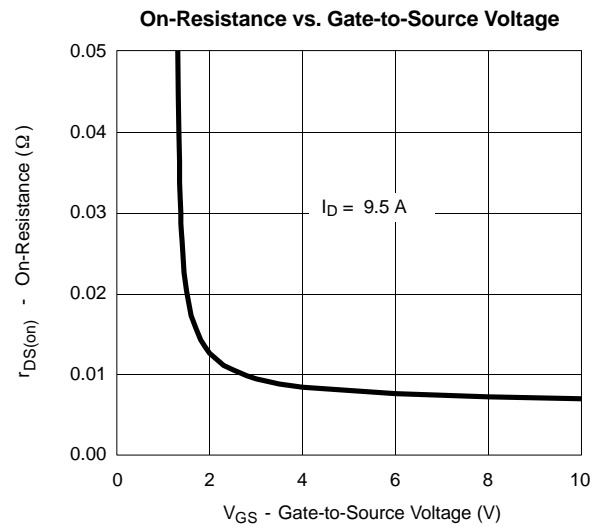
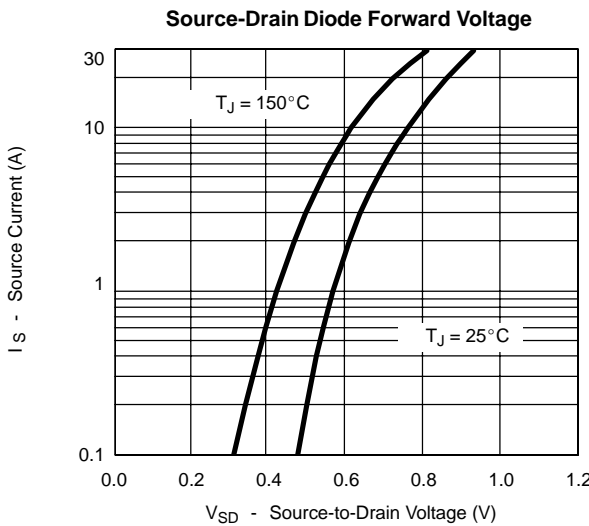
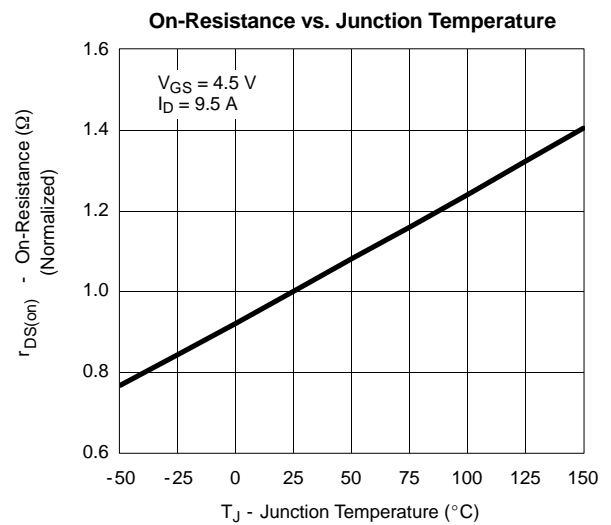
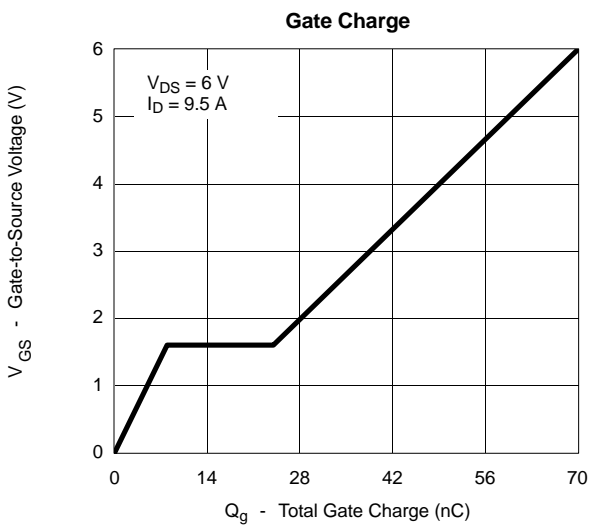
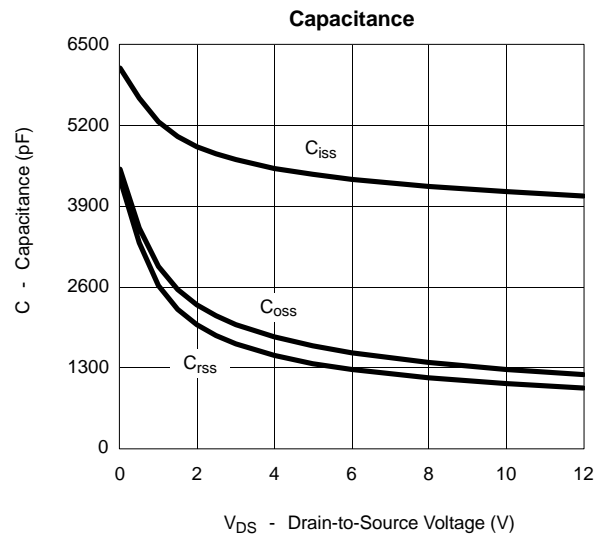
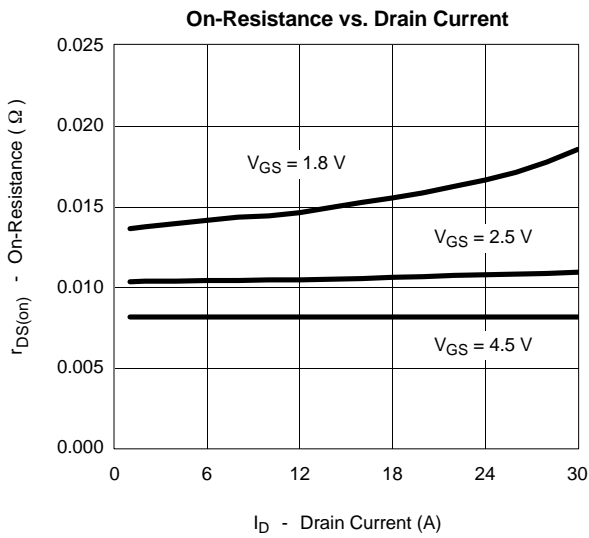
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
 b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)


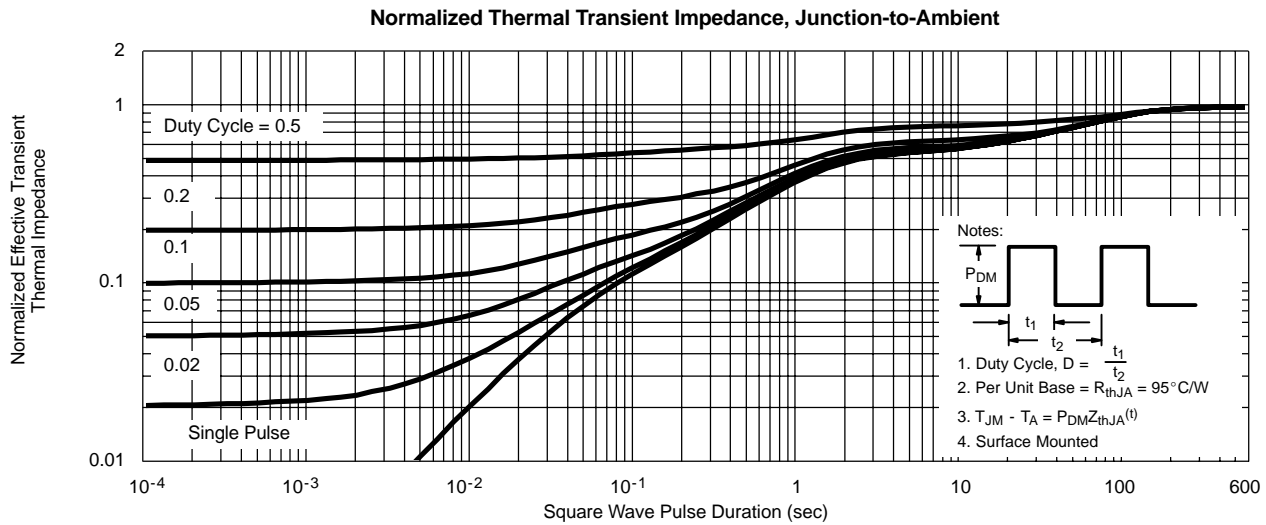
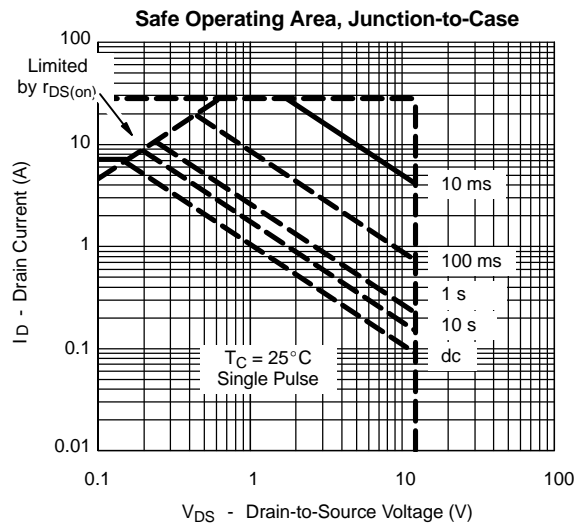
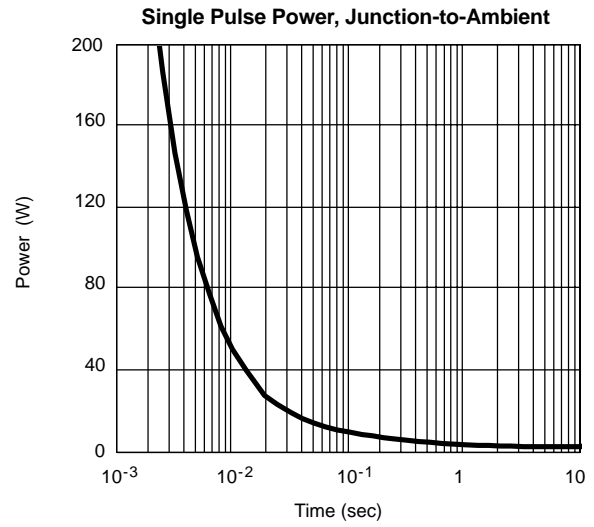
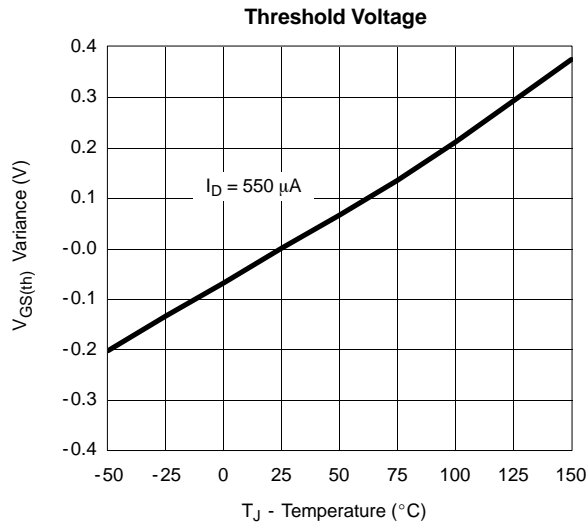


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