Discontinued Product

OKI Semiconductor

MSM9894AL
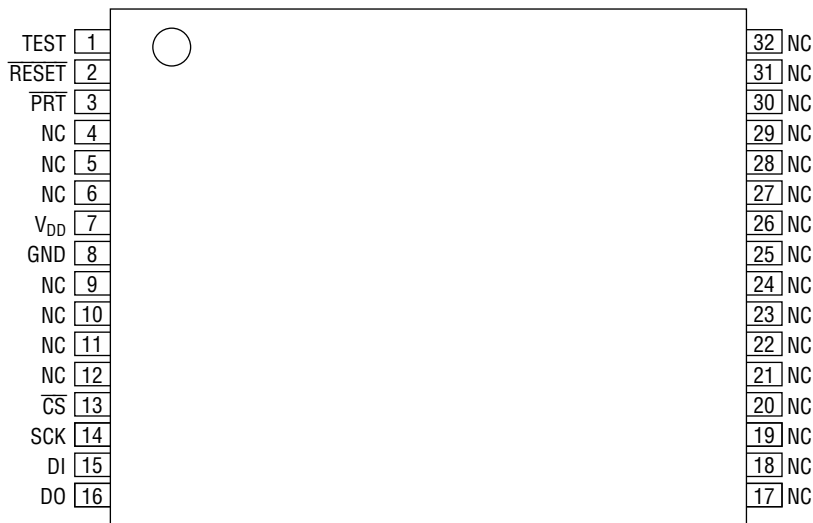
8M-bit Serial Voice Flash Memory

GENERAL DESCRIPTION

The MSM9894AL is a 8Mb flash memory that operates at 2.7 V to 3.6 V. Since backup is not needed, the number of pins is small, and the chip is contained in a small-package 32-pin TSOP, the MSM9894AL is a flash memory suitable for applications such as handy terminals. In combination with Oki's recording/playback IC (MSM9888L/MSM9889L), a solid-state recording/ playback system can be easily constructed.

FEATURES

- Small page size   : 2,112 bits per page
  (one time write unit)
- Configuration   : 4096 pages × 2112 bits
- Power supply voltage   : Single 2.7 to 3.6 V
- Operating current   : Supply current : Up to 35 mA
                            : Stanby current : Up to 10 µA
- Operating temperature : –10 to +70°C
- Write Cycles   : 10,000 cycles per page
- Package:
  32-pin plastic TSOP (TSOPI32-P-820-0.50-K)   (Product name: MSM9894ALTS-KT)

PIN CONFIGURATION (TOP VIEW)

TEST	1		32	NC
$\overline{\text{RESET}}$	2		31	NC
$\overline{\text{PRT}}$	3		30	NC
NC	4		29	NC
NC	5		28	NC
NC	6		27	NC
V_{DD}	7		26	NC
GND	8		25	NC
NC	9		24	NC
NC	10		23	NC
NC	11		22	NC
NC	12		21	NC
$\overline{\text{CS}}$	13		20	NC
SCK	14		19	NC
DI	15		18	NC
DO	16		17	NC

NC : No connection

32-Pin Plastic TSOP

PIN DESCRIPTIONS

Pin	Symbol	I/O	Description
15	DI	I	Command, address, or data input pin.
16	DO	O	Data output pin.
14	SCK	I	Inputs the data transfer clock for the DI and DO pins.
13	$\overline{CS}$	I	The device accepts the SCK pulse when $\overline{CS}$ is at "L" level and does not accept the SCK pulse when $\overline{CS}$ is at "H" level.
1	TEST	O	Output pin for test. Leave this pin open.
3	$\overline{PRT}$	I	Prohibits flash memory programming at "L" level.
2	$\overline{RESET}$	I	The device is reset when $\overline{RESET}$ is at "L" level.
7	V_{DD}	I	Power supply pin (2.7 to 3.6 V)
8	GND	I	GND pin (0 V)

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V_{DD}	Ta=25°C	−0.3 to +7.0	V
Input Voltage	V_{IN}	Ta=25°C	−0.6 to V_{DD}+0.6	V
Storage Temperature	T_{STG}	—	−55 to +150	°C

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Condition	Range	Unit
Power Supply Voltage	V_{DD}	GND=0V	2.7 to 3.6	V
Operating Temperature	T_{op}	—	−10 to +70	°C

ELECTRICAL CHARACTERISTICS

DC Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit.
Operating Current	I_{CC}	—	15	35	mA
Standby Current	I_{SB}	—	—	10	μA
Input Leackage Current	I_{IL}	—	—	1	μA
Output Leackage Current	I_{OL}	—	—	1	μA
Input Low Voltage	V_{IL}	—	—	0.6	V
Input High Voltage	V_{IH}	2.0	—	—	V
Output Low Voltage	V_{OL}	—	—	0.4	V
Output High Voltage	V_{OH}	2.0	—	—	V

PROGRAMMING/ERASE CHARACTERISTICS

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Erase/Programming Cycle	C_{EP}	—	—	—	10,000	Cycles
Data Retention Time	T_{DR}	—	—	—	10	Years
Write Disturb *1)	C_{PD}	Bit error : non	—	—	20,000	Cycles
		Bit error : 1bit	—	—	50,000	Cycles
		Bit error : 3bits	—	—	100,000	Cycles

*1) "Write Disturb" means a phenomenon that frequent write cycles executed to pages in Flash memory may cause a data error in another page to which write operations are not performed.
For example, 20,001 to 50,000 write operations performed to pages other than page "n" may cause a 1-bit error in page "n".

APPLICATION CIRCUIT
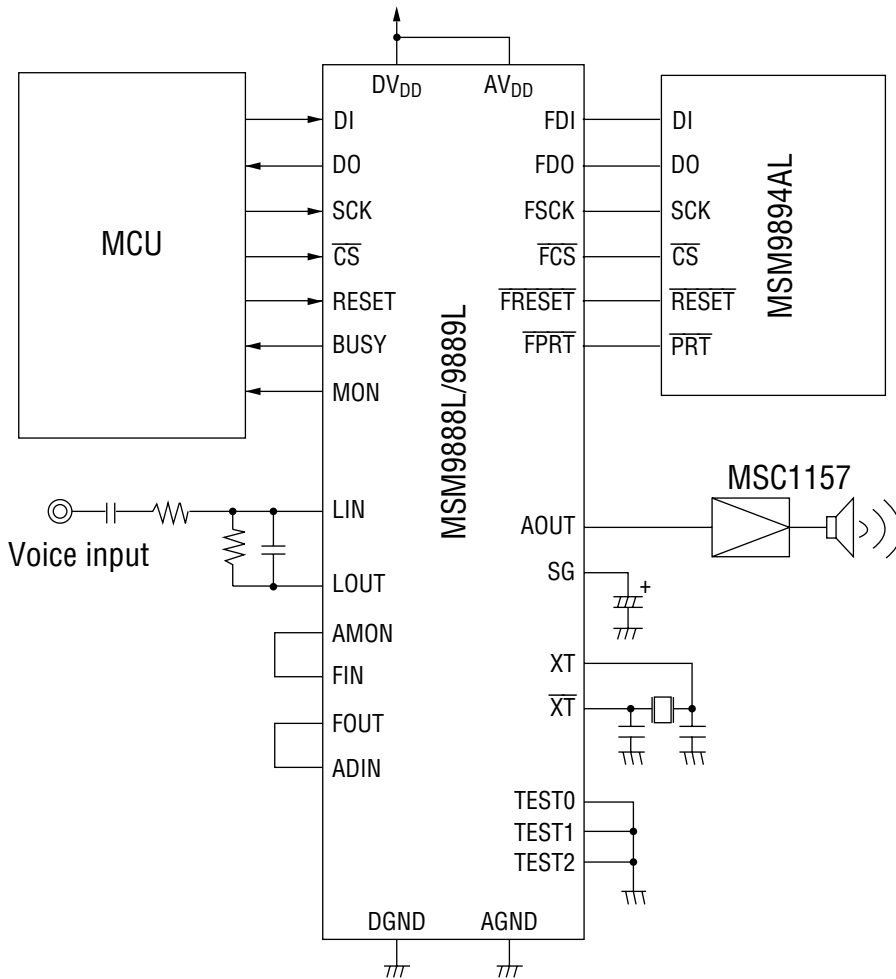
WRITE DISTURB

"Write Distrub" means a phenomenon that the change from digital "0" to "1" may occur in a Flash memory page to which data is scarcely written.
The above change can be avoided by refreshing Flash memory data with the DTRW command and WEND command of the MSM9888L/MSM9889L.
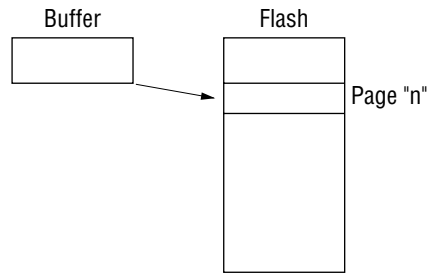
① DTRW command

This command moves some Flash memory page data to buffer.

Buffer   Flash

Page "n"
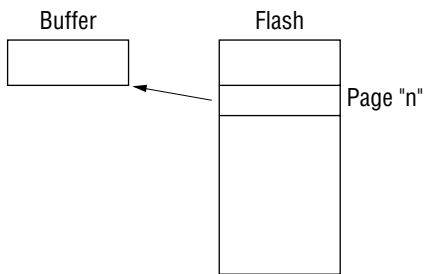
② WEND command

This command programs buffer data to Flash memory.
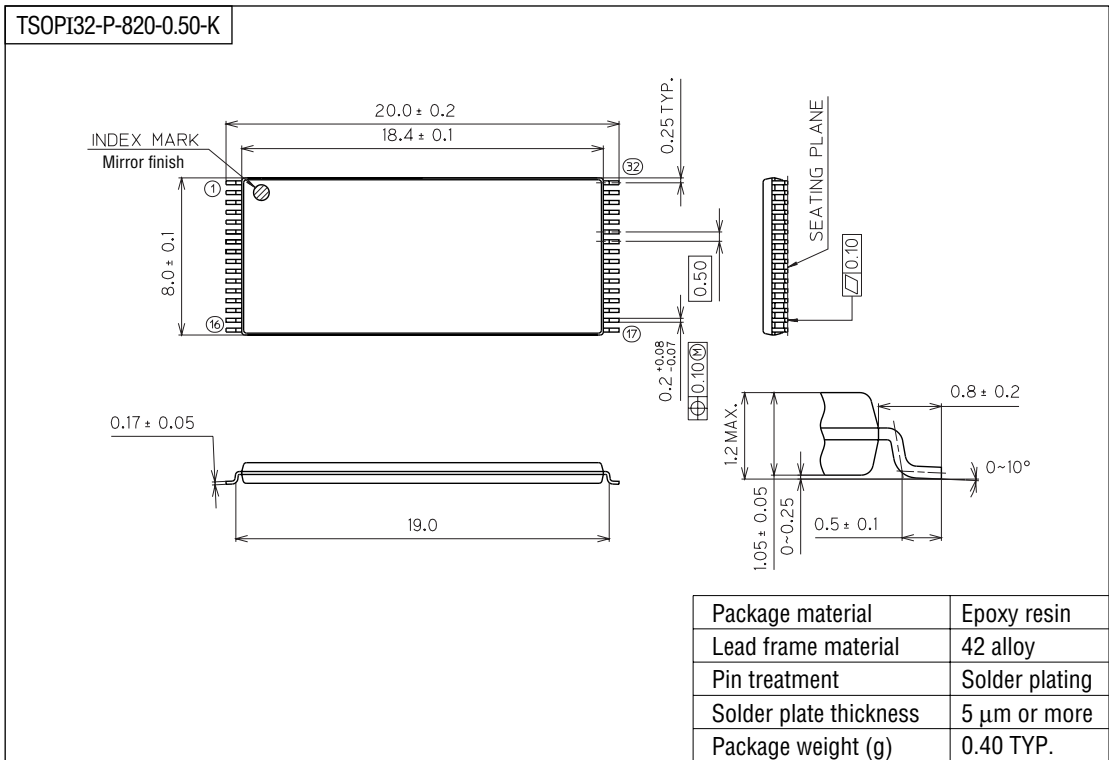
Buffer   Flash

Page "n"

TIMING WHEN POWER IS ON

Refer to the MSM9888L/MSM9889L Data Sheet. If the timing diagrams described in the MSM9888L/MSM9889L Data Sheet are not satisfied, errors such as "Recording is disabled" or "Recorded message is erased" may occur.

PACKAGE DIMENSIONS

(Unit : mm)

TSOPI32-P-820-0.50-K

Package material	Epoxy resin
Lead frame material	42 alloy
Pin treatment	Solder plating
Solder plate thickness	5 μm or more
Package weight (g)	0.40 TYP.

Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, TQFP, LQFP, SOJ, QFJ (PLCC), SHP, and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact Oki's responsible sales person on the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

NOTICE

1. The information contained herein can change without notice owing to product and/or technical improvements. Before using the product, please make sure that the information being referred to is up-to-date.

2. The outline of action and examples for application circuits described herein have been chosen as an explanation for the standard action and performance of the product. When planning to use the product, please ensure that the external conditions are reflected in the actual circuit, assembly, and program designs.

3. When designing your product, please use our product below the specified maximum ratings and within the specified operating ranges including, but not limited to, operating voltage, power dissipation, and operating temperature.

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