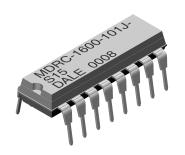
### Vishay Dale



# Resistor/Capacitor Networks, Dual-In-Line, Molded DIP, 16 Pin



#### **FEATURES**

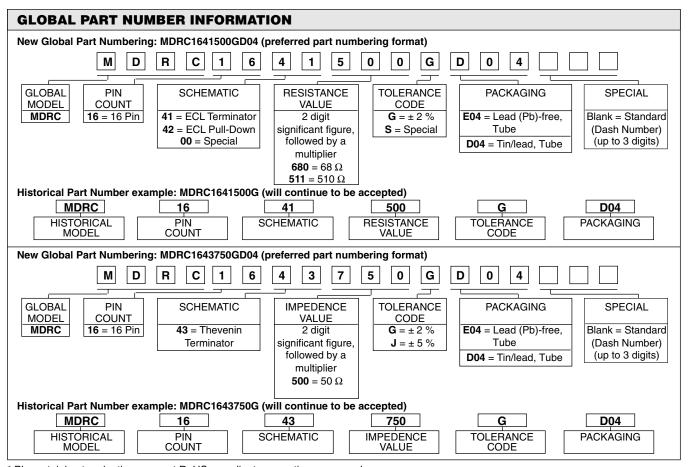
- 0.190" [4.83 mm] maximum seated height
- Rugged molded case construction
- Thick film resistive elements
- · Reduces total assembly cost
- Low temperature coefficient (- 30 °C to + 85 °C) ± 100 ppm/°C



- Compatible with automatic insertion equipment
- Reduces PC board space
- Lead (Pb)-free version is RoHS compliant

| STANDARD ELECTRICAL SPECIFICATIONS |           |   |  |                                |              |                             |                           |  |
|------------------------------------|-----------|---|--|--------------------------------|--------------|-----------------------------|---------------------------|--|
| GLOBAL<br>MODEL                    | SCHEMATIC | POWER<br>RATING<br>P <sub>25°C</sub><br>W | RESISTOR CHARACTERISTICS                   |                                |              |                             | CAPACITOR CHARACTERISTICS |  |
|                                    |           |   | PACKAGE<br>POWER<br>RATING<br>W at + 25 °C | RESISTANCE<br>TOLERANCE<br>± % | COFFFICIENT  | TCR<br>TRACKING<br>± ppm/°C | CAPACITOR<br>TOLERANCE    | CAPACITANCE<br>VOLTAGE<br>RATING<br>V max. |
| MDRC                               | 1641      | 0.15 max                                  | 2.0 max.                                   | ± 2, or 2 Ω*                   | ± 100 ppm/°C | 50                          | 0.1 μF + 40 %, - 20 %     | 25   |
| MDRC                               | 1642      | 0.15 max                                  | 2.0 max.                                   | ± 2, or 2 Ω*                   | ± 100 ppm/°C | 50                          | 0.1 μF + 40 %, - 20 %     | 25   |
| MDRC                               | 1643      | 0.20 max                                  | 2.0 max.                                   | ± 2, or 2 Ω*                   | ± 100 ppm/°C | 50                          | 0.1 μF + 40 %, - 20 %     | 25   |

<sup>\*</sup> Whichever is greater



<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

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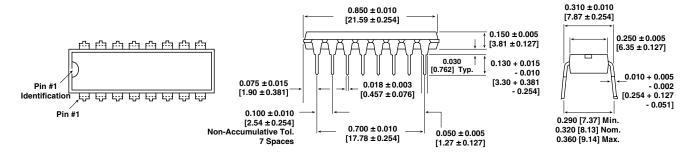




# Resistor/Capacitor Networks, Dual-In-Line, Molded DIP, 16 Pin

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### **DIMENSIONS** in inches [millimeters]



| RESISTANCE VALUE IN OHMS (G TOLERANCE) |                |                |     |  |  |
|--|----------------|----------------|-----|--|--|
|  | MDRC1643       |                |     |  |  |
| <b>MDRC1641</b> 50, 68, 75, 100        | R <sup>1</sup> | R <sup>2</sup> | Zo  |  |  |
|  | 81             | 130            | 50  |  |  |
| MDRC1642                               | 121            | 195            | 75  |  |  |
| 510                                    | 162            | 260            | 100 |  |  |

| TECHNICAL SPECIFICATIONS                    |                 |                 |  |  |
|---|-----------------|-----------------|--|--|
| PARAMETER                                   | UNIT            | MDRC            |  |  |
| Operating Voltage (at + 25 °C)              | V <sub>AC</sub> | 50 maximum      |  |  |
| Capacitor Dissipation Factor                | %               | < 3             |  |  |
| Voltage Coefficient of Resistance (typical) | ppm/V           | < 50            |  |  |
| Operating Temperature Range                 | °C              | - 30 to + 85 °C |  |  |
| Storage Temperature Range                   | °C              | - 30 to + 85 °C |  |  |

| MECHANICAL SPECIFICATIONS      |  |  |  |
|--------------------------------|--|--|--|
| Marking Resistance to Solvents | Permanency testing per MIL-STD-202, Method 215 |  |  |
| Solderability                  | Per MIL-STD-202, Method 208E                   |  |  |
| Terminals                      | Copper alloy, solder plated                    |  |  |
| Body                           | Molded epoxy                                   |  |  |
| Weight                         | 1.5 grams                                      |  |  |

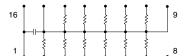
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## Resistor/Capacitor Networks, Dual-In-Line, Molded DIP, 16 Pin



#### **CIRCUIT APPLICATIONS**

#### MDRC1641 Schematic



#### - 2.0 and - 5.2 Volt ECL Terminator

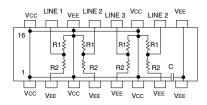
The MDRC1641 circuit contains 11 resistors of nominally equal value and a 0.01 microfarad decoupling capacitor. The MDRC-1641 is designed for ECL Line Termination to a - 2.0 volt buss. The 0.01 microfarad decoupling capacitor is for bypassing transients between supply voltages.

#### MDRC1642 Schematic



The MDRC1642 circuit contains 12 resistors of 510 ohm each and a 0.01 microfarad decoupling capacitor. The MDRC-1642 is designed for ECL Pull-down to a - 5.2 volt buss. The 0.01 microfarad decoupling capacitor is for bypassing voltage transients on the voltage buss.

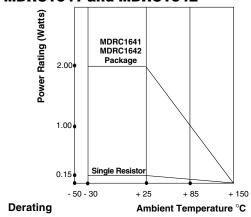
#### MDRC1641 Schematic



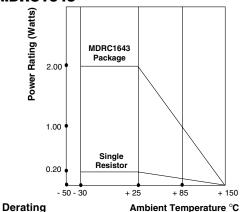
#### Thevenin Equivalent Terminator

The MDRC1643 contains four pair of series resistors. The circuit is compatible with ECL pin configurations. Each terminator section (series pair) contains a voltage divider between Vcc (0 volt) and VEE (- 5.2 volt) providing a Thevenin equivalent voltage of - 2.0 volts. A 0.01 microfarad decoupling capacitor bypasses the VEE buss.

#### **MDRC1641 and MDRC1642**



#### **MDRC1643**



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# Resistor/Capacitor Networks, Dual-In-Line, Molded DIP, 16 Pin

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| PERFORMANCE                     |  |                                       |  |  |
|---------------------------------|--|---------------------------------------|--|--|
| TEST                            | CONDITIONS   | MAX ∆ <i>R</i><br>(Typical Test Lots) |  |  |
| Thermal Shock                   | MDRC1641 and MDRC1642, 5 cycles between - 30 °C and + 85 °C MDRC1643, 5 cycles between - 65 °C and + 125 °C                              | ± 0.50 % ΔR                           |  |  |
| Short Time Overload             | 2.5 x rated working voltage 5 seconds  | ± 0.25 % ΔR                           |  |  |
| Low Temperature Operation       | MDRC1641 and MDRC1642, 45 minutes at full rated working voltage at - 30 °C MDRC1643, 45 minutes at full rated working voltage at - 65 °C | ± 0.25 % ΔR                           |  |  |
| Moisture Resistance             | 240 hours with humidity ranging from 80 % RH to 98 % RH  | ± 0.50 % ΔR                           |  |  |
| Resistance to Soldering Heat    | Leads immersed in + 350 °C solder to within 1/16" of device body for 3 seconds   | ± 0.25 % ΔR                           |  |  |
| Shock                           | Total of 18 shocks at 100 G's  | ± 0.25 % ΔR                           |  |  |
| Vibration                       | 12 hours at maximum of 20 G's between 10 and 2000 Hz   | ± 0.25 % ΔR                           |  |  |
| Load Life                       | 1000 hours at + 70 °C, rated power applied 1.5 hours "ON", 0.5 hour "OFF" for full 1000 hour period. Derated according to the curve.     | ± 0.50 % ΔR                           |  |  |
| Terminal Strength               | 4.5 pound pull for 30 seconds  | ± 0.25 % ΔR                           |  |  |
| Insulation Resistance           | 10 000 Megohm (minimum)  | -                                     |  |  |
| Dielectric Withstanding Voltage | (200 V <sub>RMS</sub> for 1 minute)  | -                                     |  |  |

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