

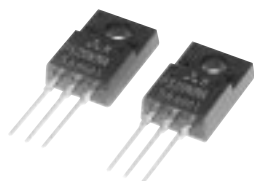
PRELIMINARY
 Notice: This is not a final specification.
 Some parametric limits are subject to change.

MITSUBISHI Nch POWER MOSFET

FS20KMA-5A

HIGH-SPEED SWITCHING USE

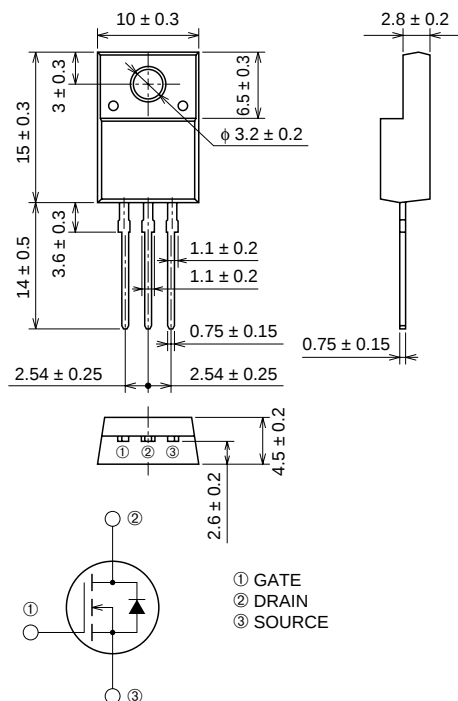
FS20KMA-5A



- 10V DRIVE
- V_{DSS} 250V
- $r_{DS(ON)}$ (MAX) 0.20Ω
- I_D 20A

OUTLINE DRAWING

Dimensions in mm



APPLICATION

Cs Switch for CRT Display monitor

MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V_{DSS}	Drain-source voltage	$V_{GS} = 0V$	250	V
V_{GSS}	Gate-source voltage	$V_{DS} = 0V$	± 20	V
I_D	Drain current		20	A
I_{DM}	Drain current (Pulsed)		60	A
I_{DA}	Avalanche drain current (Pulsed)	$L = 200\mu H$	20	A
P_D	Maximum power dissipation		40	W
T_{ch}	Channel temperature		$-55 \sim +150$	°C
T_{stg}	Storage temperature		$-55 \sim +150$	°C
V_{iso}	Isolation voltage	AC for 1minute, Terminal to case	2000	V
—	Weight	Typical value	2.0	g

Sep.1998

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FS20KMA-5A

HIGH-SPEED SWITCHING USE

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	250	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±10	μA
IDSS	Drain-source leakage current	VDS = 250V, VGS = 0V	—	—	1	mA
VGS (th)	Gate-source threshold voltage	Id = 1mA, VDS = 10V	2.0	3.0	4.0	V
rDS (ON)	Drain-source on-state resistance	Id = 10A, VGS = 10V	—	0.15	0.20	Ω
VDS (ON)	Drain-source on-state voltage	Id = 10A, VGS = 10V	—	1.50	2.00	V
yfs	Forward transfer admittance	Id = 10A, VDS = 10V	—	20.0	—	S
Ciss	Input capacitance	VDS = 25V, VGS = 0V, f = 1MHz	—	2250	—	pF
Coss	Output capacitance		—	220	—	pF
Crss	Reverse transfer capacitance		—	65	—	pF
td (on)	Turn-on delay time	VDD = 150V, Id = 10A, VGS = 10V, RGEN = RGS = 50Ω	—	35	—	ns
tr	Rise time		—	60	—	ns
td (off)	Turn-off delay time		—	400	—	ns
tf	Fall time		—	90	—	ns
VSD	Source-drain voltage	Is = 10A, VGS = 0V	—	0.95	—	V
Rth (ch-c)	Thermal resistance	Channel to case	—	—	3.13	°C/W