

# FAN4230 Dual, High Speed, 2.5V to 12V, Rail-to-Rail Amplifier

### Features at ±5V

**=AIRCHIL** 

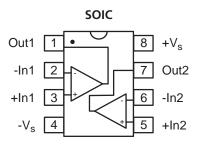
SEMICONDUCTOR

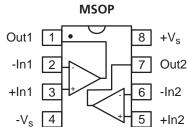
- 2.5mA supply current per amplifier
- 358MHz bandwidth
- Output voltage range at  $R_L = 150\Omega$ : -4.9V to 4.81V
- Input includes negative rail
- 217V/µs slew rate
- ±130mA output short circuit current
- $12nV/\sqrt{Hz}$  input voltage noise
- Competes with AD8052 and LMH6643
- Package options (MSOP-8 and SOIC-8)
- Fully specified at +3V, +5V, and  $\pm5V$  supplies

### **Applications**

- A/D driver
- Active filters
- CCD imaging systems
- CD/DVD ROM
- Coaxial cable drivers
- · Portable/battery-powered applications
- Twisted pair driver
- Video driver

### **Pin Assignments**





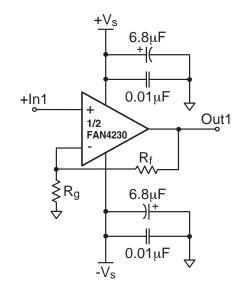
### Description

The FAN4230 is a dual, low cost, high performance, voltage feedback amplifier that consumes only 2.5mA of supply current while providing  $\pm 130$ mA of output short circuit current. The FAN4230 is designed to operate from 2.5V to 12V ( $\pm 6$ V) supplies. The common mode voltage range extends below the negative rail and the output provides rail-to-rail performance.

The FAN4230 is designed on a complimentary bipolar process and provides 358MHz of bandwidth and 217V/ $\mu$ s of slew rate at a supply voltage of ±5V. The combination of low power, rail-to-rail performance, low voltage operation, and tiny package options make the FAN4230 well suited for use in many general purpose high speed applications.

| TBD |  |  |
|-----|--|--|
|     |  |  |
|     |  |  |
|     |  |  |
|     |  |  |
|     |  |  |

### **Typical Application**



# **Electrical Characteristics** ( $V_s = +3V$ , G = 2, $R_L = 2k\Omega$ to $V_s/2$ ; unless noted)

| Parameters  | Conditions   | ТҮР  | Min & Max | UNITS  | NOTES                           |
|---|--|--|-----------|--|---------------------------------|
| Case Temperature  |  | +25°C  | +25°C     |  |                                 |
| Frequency Domain Response<br>-3dB bandwidth<br>full power bandwidth   | $\begin{array}{l} G = +1,  V_{O} = 0.2 V_{pp} \\ G = +2,  V_{O} = 0.2 V_{pp} \\ G = +2,  V_{O} = 2 V_{pp} \end{array}$ | 295<br>119<br>75                                     |           | MHz<br>MHz<br>MHz                                  |                                 |
| gain bandwidth product  |  | 155  |           | MHz  |                                 |
| Time Domain Response<br>rise and fall time<br>settling time to 0.1%<br>overshoot<br>slew rate   | 0.2V step<br>2V step<br>0.2V step,<br>3V step, G = -1  | 2.74<br>TBD<br>8<br>215                              |           | ns<br>ns<br>%<br>V/µs                              |                                 |
| Distortion and Noise Response<br>2nd harmonic distortion<br>3rd harmonic distortion<br>THD<br>input voltage noise<br>crosstalk  | 1V <sub>pp</sub> , 5MHz<br>1V <sub>pp</sub> , 5MHz<br>1V <sub>pp</sub> , 5MHz<br>>1MHz<br>10MHz                        | -80<br>-80<br>75<br>12.45<br>TBD                     |           | dBc<br>dBc<br>dB<br>nV/√Hz<br>dB                   |                                 |
| DC Performance<br>input offset voltage<br>average drift<br>input bias current<br>average drift<br>input offset current<br>power supply rejection ratio<br>open loop gain<br>quiescent current per amplifier | DC   | 1<br>TBD<br>-5<br>TBD<br>TBD<br>73<br>82<br>2.5      |           | mV<br>μV/°C<br>μA<br>nA/°C<br>μA<br>dB<br>dB<br>mA | 1<br>1<br>1<br>1<br>1<br>1<br>1 |
| Input Characteristics<br>input resistance<br>input capacitance<br>input common mode voltage range<br>common mode rejection ratio  | DC, $V_{cm} = 0V$ to $V_s - 1.5$   | TBD<br>TBD<br>-0.3 to 1.8<br>82                      |           | MΩ<br>pF<br>V<br>dB                                | 1                               |
| Output Characteristics<br>output voltage swing<br>linear output current<br>short circuit output current<br>power supply operating range   | $R_L = 2k\Omega \text{ to } V_s/2$<br>$R_L = 150\Omega \text{ to } V_s/2$  | 0.02 to 2.97<br>0.05 to 2.93<br>+99/-99<br>±130<br>3 | 2.5 to 12 | V<br>V<br>mA<br>MA<br>V                            | 1<br>1                          |

Min/max ratings are based on product characterization and simulation. Individual parameters are tested as noted. Outgoing quality levels are determined from tested parameters.

#### NOTES:

1) 100% tested at +25°C.

# **Electrical Characteristics** ( $V_s = +5V$ , G = 2, $R_L = 2k\Omega$ to $V_s/2$ ; unless noted)

| Parameters  | Conditions  | ТҮР   | Min & Max | UNITS  | NOTES                           |
|---|---|---|-----------|--|---------------------------------|
| Case Temperature  |   | +25°C   | +25°C     |  |                                 |
| Frequency Domain Response<br>-3dB bandwidth   |   | 325<br>122  |           | MHz<br>MHz   |                                 |
| full power bandwidth<br>gain bandwidth product  | $G = +2$ , $V_O = 2V_{pp}$  | 75<br>155   |           | MHz<br>MHz   |                                 |
| <b>Time Domain Response</b><br>rise and fall time<br>settling time to 0.1%<br>overshoot<br>slew rate  | 0.2V step<br>2V step<br>0.2V step,<br>5V step, G = -1   | 2.71<br>TBD<br>5.9<br>217                         |           | ns<br>ns<br>%<br>V/μs                              |                                 |
| Distortion and Noise Response<br>2nd harmonic distortion<br>3rd harmonic distortion<br>THD<br>input voltage noise<br>crosstalk  | 2V <sub>pp</sub> , 5MHz<br>2V <sub>pp</sub> , 5MHz<br>2V <sub>pp</sub> , 5MHz<br>>1MHz<br>10MHz | -76<br>-77<br>73<br>12.36<br>TBD                  |           | dBc<br>dBc<br>dB<br>nV/√Hz<br>dB                   |                                 |
| DC Performance<br>input offset voltage<br>average drift<br>input bias current<br>average drift<br>input offset current<br>power supply rejection ratio<br>open loop gain<br>quiescent current per amplifier | DC  | 1<br>TBD<br>-4.9<br>TBD<br>TBD<br>73<br>85<br>2.5 |           | mV<br>μV/°C<br>μA<br>nA/°C<br>μA<br>dB<br>dB<br>mA | 1<br>1<br>1<br>1<br>1<br>1<br>1 |
| Input Characteristics<br>input resistance<br>input capacitance<br>input common mode voltage range<br>common mode rejection ratio  | DC, $V_{cm} = 0V$ to $V_s - 1.5$  | TBD<br>TBD<br>-0.3 to 3.8<br>85                   |           | MΩ<br>pF<br>V<br>dB                                | 1                               |
| Output Characteristics<br>output voltage swing<br>linear output current   | $R_L = 2k\Omega \text{ to } V_s/2$<br>$R_L = 150\Omega \text{ to } V_s/2$                       | 0.02 to 4.96<br>0.07 to 4.89<br>+99/-99           |           | V<br>V<br>mA                                       | 1<br>1                          |
| short circuit output current power supply operating range   |   | ±130<br>5   | 2.5 to 12 | mA<br>V  |                                 |

Min/max ratings are based on product characterization and simulation. Individual parameters are tested as noted. Outgoing quality levels are determined from tested parameters.

#### NOTES:

1) 100% tested at +25°C.

## **Electrical Characteristics** ( $V_s = \pm 5V$ , G = 2, $R_L = 2k\Omega$ to $V_s/2$ ; unless noted)

| PARAMETERS  | CONDITIONS  | ТҮР  | MIN & MAX | UNITS  | NOTES                           |
|---|---|--|-----------|--|---------------------------------|
| Case Temperature  |   | +25°C  | +25°C     |  |                                 |
| Frequency Domain Response<br>-3dB bandwidth   |   | 358<br>123   |           | MHz<br>MHz   |                                 |
| full power bandwidth<br>gain bandwidth product  | $G = +2$ , $V_0^0 = 2V_{pp}^{pp}$   | 77<br>155  |           | MHz<br>MHz   |                                 |
| Time Domain Response<br>rise and fall time<br>settling time to 0.1%<br>overshoot<br>slew rate   | 0.2V step<br>1V step<br>0.2V step,<br>10V step, G = -1  | 2.7<br>TBD<br>3.8<br>217                           |           | ns<br>ns<br>%<br>V/μs                              |                                 |
| Distortion and Noise Response<br>2nd harmonic distortion<br>3rd harmonic distortion<br>THD<br>input voltage noise<br>crosstalk  | 2V <sub>pp</sub> , 5MHz<br>2V <sub>pp</sub> , 5MHz<br>2V <sub>pp</sub> , 5MHz<br>>1MHz<br>10MHz | -73<br>-77<br>72<br>12.29<br>TBD                   |           | dBc<br>dBc<br>dB<br>nV/√Hz<br>dB                   |                                 |
| DC Performance<br>input offset voltage<br>average drift<br>input bias current<br>average drift<br>input offset current<br>power supply rejection ratio<br>open loop gain<br>quiescent current per amplifier | DC  | -1<br>TBD<br>-4.5<br>TBD<br>TBD<br>73<br>92<br>2.5 |           | mV<br>μV/°C<br>μA<br>nA/°C<br>μA<br>dB<br>dB<br>mA | 1<br>1<br>1<br>1<br>1<br>1<br>1 |
| Input Characteristics<br>input resistance<br>input capacitance<br>input common mode voltage range<br>common mode rejection ratio  | DC, $V_{cm} = 0V$ to $V_s - 1.5$  | TBD<br>TBD<br>-5.3 to +3.8<br>92                   |           | MΩ<br>pF<br>V<br>dB                                | 1                               |
| Output Characteristics<br>output voltage swing<br>linear output current<br>short circuit output current   | $R_L = 2k\Omega$ to $V_s/2$<br>$R_L = 150\Omega$ to $V_s/2$                                     | -4.94 to 4.93<br>-4.9 to 4.81<br>+99/-99<br>±130   |           | V<br>V<br>mA                                       | 1<br>1                          |
| power supply operating range  |   | ±130<br>±5   | 2.5 to 12 | mA<br>V  |                                 |

Min/max ratings are based on product characterization and simulation. Individual parameters are tested as noted. Outgoing quality levels are determined from tested parameters.

#### NOTES:

1) 100% tested at +25°C.

### **Absolute Maximum Ratings**

### **Package Thermal Resistance**

| supply voltage                  | 0 to +12V                                    | Package     | $\theta_{JA}$ |  |
|---------------------------------|--|-------------|---------------|--|
| maximum junction temperatur     | e +175°C                                     | 8 lead SOIC | 152°C/W       |  |
| storage temperature range       | -65°C to +150°C                              | 8 lead MSOP | 206°C/W       |  |
| lead temperature (10 sec)       | +300°C                                       |             |               |  |
| operating temperature range (re | ecommended) -40°C to +85°C                   |             |               |  |
| input voltage range             | +V <sub>s</sub> +0.5V; -V <sub>s</sub> -0.5V |             |               |  |
| internal power dissipation      | see power derating curves                    |             |               |  |

### **Ordering Information**

| Model   | Part Number  | Package | Container | Pack Qty |
|---------|--------------|---------|-----------|----------|
| FAN4230 | FAN4230IMU8X | MSOP-8  | Reel      | 3000     |
| FAN4230 | FAN4230IM8X  | SOIC-8  | Reel      | 2500     |

Temperature range for all parts: -40°C to +85°C.

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