

CASCADABLE BROADBAND GaAs MMIC AMPLIFIER DC TO 12GHz

Typical Applications

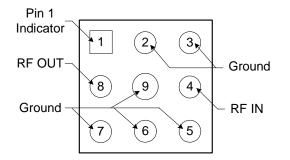
- Narrow and Broadband Commercial and Military Radio Designs
- Linear and Saturated Amplifiers

Product Description

The NBB-312 cascadable broadband InGaP/GaAs MMIC amplifier is a low-cost, high-performance solution for general purpose RF and microwave amplification needs. This 50Ω gain block is based on a reliable HBT proprietary MMIC design, providing unsurpassed performance for small-signal applications. Designed with an external bias resistor, the NBB-312 provides flexibility and stability. The NBB-310 is packaged in a low-cost, surface-mount ceramic package, providing ease of assembly for high-volume tape-and-reel requirements. It is available in either 1,000 or 3,000 piece-per-reel quantities. Connectorized evaluation board designs optimized for high frequency are also available for characterization purposes.

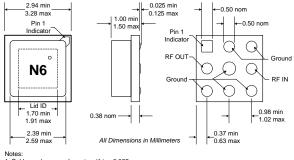
Optimum Technology Matching® Applied

🗌 Si BJT	🗌 GaAs HBT	GaAs MESFET
Si Bi-CMOS	SiGe HBT	Si CMOS
🗹 InGaP/HBT	GaN HEMT	SiGe Bi-CMOS



Functional Block Diagram

 Gain Stage or Driver Amplifiers for MWRadio/Optical Designs (PTP/PMP/ LMDS/UNII/VSAT/WLAN/Cellular/DWDM)



Notes:
 1. Solder pads are coplanar to within ±0.025 mm.
 2. Lid will be centered relative to frontside metallization with a tolerance of ±0.13 mm
 3. Mark to include two characters and dot to reference pin 1.

Package Style: MPGA, Bowtie, 3x3, Ceramic

Features

- Reliable, Low-Cost HBT Design
- 12.5dB Gain
- High P1dB of +15.8dBm at 6GHz
- Single Power Supply Operation
- 50Ω I/O Matched for High Frequency Use

Ordering Information

NBB-312	As MMIC Amplifier DC to	
NBB-312-T1 or -	T3Tape & Reel, 1000 or 3000	Pieces (respectively)
NBB-312-E	Fully Assembled Evaluation	n Board
NBB-X-K1 RF Micro Devices, 7628 Thorndike Re Greensboro, NC 2	oad	² Amp Designer's Tool Kit Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com

Absolute Maximum Ratings

Parameter	Rating	Unit			
RF Input Power	+20	dBm			
Power Dissipation	350	mW			
Device Current	70	mA			
Channel Temperature	200	°C			
Operating Temperature	-45 to +85	°C			
Storage Temperature	-65 to +150	°C			
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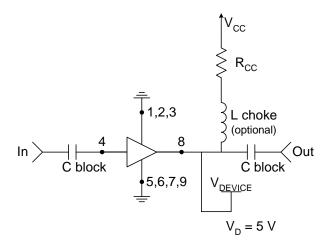
Exceeding any one or a combination of these limits may cause permanent damage.

Deremeter	Specification		11	Condition		
Parameter	Min.	Тур. М		Unit	Condition	
Overall					V _D =+5V, I _{CC} =50mA, Z ₀ =50Ω, T _A =+25°C	
Small Signal Power Gain, S21	12.5	12.9		dB	f=0.1GHz to 1.0GHz	
	12.0	12.9		dB	f=1.0GHz to 4.0GHz	
	11.4	11.7		dB	f=4.0GHz to 8.0GHz	
	9.0	9.7		dB	f=8.0GHz to 12.0GHz	
Gain Flatness, GF		+0.6		dB	f=0.1GHz to 8.0GHz	
Input VSWR		1.2:1			f=0.1GHz to 7.0GHz	
		1.65:1			f=7.0GHz to 10.0GHz	
		2.0:1			f=10.0GHz to 12.0GHz	
Output VSWR		1.5:1			f=0.1GHz to 12.0GHz	
Bandwidth, BW		11.0		GHz	BW3 (3dB)	
Output Power @						
-1dB Compression, P1dB		14.9		dBm	f=2.0GHz	
		15.8		dBm	f=6.0GHz	
		15.0		dBm	f=8.0GHz	
		12.0		dBm	f=12.0GHz	
Noise Figure, NF		4.9		dB	f=3.0GHz	
Third Order Intercept, IP3		+24.0		dBm	f=2.0GHz	
Reverse Isolation, S12		-15.6		dB	f=0.1GHz to 12.0GHz	
Device Voltage, V _D	4.7	5.0	5.3	V		
Gain Temperature Coefficient, ∂G _T /∂T		-0.0015		dB/°C		
MTTF versus Temperature						
@ I _{CC} =50mA						
Case Temperature		85		°C		
Junction Temperature		123		°C		
MTTF		>1,000,000		hours		
Thermal Resistance						
θ _{JC}		152		°C/W	$\frac{J_T - T_{CASE}}{V_D \cdot I_{CC}} = \theta_{JC}(°C/Watt)$	

Pin	Function	Description	Interface Schematic
1	GND	Ground connection. For best performance, keep traces physically short and connect immediately to ground plane.	
2	GND	Same as pin 1.	
3	GND	Same as pin 1.	
4	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	
5	GND	Same as pin 1.	
6	GND	Same as pin 1.	
7	GND	Same as pin 1.	
8	RF OUT	RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to V _{CC} . The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $R = \frac{(V_{CC} - V_{DEVICE})}{I_{CC}}$ Care should also be taken in the resistor selection to ensure that the current into the part never exceeds maximum datasheet operating current over the planned operating temperature. This means that a resistor between the supply and this pin is always required, even if a supply near 8.0V is available, to provide DC feedback to prevent thermal runaway. Alternatively, a constant current supply circuit may be implemented. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well	RF IN O
9	GND	bypassed. Same as pin 1.	

Typical Bias Configuration

Application notes related to biasing circuit, device footprint, and thermal considerations are available on request.



Recommended Bias Resistor Values					
Supply Voltage, V _{CC} (V)	8	10	12	15	20
Bias Resistor, R_{CC} (Ω)	60	100	140	200	300

Application Notes

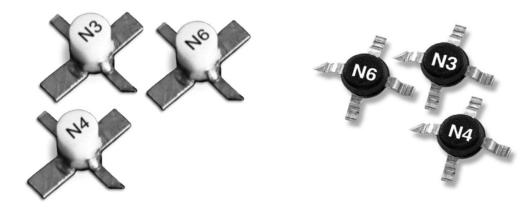
Bonding Temperature (Wedge or Ball)

It is recommended that the heater block temperature be set to $160^{\circ}C \pm 10^{\circ}C$.

Extended Frequency InGaP Amplifier Designer's Tool Kit NBB-X-K1

This tool kit was created to assist in the design-in of the RFMD NBB- and NLB-series InGap HBT gain block amplifiers. Each tool kit contains the following.

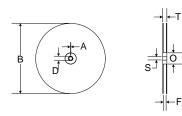
- 5 each NBB-300, NBB-310 and NBB-400 Ceramic Micro-X Amplifiers
- 5 each NLB-300, NLB-310 and NLB-400 Plastic Micro-X Amplifiers
- 2 Broadband Evaluation Boards and High Frequency SMA Connectors
- Broadband Bias Instructions and Specification Summary Index for ease of operation



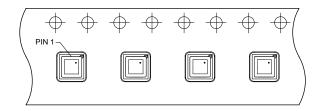
NBB-312

Tape and Reel Dimensions

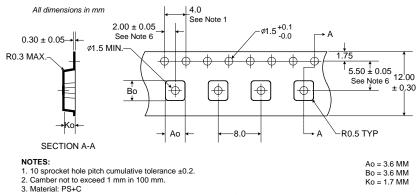
All Dimensions in Millimeters



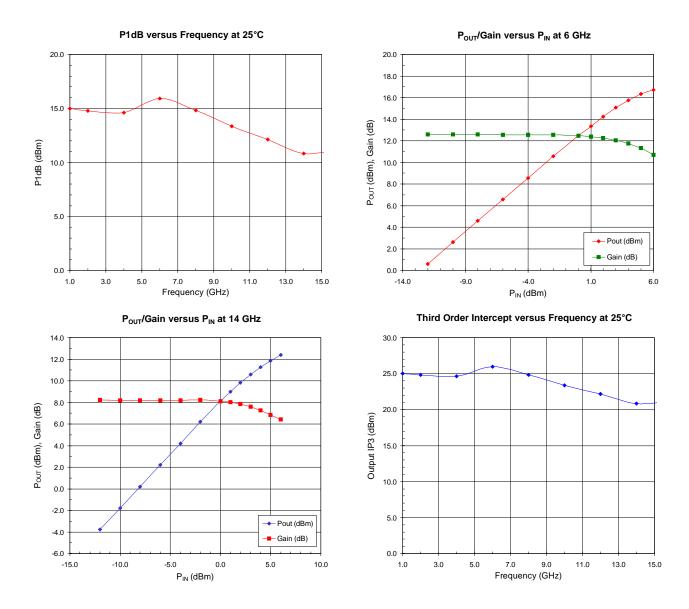
330 mm (13") REEL			Micro-X, MPGA		
	ITEMS	SYMBOL	SIZE (mm)	SIZE (inches)	
	Diameter	В	330 +0.25/-4.0	13.0 +0.079/-0.158	
FLANGE	Thickness	Т	18.4 MAX	0.724 MAX	
	Space Between Flange	F	12.4 +2.0	0.488 +0.08	
	Outer Diameter	0	102.0 REF	4.0 REF	
	Spindle Hole Diameter	S	13.0 +0.5/-0.2	0.512 +0.020/-0.008	
	Key Slit Width	A	1.5 MIN	0.059 MIN	
	Key Slit Diameter	D	20.2 MIN	0.795 MIN	



User Direction of Feed



A co and Bo measured on a plane 0.3 mm above the bottom of the pocket.
 Ko measured from a plane on the inside bottom of the pocket to the surface of the carrier.
 Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.



Rev A3 030912

Note: The s-parameter gain results shown below include device performance as well as evaluation board and connector loss variations. The insertion losses of the evaluation board and connectors are as follows:

1 GHz to 4GHz=-0.06dB 5GHz to 9GHz=-0.22dB 10GHz to 14GHz=-0.50dB 15GHz to 20GHz=-1.08dB

