

5GHz VCO



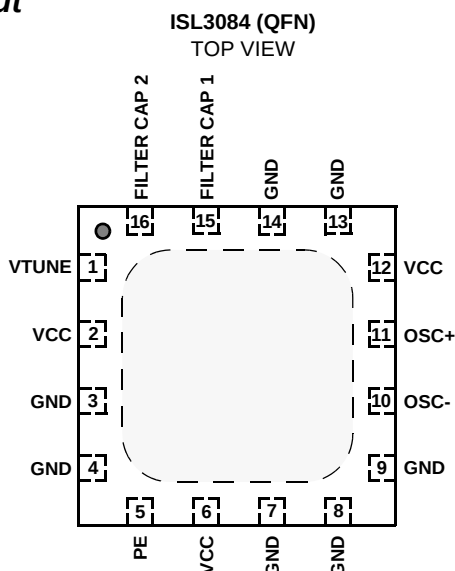
The ISL3084 is a 5GHz SiGe monolithic VCO circuit designed to simplify and reduce the cost and size of miniature wireless transceivers.

A fully integrated VCO requiring no external elements such as inductors and varactors greatly simplifies low cost local oscillator synthesized applications.

Included in this differential design is a low-power standby function and a stable process and temperature biasing operation.

The ISL3084 is housed in a 16 lead QFN package well-suited for PCMCIA and miniPCI board applications.

Pinout



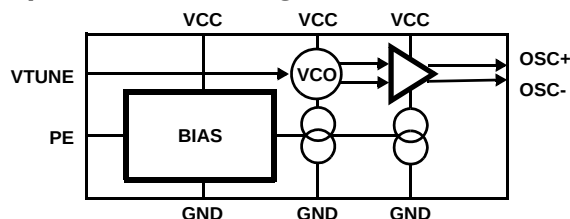
Features

- Single Supply 2.7V to 3.3V
- Frequency 4.9GHz typical
- Fully Integrated/No External Varactors or Resonators Required
- Isolation Output Buffer/Reduced Load Pulling
- Differential Design/Reduced Spurs
- Digitally Controlled Power Down Mode
- QFN Package:
 - Compliant to JEDEC PUB95 MO-220 QFN - Quad Flat No Leads - Package Outline
 - Near Chip Scale Package footprint, which improves PCB efficiency and has a thinner profile

Applications

- Systems Targeting IEEE802.11b, 11Mbps Standard
- Only required VCO function for Intersil PRISM®3 chip set
- WLAN Applications.
- PCMCIA Wireless Transceivers
- TDMA Packet Protocol Radios

Simplified Block Diagram



Ordering Information

PART NUMBER	TEMP. RANGE (°C)	PACKAGE	PKG. NO.
ISL3084IR	-40 to 85	16 Ld QFN	L16.4x4
ISL3084IR-TK	-40 to 85	Tape and Reel	

Pin Descriptions

PIN NUMBER	NAME	DESCRIPTION
1	VTUNE	Input tuning voltage from loop filter. Sensitivity to external injected noise to KVCO. Careful noise-free layout recommended.
2, 6, 12	VCC	Supply pins. Requires high quality capacitor RF decoupling. Phase noise behavior proportional to supply pushing specifications. Use good quality, low-frequency decoupling/filtering techniques.
5	PE	Power enable control pin: VCO enabled high.
10	OSC-	Differential VCO output. Terminate into 100Ω differential impedance. Terminate one end into 50Ω when single end output with equivalent loss of power.
11	OSC+	
15	FILTER CAP1	Use high quality .68μF filter capacitor to ground.
16	FILTER CAP2	Use high quality .68μF filter capacitor to VCC.
3, 4, 7, 8, 9, 13, 14	GND	Ground
Paddle	N/A	Floating paddle. Grounding is recommended.

Absolute Maximum Ratings

Supply Voltage. 3.6V
 Voltage on Any Other Pin -0.3 to $V_{CC} + 0.3V$
 V_{CC} to V_{CC} Decouple -0.3 to +0.3V
 Any GND to GND. -0.3 to +0.3V

Operating Conditions

Temperature Range. -40° to 85°C
 Supply Voltage Range. 2.7V to 3.3V

Thermal Information

Thermal Resistance (Typical, Note 1) θ_{JA} (°C/W)
 QFN Package. 47
 Junction Temperature (Plastic Package) 150°C
 Maximum Storage Temperature Range -65°C to 150°C
 For recommended soldering conditions see Tech Brief TB389.

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTE:

1. θ_{JA} is measured in free air with the component mounted on a high effective thermal conductivity test board with "direct attach" features. See Tech Brief TB379.

General DC Electrical Specifications $T_A = 25^\circ\text{C}$

PARAMETER	MIN	TYP	MAX	UNITS
Supply Voltage	2.7	-	3.0	V
Supply Current @ 3.3V	-	-	20	mA
Power Down Supply Current	-	-	100	μA
Power Up Time, Filtering dependent	-	50	-	μs
Power Down Time	-	-	1	μs
CMOS Low-Level Input Voltage	-	-	0.3* V_{CC}	V
CMOS High-Level Input Voltage	0.7* V_{CC}	-	-	V
CMOS High- or Low-Level Input Current	-10	-	10	μA

AC Electrical Specifications $V_{CC} = 2.7$ to 3.3V, V_{tune} operation from 0.5 to 2.2V, $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
RF Frequency Ranges	$V_{tune} = 0.5V$	-	4.67	4.79	GHz
	$V_{tune} = 1.35V$	-	4.92	-	GHz
	$V_{tune} = 2.2V$	5.0	5.19	-	GHz
Tuning Voltage Temperature Coefficient	$V_{tune} = 1.35V$, -40 to 85°C	-	40	-	MHz
Tuning Pin Input Leakage	$V_{tune} = 2.0V$	-	-	2	μA
VCO Gain	$V_{tune} = 1.35V$	250	300	350	MHz/V
VCO Gain Temperature Coefficient	$V_{tune} = 1.35V$, -40 to 85°C	-	20	-	MHz/V
Phase Noise	Offset 10kHz	65	70	-	dBc/Hz
	Offset 100kHz	-	95	-	dBc/Hz
Phase Noise Temperature Coefficient	Offset @ 10kHz, 0 to 85°C	-	1	4	dB
Integrated Phase Noise @ 5GHz.	10kHz to 1MHz	-	1.6		deg_rms
Output Power	Differential into 100 Ω	-9	-4	-	dBm
Supply Pushing	$V_{CC} = 2.7V$ –3.3V, across V_{tune} range	-3	+2	+10	MHz
Load Pulling	VSWR = 2:1		1.8		MHz
Output VSWR	2:1 BALUM	-	1.3:1		-

Typical Application

