

5V/3.3V HIGH-SPEED DIFFERENTIAL RECEIVER

FEATURES

- 3.3V and 5V power supply options
- Bandwidth –3dB >2.5GHz
- Internal 75kΩ input pulldown resistor
- Faster version of SY10EL16V
- 10K ECL compatible I/O
- Improved output waveform characteristics
- Available in 8-pin (3mm) MSOP and SOIC packages

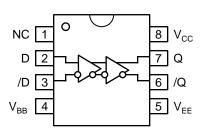
DESCRIPTION

The SY10EP16V is a high-speed differential receiver. The device is functionally equivalent to the EL16V devices, with higher performance capabilities. With output transition times significantly faster than the EL16V, the SY10EP16V is ideally suited for interfacing with highfrequency sources.

The SY10EP16V provides a V_{BB} output for either single-ended use or as a DC bias for AC coupling to the device. The V_{BB} pin should be used only as a bias for the SY10EP16V as its current sink/source capability is limited. Whenever used, the V_{BB} pin should be bypassed to ground via a 0.01 μ F capacitor.

Under open input conditions (pulled to V_{EE}), internal input clamps will force the Q output LOW.

PIN CONFIGURATION/BLOCK DIAGRAM



Available in 8-Pin SOIC and MSOP Packages

PIN NAMES

Pin	Function
D, /D	ECL Data Inputs
Q, /Q	ECL Data Outputs
V _{BB}	Reference Voltage Output
V _{CC}	Positive Power Supply
V _{EE}	Negative Power Supply

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Rating	Value	Unit
V _{EE}	Power Supply Voltage (V _{CC} = 0)	-6.0 to 0	Vdc
V _{CC}	Power Supply Voltage (V _{EE} = 0)	+6.0 to 0	Vdc
V _I	Input Voltage ($V_{CC} = 0V$, V_I not more negative than V_{EE}) Input Voltage ($V_{EE} = 0V$, V_I not more negative than V_{CC})	-6.0 to 0 +6.0 to 0	Vdc Vdc
I _{OUT}	Output Current –Continuous –Surge	50 100	mA
I _{BB}	V _{BB} Sink/Source Current ⁽²⁾ ±0.5	mA	
T _A	Operating Temperature Range	-40 to +85	٥C
T _{store}	Storage Temperature Range	–65 to +150	۵°

NOTE:

1. Permanent device damage may occur if ABSOLUTE MAXIMUM RATINGS are exceeded. This is a stress rating only and functional operation is not implied at conditions other than those detailed in the operational sections of this data sheet. Exposure to ABSOLUTE MAXIMUM RATING conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS⁽¹⁾

$\overline{V_{CC}} = 0V; V_{EE} = -5.5V \text{ to } -3.0V; V_{CC} = 3.0V \text{ to } 5.5V, V_{EE} = 0V^{(5)}$

		$T_A = -40^{\circ}C$		T _A = +25°C			T _A = +85°C		
Symbol	Parameter	Min.	Max.	Min.	Тур.	Max.	Min.	Max.	Unit
I _{EE}	Power Supply Current ⁽²⁾	—	42	—	_	42	_	42	mA
V _{OH}	Output HIGH Voltage ⁽³⁾	V _{CC} -1080	V _{CC} -0890	V _{CC} -0980		V _{CC} -0810	V _{CC} –0910	V _{CC} -0720	mV
V _{OL}	Outuput LOW Voltage ⁽³⁾	V _{CC} –1950	V _{CC} –1650	V _{CC} –1950		V _{CC} –1630	V _{CC} –1950	V _{CC} –1595	mV
V_{IH}	Input HIGH Voltage	V _{CC} -1230	V _{CC} -0890	V _{CC} –1130		V _{CC} -0810	V _{CC} –1060	V _{CC} -0720	mV
V _{IL}	Input LOW Voltage	V _{CC} –1950	V _{CC} –1500	V _{CC} –1950		V _{CC} –1480	V _{CC} –1950	V _{CC} –1445	mV
V _{BB}	Output Reference Voltage	V _{CC} –1430	V _{CC} -1300	V _{CC} –1350		V _{CC} –1250	V _{CC} –1310	V _{CC} –1190	mV
V _{IHCMR}	Input HIGH Voltage Common Mode Range ⁽⁴⁾	V _{EE} +2.0	V _{CC} -4.0	V _{EE} +2.0		V _{CC} -4.0	V _{EE} +2.0	V _{CC} -4.0	V
I _{IH}	Input HIGH Current	_	150	_		150	_	150	μΑ
I _{IL}	Input LOW Current	0.5	_	0.5		_	0.5	_	μΑ

NOTES:

1. 10EP circuits are designed to meet the DC specifications shown in the above table after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and traverse airflow greater than 500lfpm is maintained.

2. $V_{cc} = 0V$, $V_{ee} = V_{ee}$ (Min.) to V_{ee} (Max.), all other pins floating.

3. All loading with 50 Ω to V_{cc} – 2.0V.

4. V_{IHCMR} (Min.) varies 1:1 with V_{EE} , Max. varies 1:1 with V_{CC} .

5. Input and output parameters vary 1:1 with $\mathrm{V}_{\mathrm{cc}}.$

AC ELECTRICAL CHARACTERISTICS

 $\overline{V_{CC}} = 0V; V_{EE} = -5.5V \text{ to } -3.0V; V_{CC} = 3.0V \text{ to } 5.5V, V_{EE} = 0V$

		Г	T _A = −40°C		T _A = +25°C			T _A = +85°C	
Symbol	Parameter	Mir	. Max.	Min.	Тур.	Max.	Min.	Max.	Unit
f _{MAX}	Maximum Toggle Frequency ⁽¹⁾		—	—	3	—	—	—	GHz
t _{PLH} t _{PHL}	Propagation Delay toOutput Differential (2) $D \rightarrow Q$,	/Q 100	300	150	200	250	200	300	ps
t _{SKEW}	Device Skew	_	—	—	5.0	20	—	20	ps
t _r t _f	Output Rise/Fall Times (20% to 80%)	Q 60	180	60	110	180	70	180	ps

NOTES:

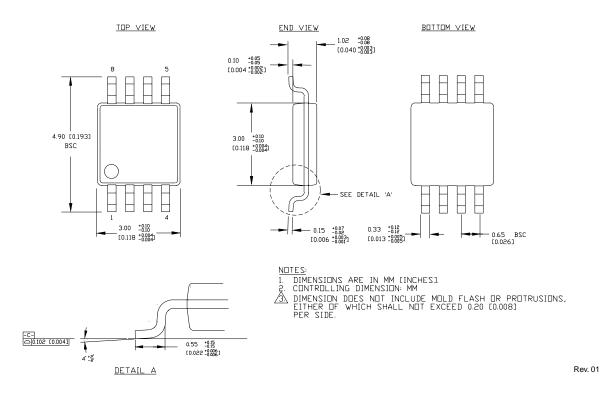
1. $\rm f_{MAX}$ guaranteed for functionality only. $\rm V_{OL}$ and $\rm V_{OH}$ levels are guaranteed at DC only.

2. V_{IN} = 800mV

PRODUCT ORDERING INFORMATION

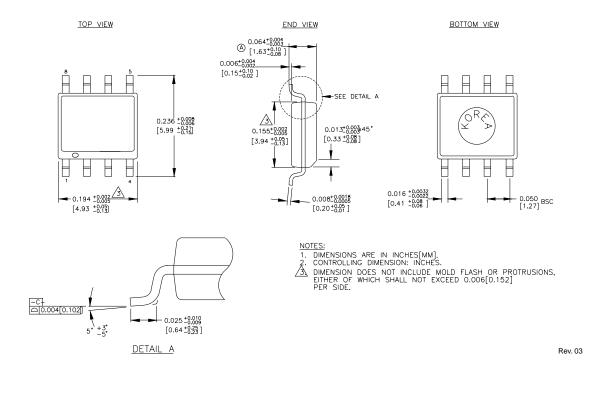
Ordering Code	Package Type	Operating Range	Package Marking
SY10EP16VZC	Z8-1	Commercial	HP16
SY10EP16VZCTR	Z8-1	Commercial	HP16
SY10EP16VKC	K8-1	Commercial	HEP16
SY10EP16VKCTR	K8-1	Commercial	HEP16

8 LEAD MSOP (K8-1)



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8 LEAD PLASTIC SOIC (Z8-1)



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