

16,384 WORD × 4 BIT CMOS STATIC RAM

DESCRIPTION

The TC55417PJ-H is a 65,536 bit high speed static random access memory organized as 16,384 words by 4 bits using CMOS technology, and operated from a single 5-volt supply. Toshiba's high performance device technology provides both high speed and low power features with a maximum access time of 15ns / 20ns / 25ns / 35ns and maximum operating current of 120mA / 100mA / 100mA / 80mA at minimum cycle time.

The TC55417PJ-H also features an automatic stand-by mode. When deselected by Chip Enable (CE), the operating current is reduced to 1mA.

The TC55417PJ-H is suitable for use in cache memory and high speed storage, where high speed / high density are required.

The TC55417PJ-H is packaged in a 24 pin standard plastic DIP and a 24 pin plastic SOJ, with 0.3 inch width for high density assembly.

The TC55417PJ-H is fabricated with ion implanted CMOS silicon gate MOS technology for high performance and high reliability.

FEATURES

- Fast access time :

TC55417PJ-15H	15ns(MAX.)
TC55417PJ-20H	20ns(MAX.)
TC55417PJ-25H	25ns(MAX.)
TC55417PJ-35H	35ns(MAX.)

- Low power dissipation :

Operation	TC55417PJ-15H	120mA(MAX.)
Standby	TC55417PJ-20H	100mA(MAX.)
Standby	TC55417PJ-25H	100mA(MAX.)
Standby	TC55417PJ-35H	80mA(MAX.)

- 5V single power supply : 5V±10%

- Output buffer control : OE

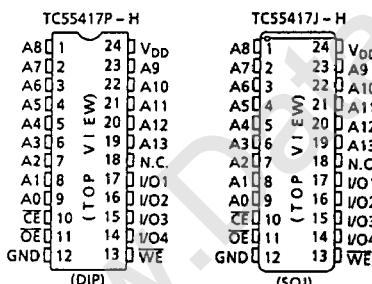
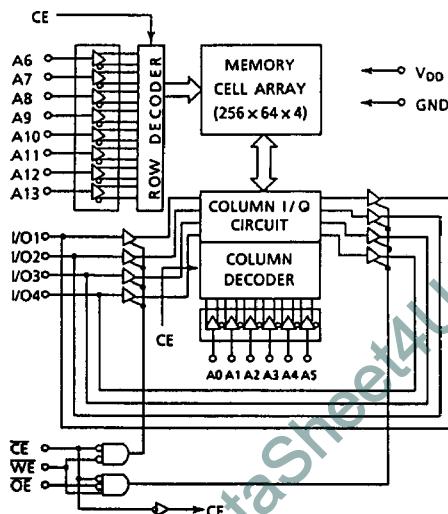
- Fully static operation

- Package TC55417P-H : DIP24-P-300B

- Directly TTL compatible :

All Input and Output

TC55417J-H : SOJ24-P-300A

PIN CONNECTION**BLOCK DIAGRAM****PIN NAMES**

A0 ~ A13	Address Inputs
I/O1 ~ I/O4	Data Input/Output
CE	Chip Enable Input
WE	Write Enable Input
OE	Output Enable Input
V _{DD}	Power (+ 5V)
GND	Ground
N.C.	No Connection

TC55417P/J-15H, TC55417P/J-20H TC55417P/J-25H, TC55417P/J-35H

MAXIMUM RATINGS

SYMBOL	ITEM	RATING	UNITS
V_{DD}	Power Supply Voltage	-0.5~7.0	V
V_{IN}	Input Voltage	-2.0~7.0	V
V_{OUT}	Output Voltage	-0.5~ $V_{DD} + 0.5$	V
P_D	Power Dissipation	650	mW
T_{solder}	Soldering Temperature · Time	260 · 10	°C · sec
T_{strg}	Storage Temperature	-65~150	°C
T_{opr}	Operating Temperature	-10~85	°C

DC RECOMMENDED OPERATING CONDITIONS (Ta = 0~70°C)

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
V_{DD}	Power Supply Voltage	4.5	5.0	5.5	V
V_{IH}	Input High Voltage	2.2	-	$V_{DD} + 0.5$	V
V_{IL}	Input Low Voltage	* -3.0	-	0.8	V

* Pulse width ≤ 10ns, DC : -0.5V (min)

DC CHARACTERISTICS (Ta = 0~70°C, $V_{DD} = 5V \pm 10\%$)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
I_{IL}	Input Leakage Current	$V_{IN} = 0~V_{DD}$	-	-	± 1	µA
I_{OH}	Output High Current	$V_{OH} = 2.4V$	-4	-	-	mA
I_{OL}	Output Low Current	$V_{OL} = 0.4V$	8	-	-	mA
I_{LO}	Output Leakage Current	$\overline{CE} = V_{IH}$ or $\overline{OE} = V_{IH}$ or $\overline{WE} = V_{IL}$ $V_{OUT} = 0~V_{DD}$	-	-	± 1	µA
I_{DDO}	Operating Current	$V_{DD} = 5.5V$, tcycle = Min cycle	-15H	-	120	mA
		$\overline{CE} = V_{IL}$, $I_{OUT} = 0mA$	-20H	-	100	
		$\overline{CE} = V_{DD} - 0.2V$	-25H	-	100	
		Other Input = V_{IH}/V_{IL}	-35H	-	80	
I_{DDS1}	Standby Current	$V_{DD} = 5.5V$, tcycle = Min cycle $\overline{CE} = V_{IH}$, Other Input = V_{IH}/V_{IL}	-	-	25	mA
I_{DDS2}		$\overline{CE} = V_{DD} - 0.2V$ Other Input = $V_{DD} - 0.2V$ or 0.2V	-	-	1	

CAPACITANCE (Ta = 25°C)

SYMBOL	PARAMETER	TEST CONDITION	MAX.	UNIT
C_{IN}	Input Capacitance	$V_{IN} = GND$	5	pF
C_{OUT}	Output Capacitance	$V_{OUT} = GND$	7	pF

Note : This parameter periodically sampled is not 100% tested.

TC55417P/J-15H, TC55417P/J-20H TC55417P/J-25H, TC55417P/J-35H

AC CHARACTERISTICS (Ta = 0~70°C⁽⁴⁾, V_{DD} = 5V ± 10%)

READ CYCLE

SYMBOL	PARAMETER	TC55417P/J-15H		TC55417P/J-20H		TC55417P/J-25H		TC55417P/J-35H		UNIT
		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
t _{RC}	Read Cycle Time	15	-	20	-	25	-	35	-	ns
t _{ACC}	Address Access Time	-	15	-	20	-	25	-	35	ns
t _{CO}	Chip Enable Access Time	-	15	-	20	-	25	-	35	ns
t _{OE}	Output Enable to Output Valid	-	9	-	10	-	10	-	10	ns
t _{COE}	Output Enable Time from CE	5	-	5	-	5	-	5	-	ns
t _{COD}	Output Disable Time from CE	-	6	-	6	-	6	-	6	ns
t _{OEE}	Output Enable Time from OE	0	-	0	-	0	-	0	-	ns
t _{ODO}	Output Disable Time from OE	-	5	-	5	-	5	-	5	ns
t _{OH}	Output Data Hold Time	5	-	5	-	5	-	5	-	ns
t _{PU}	Power Up Time	0	-	0	-	0	-	0	-	ns
t _{PD}	Power Down Time	-	15	-	20	-	25	-	35	ns

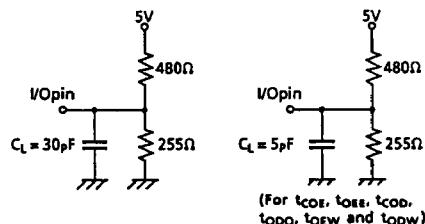
WRITE CYCLE

SYMBOL	PARAMETER	TC55417P/J-15H		TC55417P/J-20H		TC55417P/J-25H		TC55417P/J-35H		UNIT
		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
t _{WC}	Write Cycle Time	15	-	20	-	25	-	35	-	ns
t _{WP}	Write Pulse Width	12	-	13	-	13	-	13	-	ns
t _{CW}	Chip Enable to End of Write	12	-	13	-	13	-	13	-	ns
t _{AS}	Address Set Up Time	0	-	0	-	0	-	0	-	ns
t _{WR}	Write Recovery Time	0	-	0	-	0	-	0	-	ns
t _{OEW}	Output Enable Time from WE	0	-	0	-	0	-	0	-	ns
t _{ODW}	Output Disable Time from WE	-	6	-	6	-	6	-	6	ns
t _{DS}	Data Set Up Time	9	-	10	-	10	-	10	-	ns
t _{DH}	Data Hold Time	0	-	0	-	0	-	0	-	ns

AC TEST CONDITIONS

Input Pulse Levels	3.0V/0.0V
Input Rise and Fall Time	3ns
Input Timing Measurement Reference Levels	2.2V/0.8V
Output Timing Measurement Reference Levels	2.0V/0.8V
Output Load	See Fig. 1

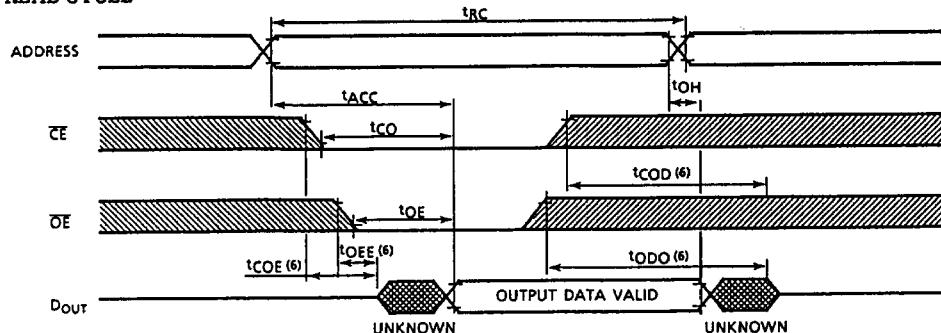
Fig. 1



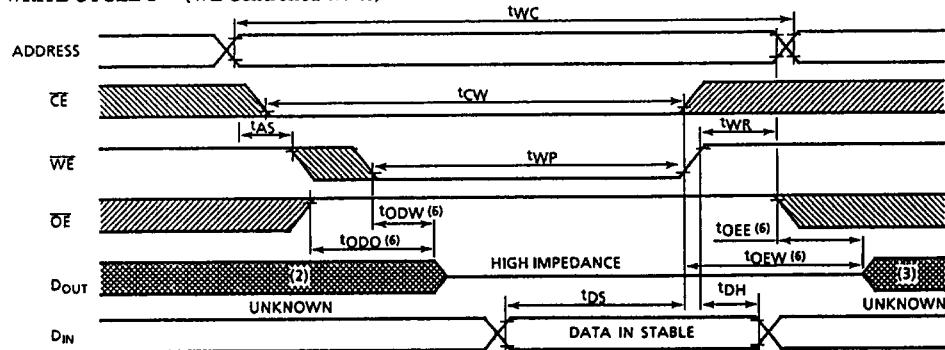
TC55417P/J-15H, TC55417P/J-20H TC55417P/J-25H, TC55417P/J-35H

TIMING WAVEFORMS

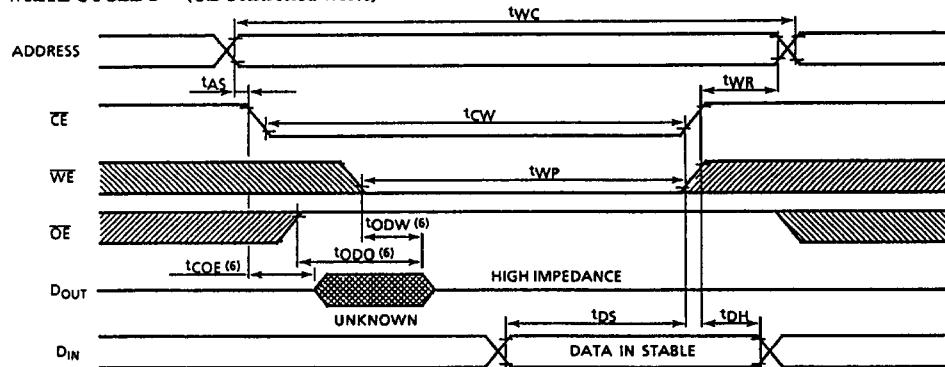
READ CYCLE⁽⁴⁾



WRITE CYCLE 1⁽⁵⁾ (WE Controlled Write)

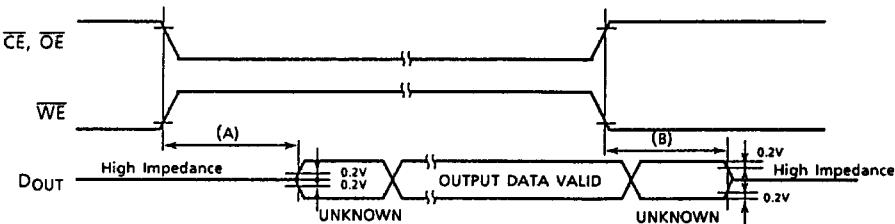


WRITE CYCLE 2⁽⁵⁾ (CE Controlled Write)



TC55417P/J-15H, TC55417P/J-20H TC55417P/J-25H, TC55417P/J-35H

- Note:
1. \overline{WE} is High for Read Cycle.
 2. Assuming that \overline{CE} Low transition occurs coincident with or after \overline{WE} Low transition, outputs remain in a high impedance state.
 3. Assuming that \overline{CE} High transition occurs coincident with or prior to \overline{WE} High transition, outputs remain in a high impedance state.
 4. The Operating temperature (T_a) is guaranteed with transverse air flow exceeding 400 linear feet per minute.
 5. The \overline{OE} input can be held on low (V_{IL}) in write cycle.
 6. These parameters are specified as follows and measured by using the load shown in Fig.1.
 - (A) $t_{COE}, t_{OEE}, t_{OEW}$ Output Enable Time
 - (B) $t_{COD}, t_{ODO}, t_{ODW}$ Output Disable Time

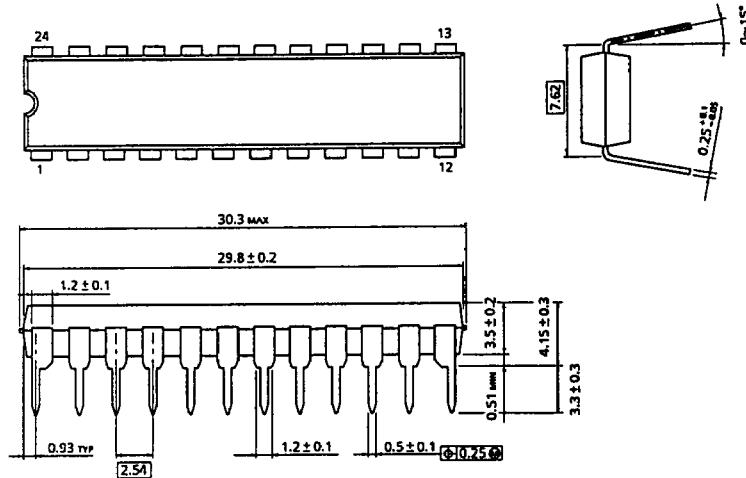


TC55417P/J-15H, TC55417P/J-20H TC55417P/J-25H, TC55417P/J-35H

OUTLINE DRAWINGS

Plastic DIP (DIP-24-300B)

Unit in mm



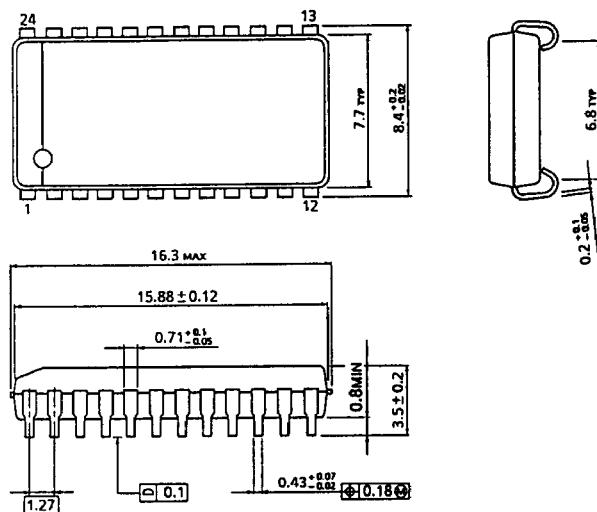
Weight : 1.72 g (TYP)

**TC55417P/J-15H, TC55417P/J-20H
TC55417P/J-25H, TC55417P/J-35H**

OUTLINE DRAWINGS

Plastic SOJ (SOJ24-P-300A)

Unit in mm



Weight : 0.72 g (TYP)