Silicon N Channel Power MOS FET High Speed Power Switching

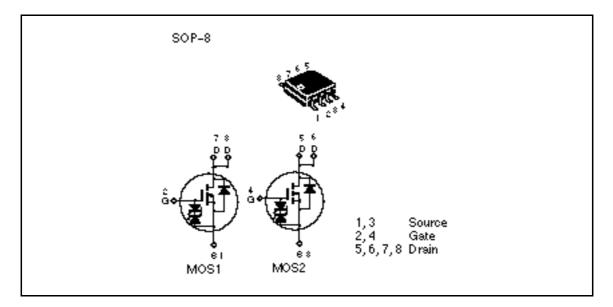


ADE-208-525 C 4th. Edition

Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	28	V	
Gate to source voltage	V _{GSS}	±12	V	
Drain current	l _D	7.5	А	
Drain peak current	I D(pulse) * 1	60	А	
Body to drain diode reverse drain current	I _{DR}	7.5	A	
Channel dissipation	Pch*2	2	W	
Channel dissipation	Pch* ³	3	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW 10µs, duty cycle 1 %

2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW 10s

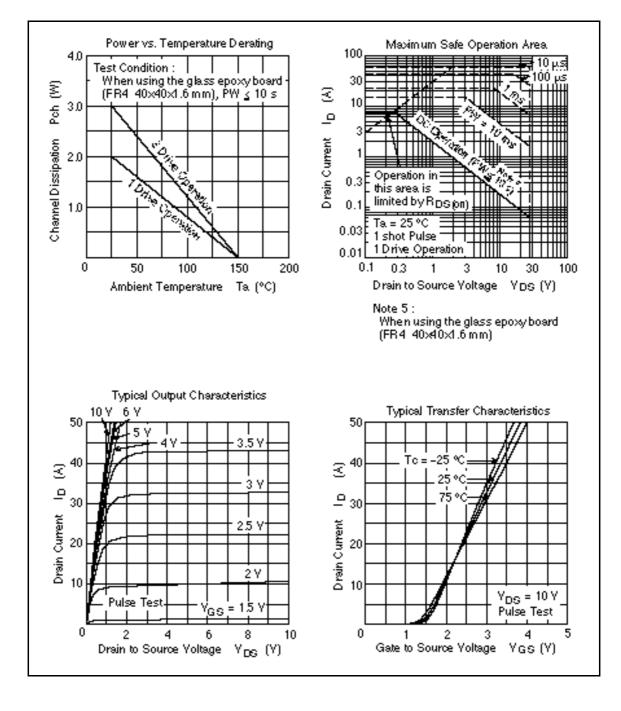
3. 2 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW $\,$ 10s $\,$

Electrical Characteristics (Ta = 25°C)

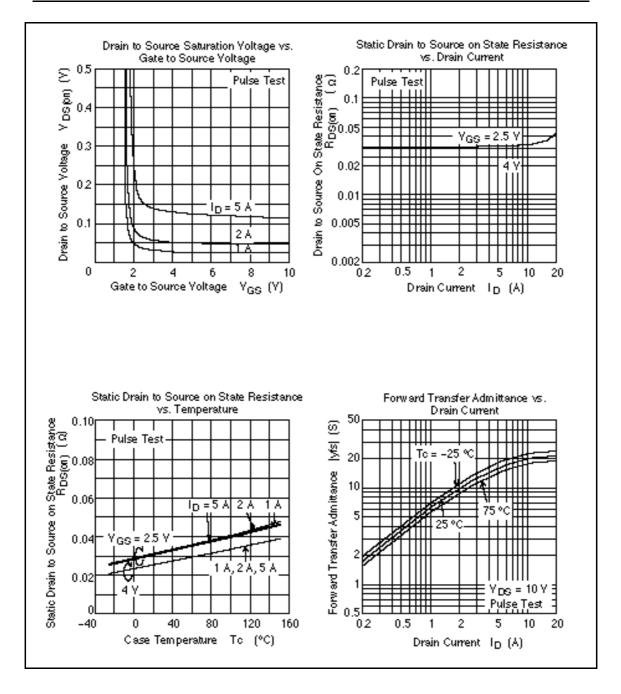
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	28	_	_	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	±12	_	_	V	$I_{g} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	—		±10	μA	$V_{\text{GS}} = \pm 10 V, \ V_{\text{DS}} = 0$
Zero gate voltege drain current	I _{DSS}		_	1	μA	$V_{\rm DS} = 28 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	0.4	_	1.4	V	$V_{DS} = 10V, I_{D} = 1mA$
Static drain to source on state	$R_{\text{DS(on)}}$	_	0.025	0.033		$I_{\rm D} = 4A, V_{\rm GS} = 4V^{*1}$
resistance	R _{DS(on)}	_	0.031	0.043		$I_{\rm D} = 4A, V_{\rm GS} = 2.5V^{*1}$
Forward transfer admittance	y _{fs}	9.5	15	—	S	$I_{\rm D} = 4A, V_{\rm DS} = 10V^{*1}$
Input capacitance	Ciss		780	_	рF	$V_{DS} = 10V$
Output capacitance	Coss		470	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		190	_	рF	f = 1MHz
Turn-on delay time	t _{d(on)}		20	_	ns	$V_{GS} = 4V, I_D = 4A$
Rise time	t,	_	170	_	ns	V _{DD} 10V
Turn-off delay time	t _{d(off)}	_	140	_	ns	—
Fall time	t _f	_	170	_	ns	_
Body to drain diode forward voltage	V_{DF}	—	0.88	1.15	V	$IF = 7.5A, V_{GS} = 0^{*1}$
Body to drain diode reverse recovery time	t _{rr}	—	65	—	ns	IF = 7.5A, V _{GS} = 0 diF/ dt =20A/µs
Noto: 1 Dulas test						

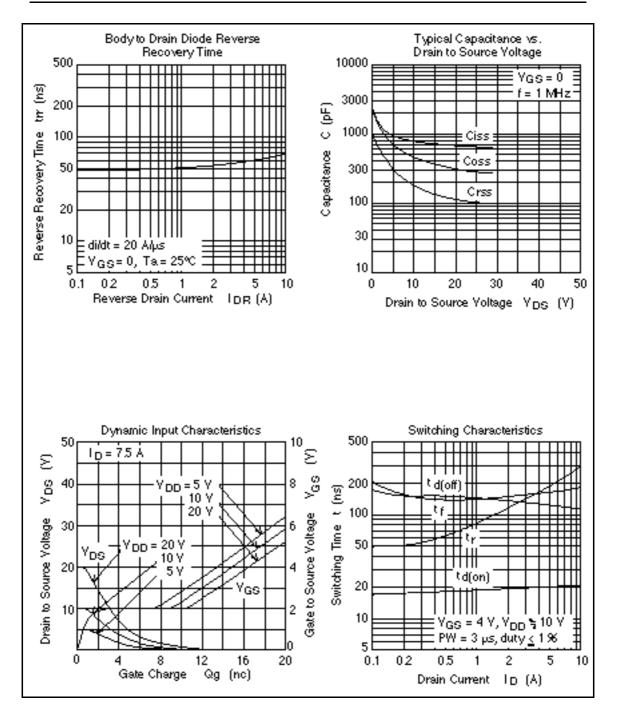
Note: 1. Pulse test

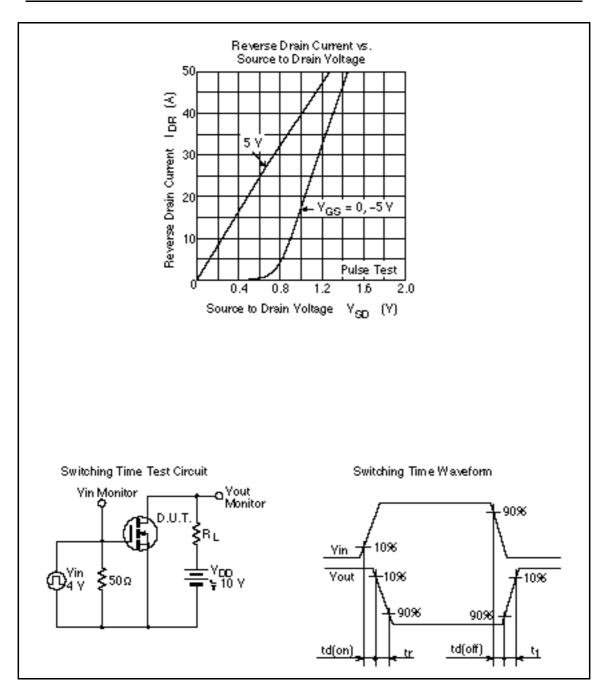
Main Characteristics

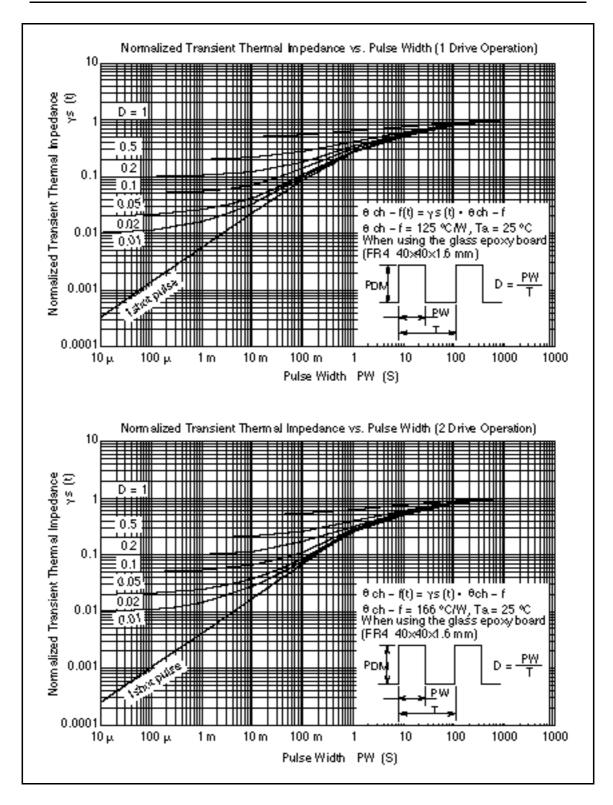


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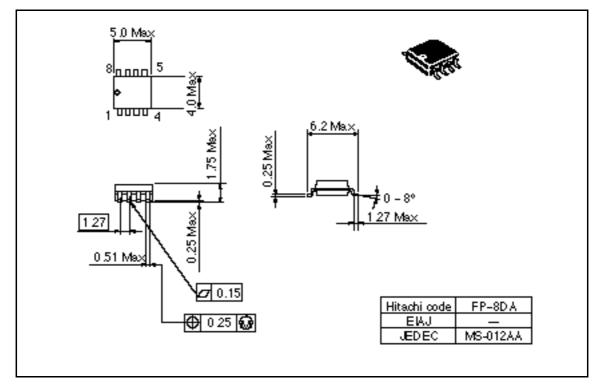






Package Dimensions





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Hitachi, Ltd.

Semiconductor & IC DW. Nippon Bidg, 2-5-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tet Tokyo (03, 3270-2111 Fax: (03, 3270-5109

For Turther in forms for write to : Hitschi America, Ud Semiconductor & IC Div. 2000 Sierre Point Perlavay Briebene, CA. 94005-4835 U SA Tet 445-589-8300 Fax: 445-589-8300

Hitschi Burope GmbH Bedronic Components Group Cohlinertal Burope Dornecher Straße 3 Destigz Feldkirchen München Tet (1994) 94 80.0 Fex (1994) 29 30 00 Hitschi Burope Ltd. Bectronic Components Div. Northern Burope Hesdquertere Whitebrock Ferk Lower Cookhem Road Neiderheed Berkshire SL63YA Urited Kingdom Tet 0628-585000 Fex 0628-778322 Hitschi Asia Pta. Ltd +5 Collyer Guay #20-00 Hitschi Tower Snappore 0404 Tet 535-2400 Fex: 535-4533

Hitschi Asia (Hong Kong) Ltd. Unit 705, North Towar, World Finance Cantre, Herbour City, Carton Road Taim She Tou, Kowloon Hang Kong Teit 27350218 Fex: 27306074

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