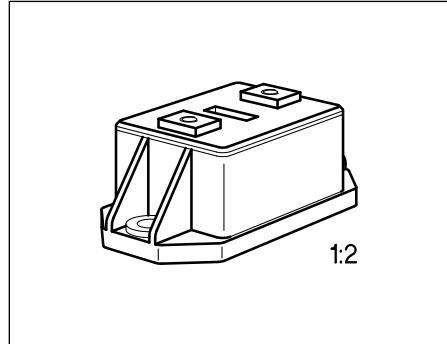


V_{DS} = 100 V

I_D = 200 A

$R_{DS(on)}$ = 8.5 mΩ

- Power module
- Single switch
- N channel
- Enhancement mode
- Package with insulated metal base plate
- Package outline/Circuit diagram: 1¹⁾



Type	Ordering Code
BSM 111 AR	C67076-S1013-A2

Maximum Ratings

Parameter	Symbol	Values	Unit
Drain-source voltage	V_{DS}	100	V
Drain-gate voltage, $R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	100	
Gate-source voltage	V_{GS}	± 20	
Continuous drain current, $T_C = 25^\circ\text{C}$	I_D	200	A
Pulsed drain current, $T_C = 25^\circ\text{C}$	$I_{D \text{ puls}}$	600	
Operating and storage temperature range	T_j, T_{stg}	- 55 ... + 150	°C
Power dissipation, $T_C = 25^\circ\text{C}$	P_{tot}	700	W
Thermal resistance Chip-case	R_{thJC}	≤ 0.18	K/W
Insulation test voltage ²⁾ , $t = 1 \text{ min.}$	V_{is}	2500	V_{ac}
Creepage distance, drain-source	-	16	mm
Clearance, drain-source	-	11	
DIN humidity category, DIN 40 040	-	F	-
IEC climatic category, DIN IEC 68-1	-	55/150/56	

¹⁾ See chapter Package Outline and Circuit Diagrams.

²⁾ Insulation test voltage between drain and base plate referred to standard climate 23/50 in acc. with DIN 50 014, IEC 146, para. 492.1.

Electrical Characteristicsat $T_j = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Drain-source breakdown voltage $V_{GS} = 0$, $I_D = 0.25 \text{ mA}$	$V_{(\text{BR})DSS}$	100	—	—	V
Gate threshold voltage $V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	$V_{GS(\text{th})}$	2.1	3.0	4.0	
Zero gate voltage drain current $V_{DS} = 100 \text{ V}$, $V_{GS} = 0$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	I_{DSS}	—	50	250	μA
—	—	—	300	1000	
Gate-source leakage current $V_{GS} = 20 \text{ V}$, $V_{DS} = 0$	I_{GS}	—	10	100	nA
Drain-source on-state resistance $V_{GS} = 10 \text{ V}$, $I_D = 130 \text{ A}$	$R_{DS(\text{on})}$	—	7	8.5	$\text{m}\Omega$

Dynamic Characteristics

Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS(\text{on}) \text{ max.}}$, $I_D = 130 \text{ A}$	g_{fs}	60	75	—	S
Input capacitance $V_{GS} = 0$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{iss}	—	10	13	nF
Output capacitance $V_{GS} = 0$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{oss}	—	5	7.5	
Reverse transfer capacitance $V_{GS} = 0$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{rss}	—	1.8	2.7	
Turn-on time t_{on} ($t_{\text{on}} = t_{d(\text{on})} + t_r$) $V_{CC} = 50 \text{ V}$, $V_{GS} = 10 \text{ V}$ $I_D = 130 \text{ A}$, $R_{GS} = 3.3 \Omega$	$t_{d(\text{on})}$	—	280	—	ns
	t_r	—	220	—	
Turn-off time t_{off} ($t_{\text{off}} = t_{d(\text{off})} + t_f$) $V_{CC} = 50 \text{ V}$, $V_{GS} = 10 \text{ V}$ $I_D = 130 \text{ A}$, $R_{GS} = 3.3 \Omega$	$t_{d(\text{off})}$	—	220	—	
	t_f	—	60	—	

Electrical Characteristics (cont'd)at $T_j = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

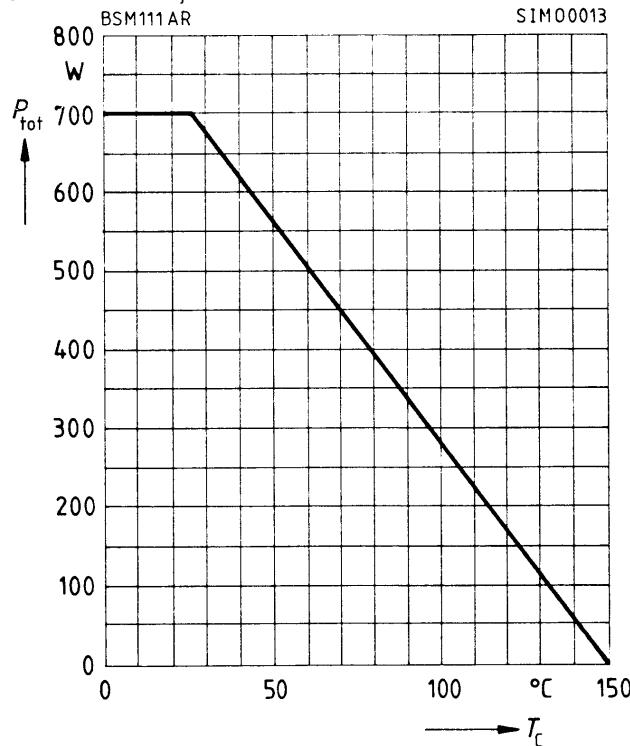
Reverse diode

Continuous reverse drain current $T_C = 25^\circ\text{C}$	I_S	—	—	200	A
Pulsed reverse drain current $T_C = 25^\circ\text{C}$	I_{SM}	—	—	600	
Diode forward on-voltage $I_F = 400 \text{ A}$, $V_{GS} = 0$	V_{SD}	—	1.25	1.6	V
Reverse recovery time $I_F = I_S$, $di_F/dt = 100 \text{ A}/\mu\text{s}$, $V_R = 30 \text{ V}$	t_{rr}	—	400	—	ns
Reverse recovery charge $I_F = I_S$, $di_F/dt = 100 \text{ A}/\mu\text{s}$, $V_R = 30 \text{ V}$	Q_{rr}	—	3.5	—	μC

Characteristics at $T_j = 25^\circ\text{C}$, unless otherwise specified.

Power dissipation $P_{\text{tot}} = f(T_C)$

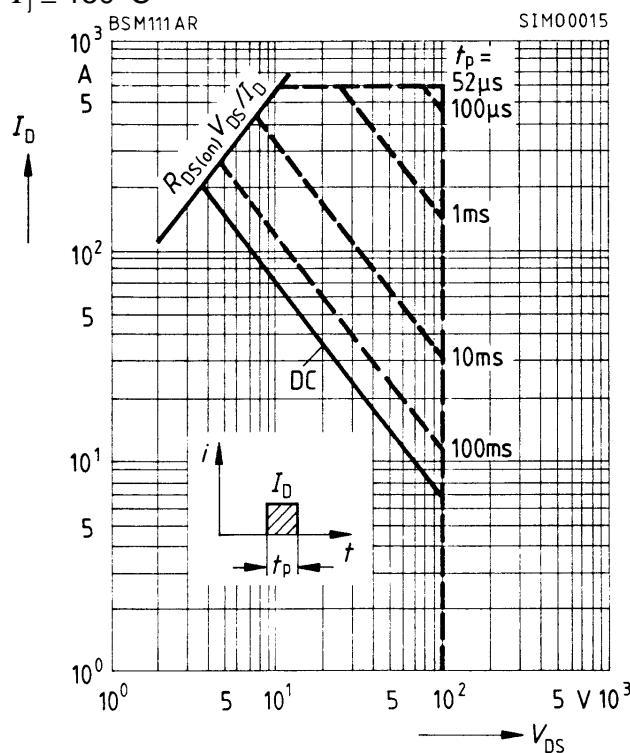
parameter: $T_j = 150^\circ\text{C}$



Safe operating area $I_D = f(V_{DS})$

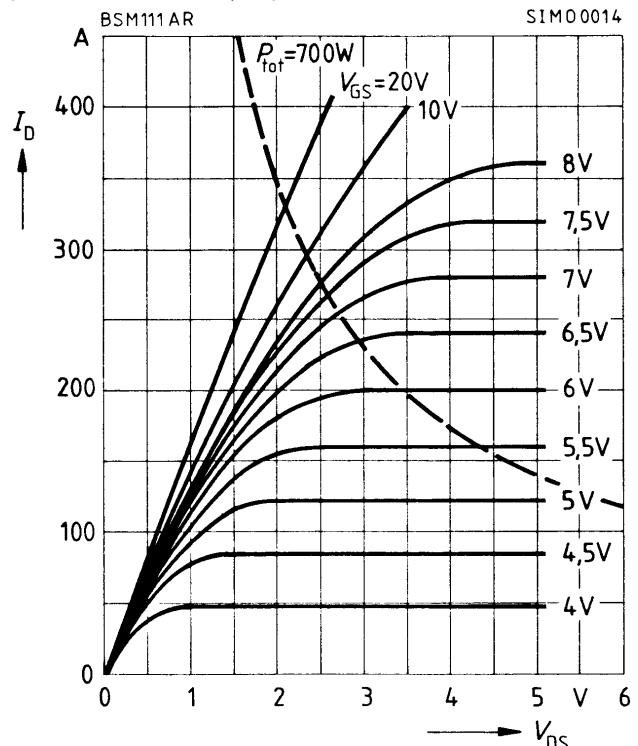
parameter: single pulse, $T_C = 25^\circ\text{C}$

$T_j \leq 150^\circ\text{C}$



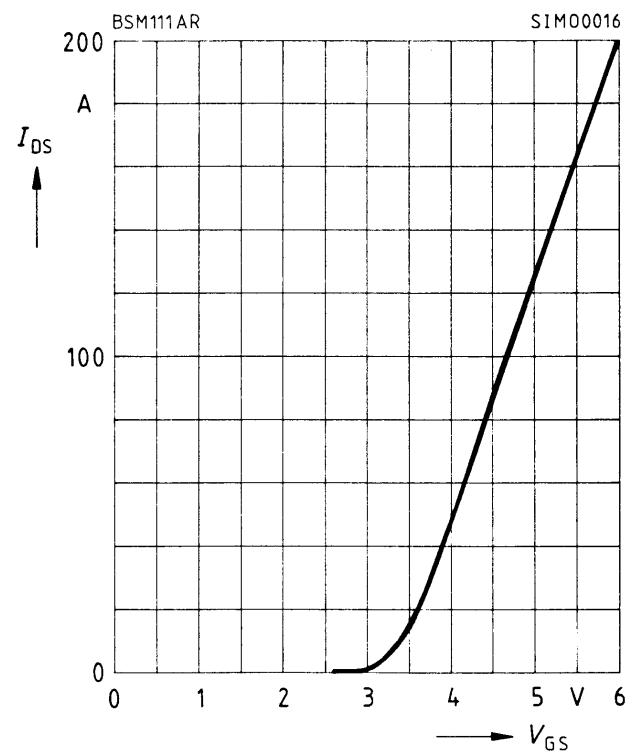
Typ. output characteristics $I_D = f(V_{DS})$

parameter: = 80 µs pulse test



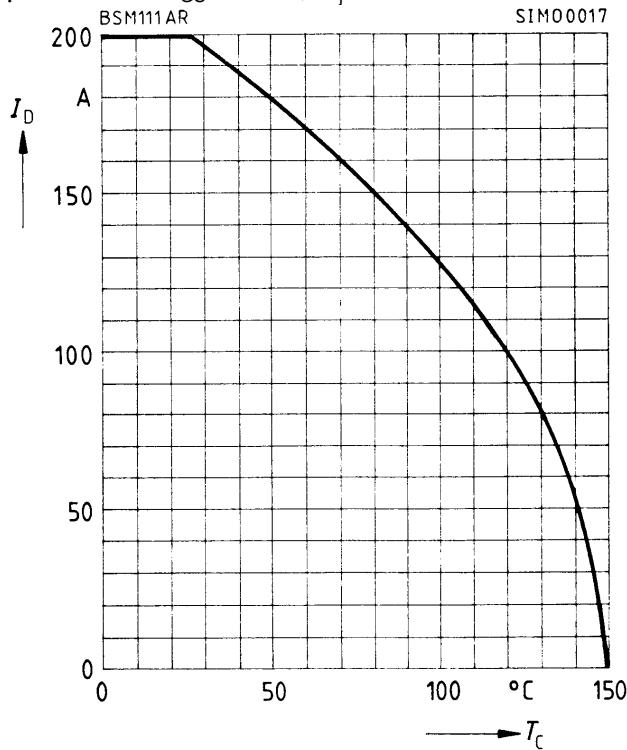
Typ. transfer characteristic $I_D = f(V_{GS})$

parameter: = 80 µs pulse test, $V_{DS} = 25\text{V}$



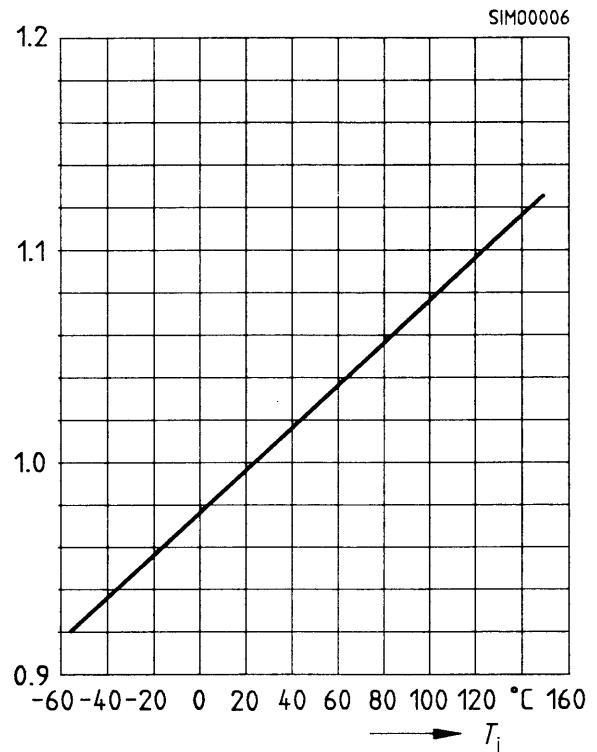
Continuous drain-source current

$I_D = f(T_C)$,
parameter: $V_{GS} \geq 10$ V, $T_j = 150$ °C



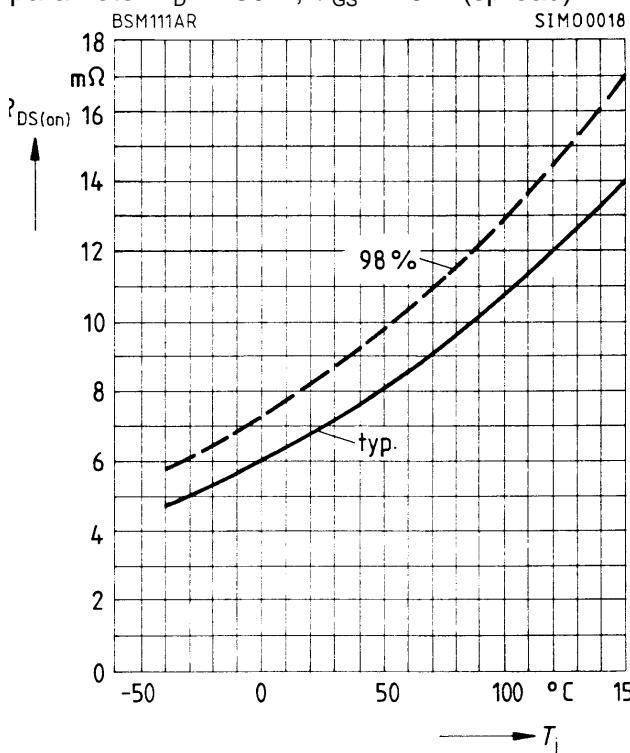
Drain-source breakdown voltage

$V_{(BR)DSS}(T_j) = b \times V_{(BR)DSS}(25\text{ }^{\circ}\text{C})$



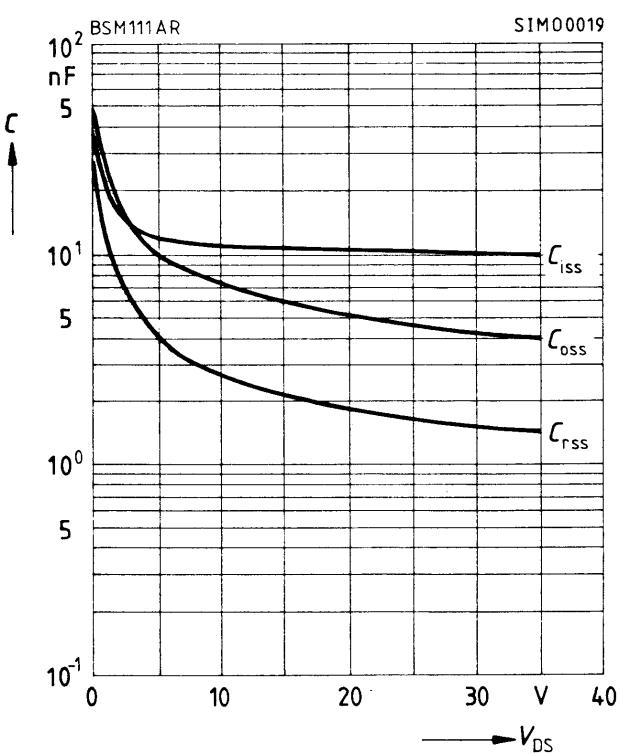
Drain source on-state resistance

$R_{DS(on)} = f(T_j)$
parameter: $I_D = 130$ A; $V_{GS} = 10$ V (spread)



Typical capacitances $C = f(V_{DS})$

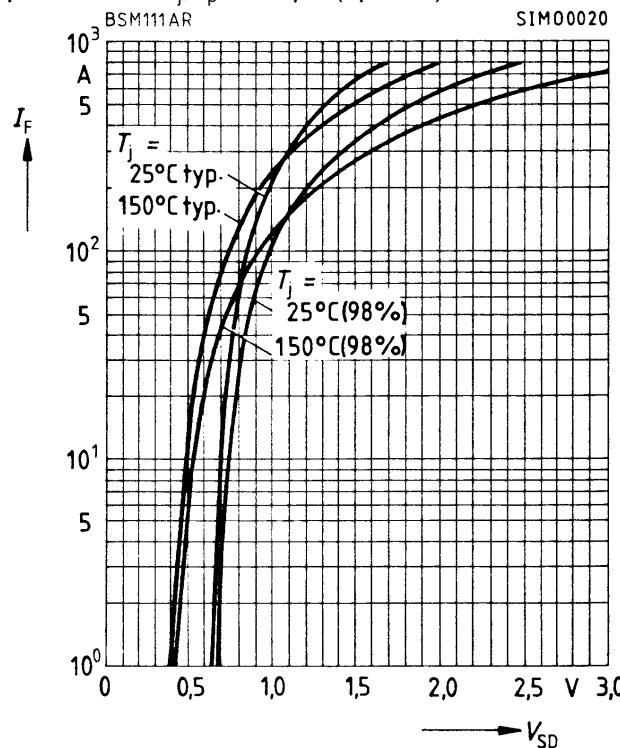
parameter: $V_{GS} = 0$, $f = 1$ MHz



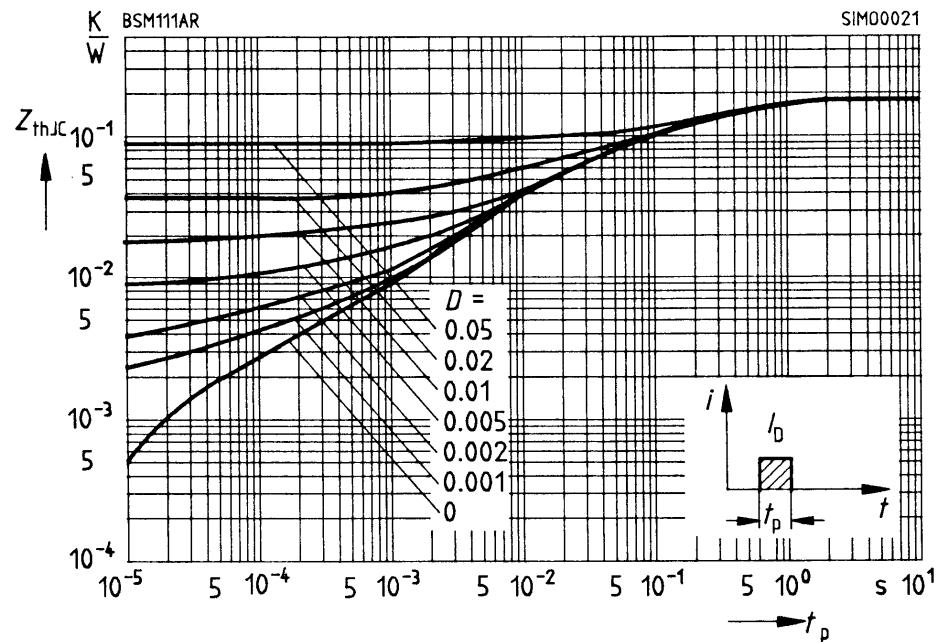
Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

parameter: $T_j, t_p = 80 \mu\text{s}$ (spread)



Transient thermal impedance $Z_{\text{thJC}} = f(t_p)$
 parameter: $D = t_p/T$



Typ. gate charge $V_{\text{GS}} = f(Q_{\text{Gate}})$
 parameter: $I_{\text{Dpuls}} = 300 \text{ A}$

