Silicon N Channel MOS FET High Speed Power Switching

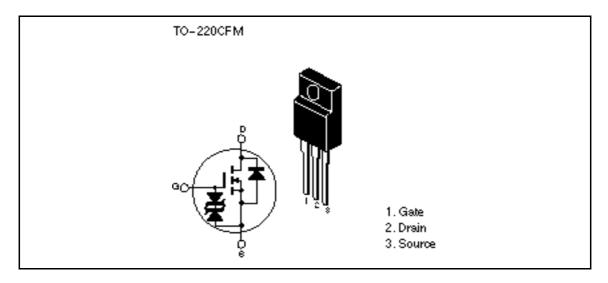
# HITACHI

ADE-208-556 Target Specification 1st. Edition

#### Features

- Low on-resistance
  - $R_{DS} = 0.040$  typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

### Outline





## **Absolute Maximum Ratings** (Ta = $25^{\circ}$ C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	60	V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	15	A	
Drain peak current	↓ *1 D(pulse)	60	A	
Body to drain diode reverse drain current	I <sub>DR</sub>	15	A	
Avalanche current	I* <sup>3</sup>	15	A	
Avalanche energy	E <sub>AR</sub> * <sup>3</sup>	19	mJ	
Channel dissipation	Pch* <sup>2</sup>	25	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	
Natar A DW 40.00 dute scale 4.0/				

Notes: 1. PW 10 $\mu$ s, duty cycle 1 %

2. Value at Tc =  $25^{\circ}$ C

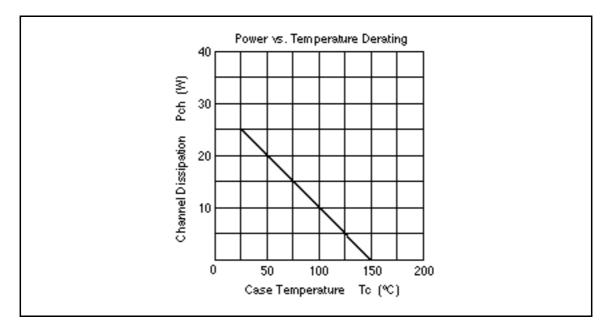
3. Value at Tch =  $25^{\circ}$ C, Rg 50

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	60	—	_	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{\rm GS}=\pm 16V,~V_{\rm DS}=0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	—	10	μA	$V_{DS} = 60 V, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	2.5	V	$I_{\rm D} = 1$ mA, $V_{\rm DS} = 10$ V
Static drain to source on state	R <sub>DS(on)</sub>	—	0.040	0.052		$I_{\rm D} = 8A, V_{\rm GS} = 10V^{*1}$
resistance	$R_{\text{DS(on)}}$	_	0.060	0.105		$I_{\rm D} = 8A, V_{\rm GS} = 4V^{*1}$
Forward transfer admittance	y <sub>fs</sub>	10	16	_	S	$I_{\rm D} = 8A, V_{\rm DS} = 10V^{*1}$
Input capacitance	Ciss	_	500	_	pF	$V_{DS} = 10V$
Output capacitance	Coss	_	260	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	110	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$I_{\rm D} = 8A, V_{\rm GS} = 10V$
Rise time	t <sub>r</sub>	_	80	_	ns	R <sub>L</sub> = 3.75
Turn-off delay time	t <sub>d(off)</sub>		100		ns	
Fall time	t <sub>f</sub>	_	110		ns	_
Body to drain diode forward voltage	$V_{\text{DF}}$	—	0.9	_	V	$I_{F} = 15A, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	50		ns	$I_F = 15A, V_{GS} = 0$ diF/ dt = 50A/µs

## **Electrical Characteristics** (Ta = $25^{\circ}$ C)

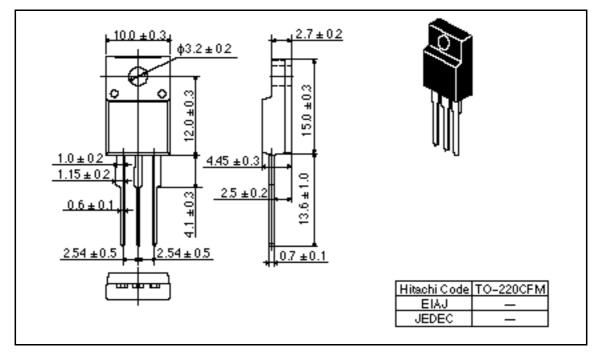
Note: 1. Pulse test

## **Main Characteristics**



## **Package Dimentions**





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