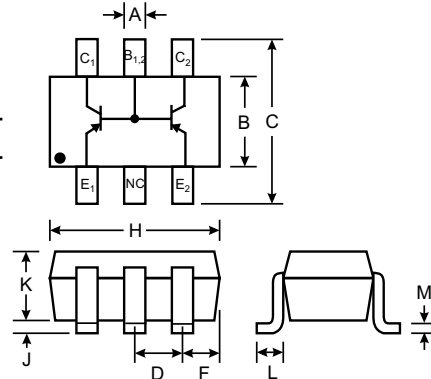


Features

- Epitaxial Planar Die Construction
- Intrinsically Matched PNP Pair (Note 1)
- Small Surface Mount Package
- 2% h_{FE} Matched Tolerance
- 1% h_{FE} Matched Tolerance On Request

Mechanical Data

- Case: SOT-26, Molded Plastic
- Case Material - UL Flammability Rating Classification 94V-0
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking: K3Q
- Weight: 0.015 grams (approx.)
- Ordering Information: See Below



SOT-26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
F	—	—	0.55
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
All Dimensions in mm			

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

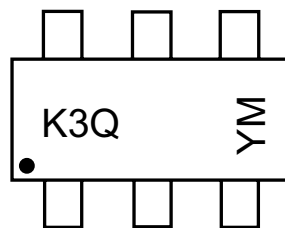
Characteristic	Symbol	DMMT3906	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-200	mA
Power Dissipation (Note 2)	P_d	225	mW
Thermal Resistance, Junction to Ambient (Note 2)	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Operating and Storage and Temperature Range	T_j, T_{STG}	-55 to +150	$^\circ\text{C}$

Ordering Information (Note 3)

Device	Packaging	Shipping
DMMT3906-7	SOT-26	3000/Tape & Reel

- Notes:
1. Built with adjacent die from a single wafer.
 2. Device mounted on FR5 PCB: 1.0 x 0.75 x 0.62 in.; pad layout as shown on suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 3. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



K3Q = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: N = 2002
 M = Month ex: 9 = September

Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004
Code	J	K	L	M	N	O	P

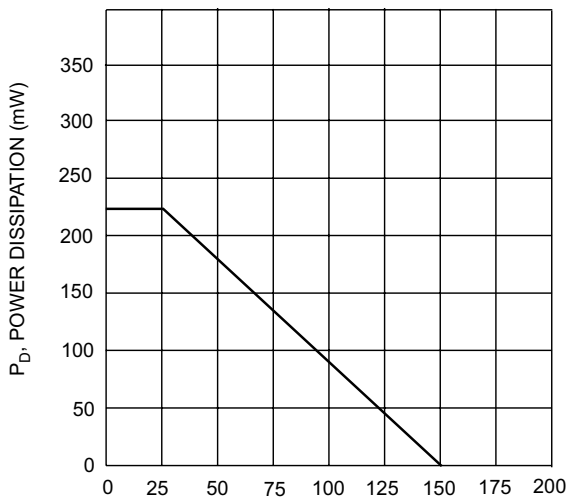
Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

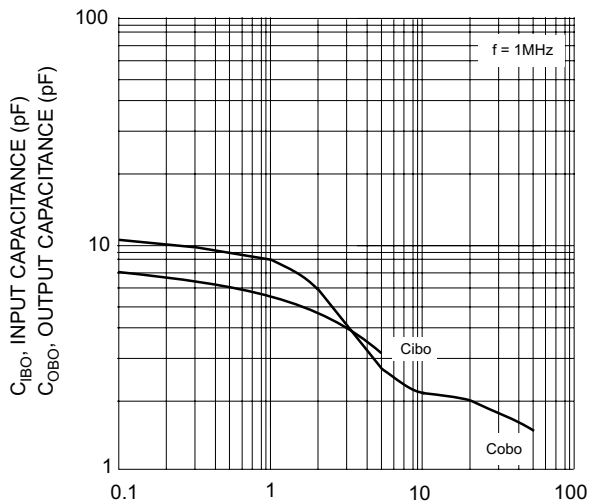
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)					
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	-40	—	V	$I_C = -10\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	-40	—	V	$I_C = -1.0\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-5.0	—	V	$I_E = -10\mu\text{A}, I_C = 0$
Collector Cutoff Current	I_{CEX}	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3.0\text{V}$
Base Cutoff Current	I_{BL}	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3.0\text{V}$
ON CHARACTERISTICS (Note 4)					
DC Current Gain (Note 5)	h_{FE}	60 80 100 60 30	— — 300 — —	—	$I_C = -100\mu\text{A}, V_{CE} = -1.0\text{V}$ $I_C = -1.0\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -10\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -50\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -100\text{mA}, V_{CE} = -1.0\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	—	-0.25 -0.40	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
Base- Emitter Saturation Voltage	$V_{BE(SAT)}$	-0.65 —	-0.85 -0.95	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	—	4.5	pF	$V_{CB} = -5.0\text{V}, f = 1.0\text{MHz}, I_E = 0$
Input Capacitance	C_{ibo}	—	10	pF	$V_{EB} = -0.5\text{V}, f = 1.0\text{MHz}, I_C = 0$
Input Impedance	h_{ie}	2.0	12	k Ω	$V_{CE} = 10\text{V}, I_C = 1.0\text{mA},$ $f = 1.0\text{kHz}$
Voltage Feedback Ratio	h_{re}	0.1	10	$\times 10^{-4}$	
Small Signal Current Gain	h_{fe}	100	400	—	
Output Admittance	h_{oe}	3.0	60	μS	
Current Gain-Bandwidth Product	f_T	250	—	MHz	$V_{CE} = -20\text{V}, I_C = -10\text{mA},$ $f = 100\text{MHz}$
Noise Figure	NF	—	4.0	dB	$V_{CE} = -5.0\text{V}, I_C = -100\mu\text{A},$ $R_S = 1.0\text{k}\Omega, f = 1.0\text{kHz}$
SWITCHING CHARACTERISTICS					
Delay Time	t_d	—	35	ns	$V_{CC} = -3.0\text{V}, I_C = -10\text{mA},$ $V_{BE(off)} = 0.5\text{V}, I_{B1} = -1.0\text{mA}$
Rise Time	t_r	—	35	ns	
Storage Time	t_s	—	225	ns	$V_{CC} = -3.0\text{V}, I_C = -10\text{mA},$ $I_{B1} = I_{B2} = -1.0\text{mA}$
Fall Time	t_f	—	75	ns	

Notes: 4. Short duration test pulse used to minimize self-heating effect.

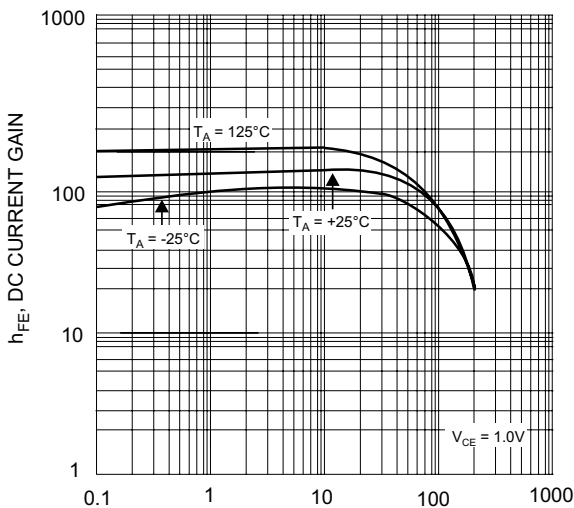
5. The DC current gain, h_{FE} , is matched at $I_C = -10\text{mA}$ and $V_{CE} = -1.0\text{V}$ with typical matched tolerances of 1% and maximum of 2%.



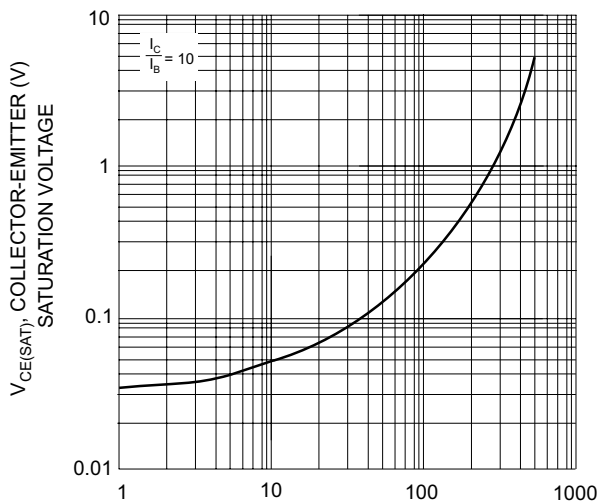
T_A , AMBIENT TEMPERATURE ($^{\circ}C$)
Fig. 1, Max Power Dissipation vs Ambient Temperature



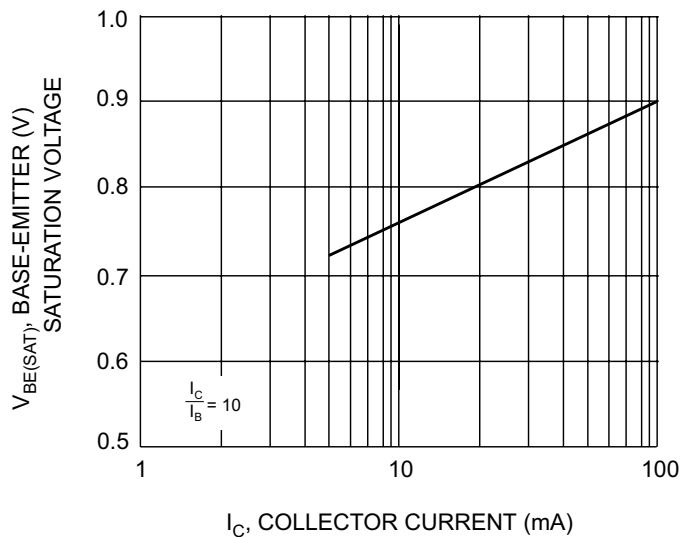
V_{CB} , COLLECTOR-BASE VOLTAGE (V)
Fig. 2, Input and Output Capacitance vs. Collector-Base Voltage



I_C , COLLECTOR CURRENT (mA)
Fig. 3, Typical DC Current Gain vs Collector Current



I_C , COLLECTOR CURRENT (mA)
Fig. 4, Typical Collector-Emitter Saturation Voltage vs. Collector Current



I_C , COLLECTOR CURRENT (mA)
Fig. 5, Typical Base-Emitter Saturation Voltage vs. Collector Current