Triacs

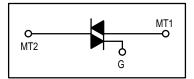
Silicon Bidirectional Triode Thyristors

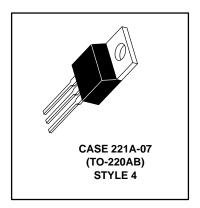
. . . designed primarily for full-wave ac control applications, such as light dimmers, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Four Modes (2N6348A, 2N6349A)
- · For 400 Hz Operation, Consult Factory
- 8 Ampere Devices Available as 2N6344 thru 2N6349

2N6348A 2N6349A

TRIACs
12 AMPERES RMS
600 and 800 VOLTS





MAXIMUM RATINGS (T_J = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit	
*Peak Repetitive Off-State Voltage ⁽¹⁾ (Gate Open, T _J = -40 to +110°C 1/2 Sine Wave 50 to 60 Hz, Gate Open	VDRM		Volts	
2N6348A 2N6349A		600 800		
*RMS On-State Current $(T_C = +80^{\circ}C)$ (Full Cycle Sine Wave 50 to 60 Hz) $(T_C = +95^{\circ}C)$	I _{T(RMS)}	12 6	Amps	
*Peak Non-repetitive Surge Current (One Full Cycle, 60 Hz, T _C = +80°C) Preceded and Followed by Rated Current	ITSM	120	Amps	
Circuit Fusing (t = 8.3 ms)	I ² t	59	A ² s	
*Peak Gate Power ($T_C = +80$ °C, Pulse Width = 2 μ s)	P _{GM}	20	Watts	
*Average Gate Power (T _C = +80°C, t = 8.3 ms)	P _{G(AV)}	0.5	Watt	
*Peak Gate Current	IGM	2	Amps	
*Peak Gate Voltage	V _{GM}	±10	Volts	
*Operating Junction Temperature Range	TJ	-40 to +125	°C	
*Storage Temperature Range	T _{stg}	-40 to +150	°C	

^{*}Indicates JEDEC Registered Data.

^{1.} V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



2N6348A 2N6349A

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
*Thermal Resistance, Junction to Case	$R_{ heta JC}$	2	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$, unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
*Peak Blocking Current $(V_D = Rated \ V_{DRM}, \ gate \ open)$ $T_J = 25^{\circ}C$ $T_J = 110^{\circ}C$	IDRM	_	_	10 2	μA mA
*Peak On-State Voltage (Either Direction) (I _{TM} = 17 A Peak; Pulse Width = 1 to 2 ms, Duty Cycle ≤ 2%)	V _{TM}		1.3	1.75	Volts
Gate Trigger Current (Continuous dc) $ (V_D = 12 \text{ Vdc}, R_L = 100 \text{ Ohms}) $ $ MT2(+), G(+) $ $ MT2(+), G(-) $ $ MT2(-), G(-) $ $ MT2(-), G(+) $ $ ^*MT2(+), G(+); MT2(-), G(-) T_C = -40^{\circ}C $ $ ^*MT2(+), G(-); MT2(-), G(+) T_C = -40^{\circ}C $	^I GT		6 6 10 25 —	50 75 50 75 100 125	mA
Gate Trigger Voltage (Continuous dc) $ (V_D = 12 \text{ Vdc}, R_L = 100 \text{ ohms}) $ $ MT2(+), G(+) $ $ MT2(+), G(-) $ $ MT2(-), G(-) $ $ MT2(-), G(+) $ $ ^*MT2(+), G(+); MT2(-), G(-) T_C = -40^{\circ}C $ $ ^*MT2(+), G(-); MT2(-), G(+) T_C = -40^{\circ}C $ $ (V_D = \text{Rated V}_{DRM}, R_L = 10 \text{ k ohms}, T_J = 100^{\circ}C) $ $ ^*MT2(+), G(+); MT2(-), G(-) $ $ ^*MT2(+), G(-); MT2(-), G(-) $	VGT		0.9 0.9 1.1 1.4 —	2 2.5 2 2.5 2.5 2.5 3	Volts
Holding Current (Either Direction)	lн	_	6 —	40 75	mA
*Turn-On Time (V_D = Rated V_{DRM} , I_{TM} = 17 A, I_{GT} = 120 mA, Rise Time = 0.1 μ s, Pulse Width = 2 μ s)	^t gt	_	1.5	2	μs
Critical Rate of Rise of Commutation Voltage (V_D = Rated V_{DRM} , I_{TM} = 17 A, Commutating di/dt = 6.1 A/ms, Gate Unenergized, T_C = 80°C)	dv/dt(c)	_	5	_	V/µs

^{*}Indicates JEDEC Registered Data.



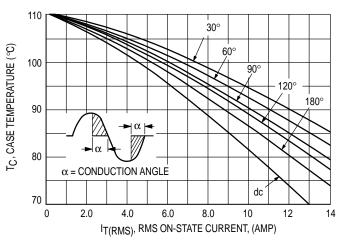
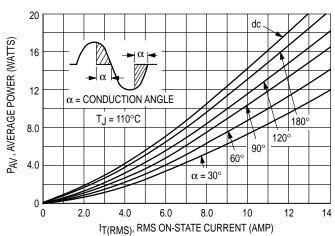


FIGURE 2 - ON-STATE POWER DISSIPATION



2N6348A 2N6349A

FIGURE 3 - TYPICAL GATE TRIGGER VOLTAGE

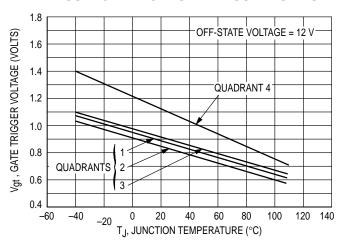


FIGURE 4 - TYPICAL GATE TRIGGER CURRENT

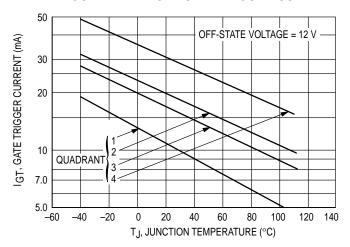


FIGURE 5 - ON-STATE CHARACTERISTICS

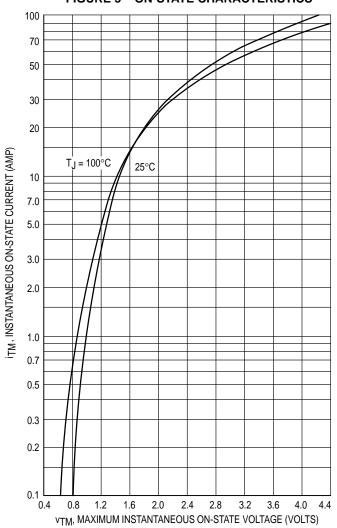


FIGURE 6 - TYPICAL HOLDING CURRENT

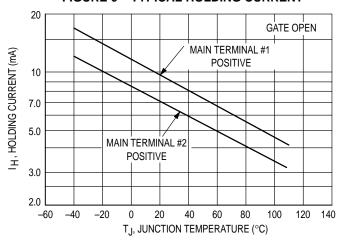


FIGURE 7 - MAXIMUM NON-REPETITIVE SURGE CURRENT

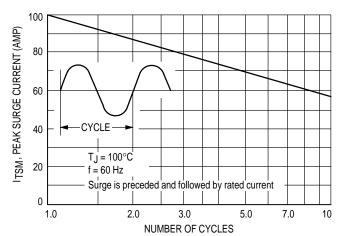
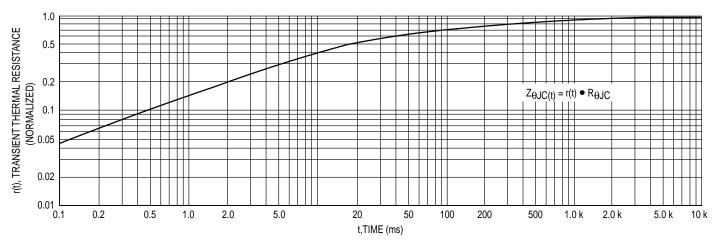
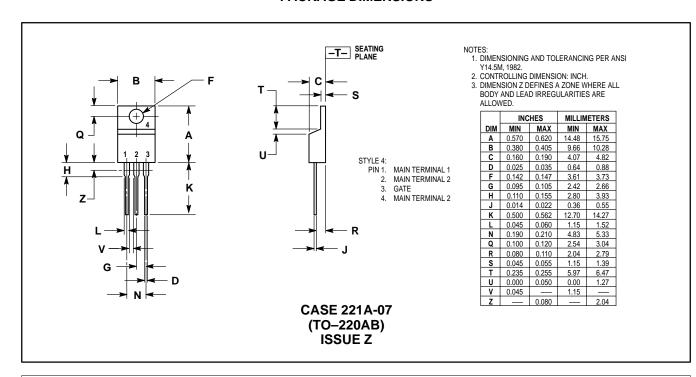


FIGURE 8 - TYPICAL THERMAL RESPONSE



PACKAGE DIMENSIONS



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♦ 2N6348A/D