

# UP TO 1A SWITCHING REGULATOR WITH ADJUSTABLE CURRENT LIMIT

- UP TO 1A OUTPUT CURRENT
- OPERATING INPUT VOLTAGE FROM 8V TO 36V
- PRECISE 3.3V (±2%) REFERENCE VOLTAGE
- 5% OUTPUT CURRENT ACCURACY
- OUTPUT VOLTAGE ADJUSTABLE FROM 1.235V TO 34V
- 250KHz INTERNALLY FIXED FREQUENCY
- VOLTAGE FEEDFORWARD
- ZERO LOAD CURRENT OPERATION
- ADJUSTABLE CURRENT LIMIT
- PROTECTION AGAINST FEEDBACK DISCONNECTION
- THERMAL SHUTDOWN

#### **APPLICATIONS**

- CHARGERS FOR NiCd, NiMH BATTERIES AND PREREGULATOR FOR LITHIUM-ION BATTERIES
- ADJUSTABLE CURRENT GENERATOR
- SIMPLE STEP-DOWN CONVERTERS WITH ADJUSTABLE CURRENT LIMIT
- BATTERY EQUIPPED SYSTEMS
- DISTRIBUTED POWER SUPPLY
- MOBILE PC & SUBNOTEBOOK

#### **DESCRIPTION**

The L6902D is a complete and simple step down switching regulator with adjustable current limit. Based on a voltage mode structure it integrates a cur-



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**ORDERING NUMBERS: L6902D** 

L6902D013TR (Tape & Reel)

rent error amplifier to have a constant voltage and constant current control.

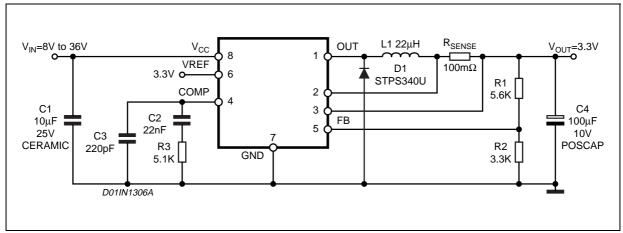
By means of an on board current sense resistor and the availability of the current sense pins (both compatible to Vcc and for Cs- compatible with GND too) a current limit programming is very simple and accurate (±5%). Moreover constant current control can be used to charge NiMH and NiCd batteries.

The device can be used as a standard DC/DC converter with adjustable current limit (set by using the external sense resistor).

The internal robust P-Channel DMOS transistor with a typical of  $250m\Omega$  assures high efficiency and a minimum dropout even at high output current level. The internal limiting current (latched function) of typical value of 2.5A protects the device from accidental output short circuit avoiding dangerous loads damage.

If the temperature of the chip goes higher than a fixed internal threshold (150°C with 20°C hysteresis), the power stage is turned off.

#### **TEST AND APPLICATION CIRCUIT**



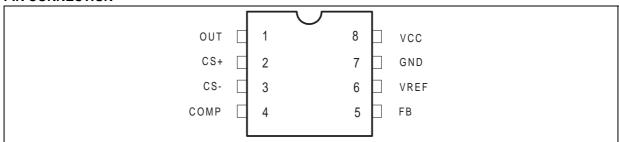
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### **DESCRIPTION** (Continued)

Other protections beside thermal shutdown complete the device for a safe and reliable application: overvoltage protection, frequency folback overcurrent protection and protection vs. feedback disconnection.

The internal fixed switching frequency of 250KHz, and the SO-8 package pin allow to built an ultra compact DC/DC converter with a minimum board space.

#### **PIN CONNECTION**



#### **PIN DESCRIPTION**

N°	Pin	Function
1	OUT	Regular Output
2	CS+	Current Error Amplifier input (current sense at higher voltage)
3	CS-	Current Error Amplifier input (current sense at lower voltage)
4	COMP	E/A output to be used for frequency compensation
5	FB	Stepdown feedback input. Connecting directly to this pin results in an output voltage of 1.235V. An external resistive divider is required for higher output voltages. In this case:
		$V_{out} = V_{FB} \cdot \left(1 + \frac{R1}{R2}\right) = 1.235 V \left(1 + \frac{R1}{R2}\right)$
6	$V_{REF}$	3.3V VREF. No cap is need for stability.
7	GND	Ground
8	VCC	Unregulated DC input voltage.

### THERMAL DATA

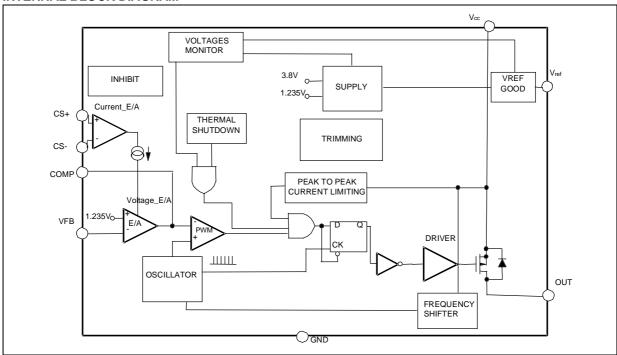
Symbol	Parameter	Value	Unit
R <sub>th j-amb</sub>	Thermal Resistance Junction to Ambient Max.	110 (*)	°C/W

<sup>(\*)</sup> Package mounted on board.

#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
V <sub>8</sub>	Input Voltage	40	V
V <sub>1</sub>	Output DC voltageOutput peak voltage at t = 0.1μs	-1 to 40 -5 to 40	V V
I <sub>1</sub>	Maximum output current	Internally limited	
V <sub>4</sub> , V <sub>5</sub>	Analog pins	4	V
V <sub>2</sub> , V <sub>3</sub>	Analog pins	-0.3V to V <sub>CC</sub>	V
P <sub>tot</sub>	Power dissipation at T <sub>amb</sub> ≤ 70 °C	0.7	W
Tj	Operating junction temperature range	-40 to 150	°C
T <sub>stg</sub>	Storage temperature range	-55 to 150	°C

### **INTERNAL BLOCK DIAGRAM**



#### **ELECTRICAL CHARACTERISTCS**

 $(T_j = 25^{\circ}C, V_{CC} = 12V, unless otherwise specified.)$  (•) Specification Referred to Tj from 0 to 125°C.

Symbol	Parameter	Test Condition		Min.	Тур.	Max.	Unit
V <sub>CC</sub>	Operating input voltage range	V <sub>O</sub> = 1.235V; I <sub>O</sub> = 1A	•	8		36	V
V <sub>d</sub>	Dropout voltage	V <sub>CC</sub> = 8V; I <sub>O</sub> = 1A	•		0.25	0.5	V
lo	Operating charging current	$R_{\text{sense}} = 0.1\Omega$		0.95	1	1.05	Α
			•	0.92		1.08	Α
lj	Maximum limiting current	V <sub>CC</sub> = 8V to 36V	•	2	2.5	3.2	Α
fs	Switching frequency		•	212	250	287	kHz
				225	250	275	kHz
d	Duty cycle			0		100	%
DYNAMIC	CHARACTERISTICS		•	•	•		
V <sub>5</sub>	Voltage feedback (FB)	8V < V <sub>CC</sub> < 36V,		1.21	1.235	1.259	V
	20mA < I <sub>O</sub> < 1A		•	1.198	1.235	1.272	V
η	Efficiency	V <sub>O</sub> = 5V, V <sub>CC</sub> = 12V			90		%
DC CHAR	RACTERISTICS		·	•	ı		
I <sub>qop</sub>	Total operating quiescent current		•		3	5	mA
Iq	Quiescent current	Duty cycle = 0; VFB = 1.5V				2.7	mA

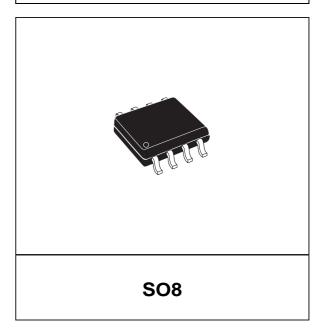
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Symbol	Parameter	rameter Test Condition		Min.	Тур.	Max.	Unit
VOLTAGE	ERROR AMPLIFIER	1		1			
V <sub>OH</sub>	High level output voltage	V <sub>FB</sub> = 1V		3.6			V
V <sub>OL</sub>	Low level output voltage	V <sub>FB</sub> = 1.5				0.4	V
I <sub>o source</sub>	Source output current	V <sub>comp</sub> = 1.9V; V <sub>FB</sub> = 1V		200	300		μΑ
I <sub>o sink</sub>	Sink output current	$V_{comp} = 1.9V; V_{FB} = 1.5V$		1	1.5		mA
I <sub>b</sub>	Source bias current				2.5	4	μΑ
	DC open loop gain	R <sub>L</sub> = 0		50	58		dB
Яm	Transconductance	I <sub>comp</sub> = -0.1 to 0.1mA V <sub>comp</sub> = 1.9V			2.3		mS
CURREN	T ERROR AMPLIFIER		ı			l	
V <sub>offs</sub>	Input offset voltage	V <sub>CS</sub> - = 1.8V; V <sub>CS</sub> + = V <sub>comp</sub>		90	100	110	mV
I <sub>CS+</sub>	CS+ Output Current	$I_O = 1A$ , $R_{sense} = 100m\Omega$ $V_{out} < V_{CC} - 2V$			1.5	3	μΑ
I <sub>CS</sub> -	CS- Output Current	$I_O = 1A$ , $R_{sense} = 100m\Omega$ $V_{out} < V_{CC} - 2V$			1.5	3	μΑ
REFERE	NCE SECTION	<b>'</b>		ı			
	Reference Voltage			3.234	3.3	3.366	V
		I <sub>REF</sub> = 0 to 5mA V <sub>CC</sub> = 8V to 36V	•	3.2	3.3	3.399	V
	Line Regulation	I <sub>REF</sub> = 0mA V <sub>CC</sub> = 8V to 36V			5	10	mV
	Load Regulation	I <sub>REF</sub> = 0 to 5mA			8	15	mV
	Short Circuit Current			10			mA

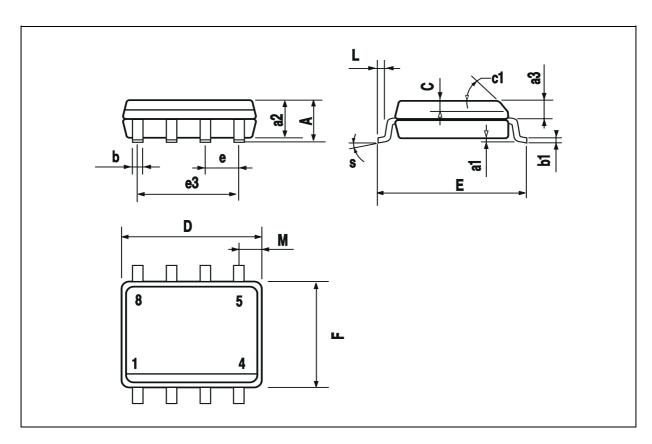
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DIM.		mm			inch	
Dilvi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α			1.75			0.069
a1	0.1		0.25	0.004		0.010
a2			1.65			0.065
аЗ	0.65		0.85	0.026		0.033
b	0.35		0.48	0.014		0.019
b1	0.19		0.25	0.007		0.010
С	0.25		0.5	0.010		0.020
c1			45° (	(typ.)		
D (1)	4.8		5.0	0.189		0.197
E	5.8		6.2	0.228		0.244
е		1.27			0.050	
еЗ		3.81			0.150	
F (1)	3.8		4.0	0.15		0.157
L	0.4		1.27	0.016		0.050
М			0.6			0.024
S	8° (max.)					

## OUTLINE AND MECHANICAL DATA



<sup>(1)</sup> D and F do not include mold flash or protrusions. Mold flash or potrusions shall not exceed 0.15mm (.006inch).



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